

TRANSISTOR DESIGN

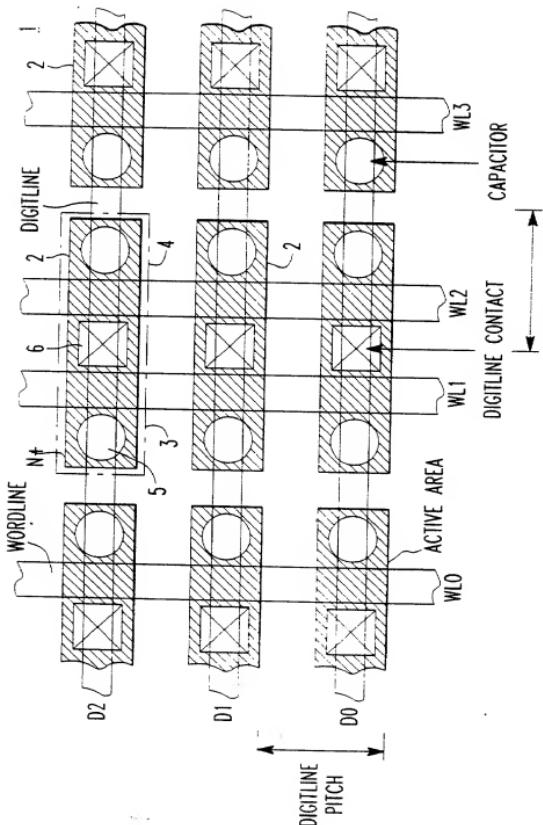
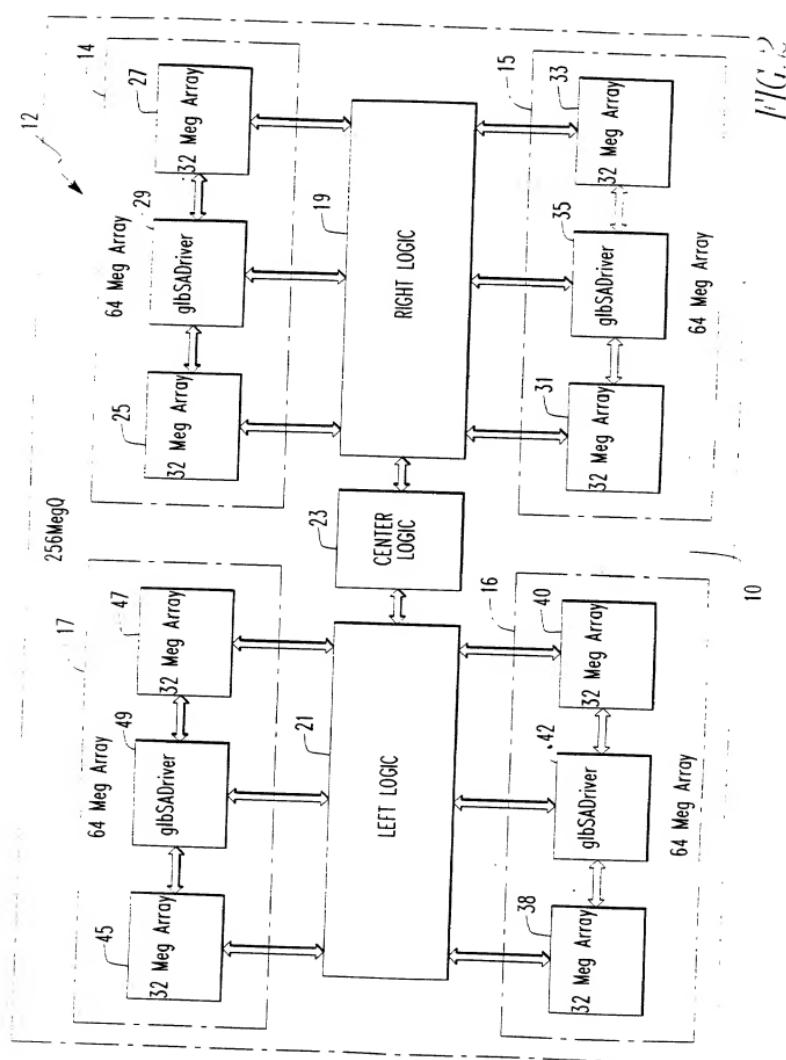


FIG. 1



H/G.2

FIG. 3A

3-367

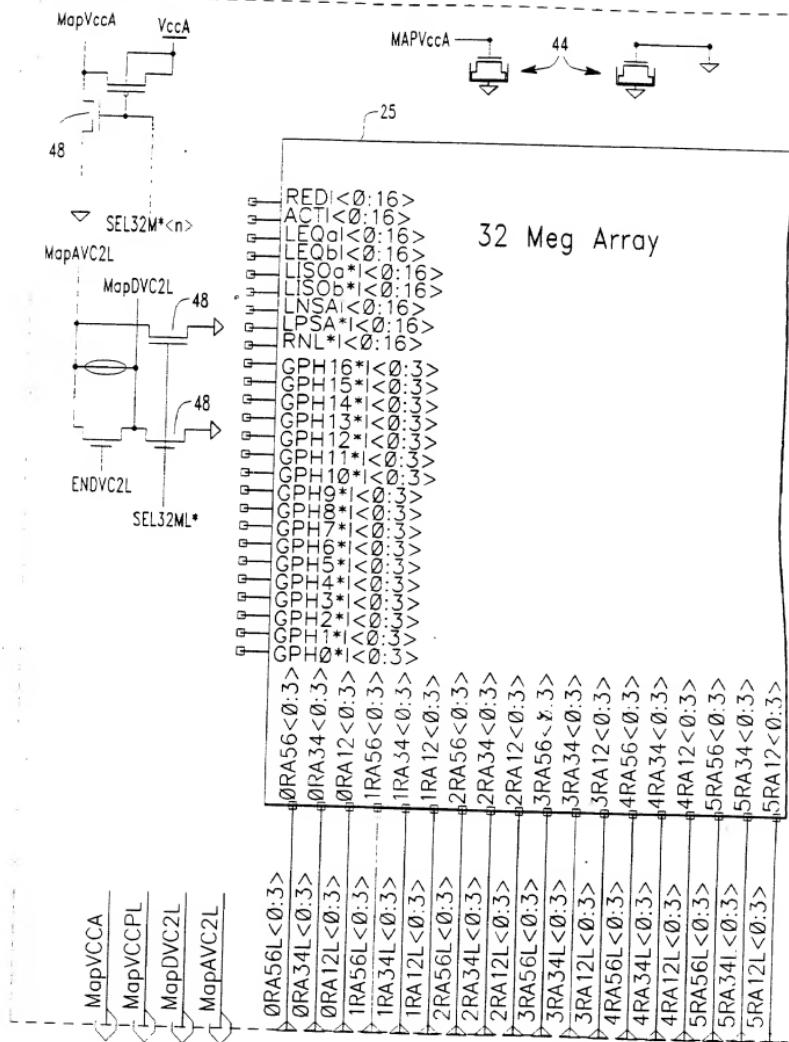


FIG. 3B

32 Meg Array

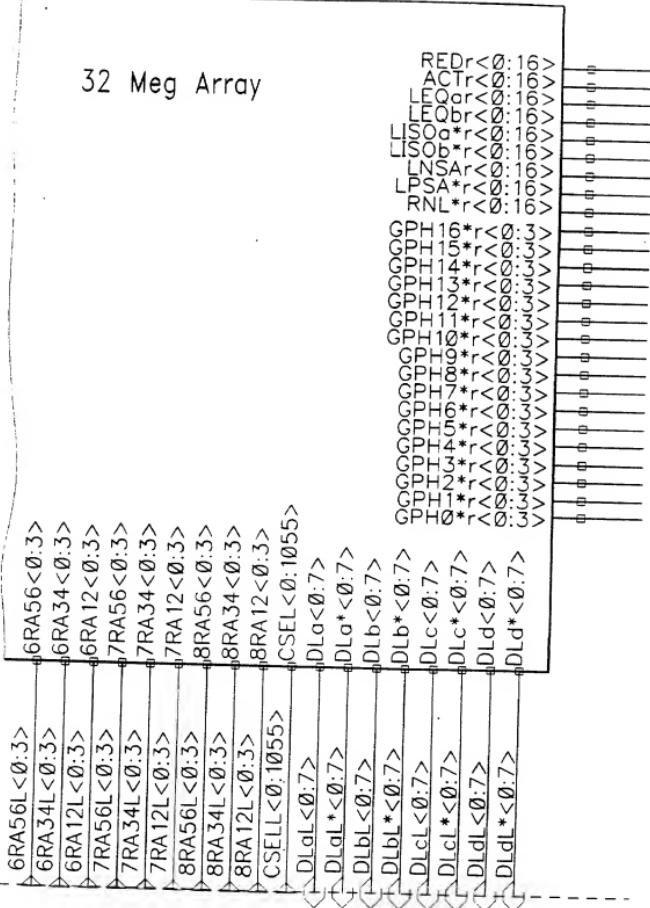


FIG. 3C

64 Meg Array

29

glbSADriver

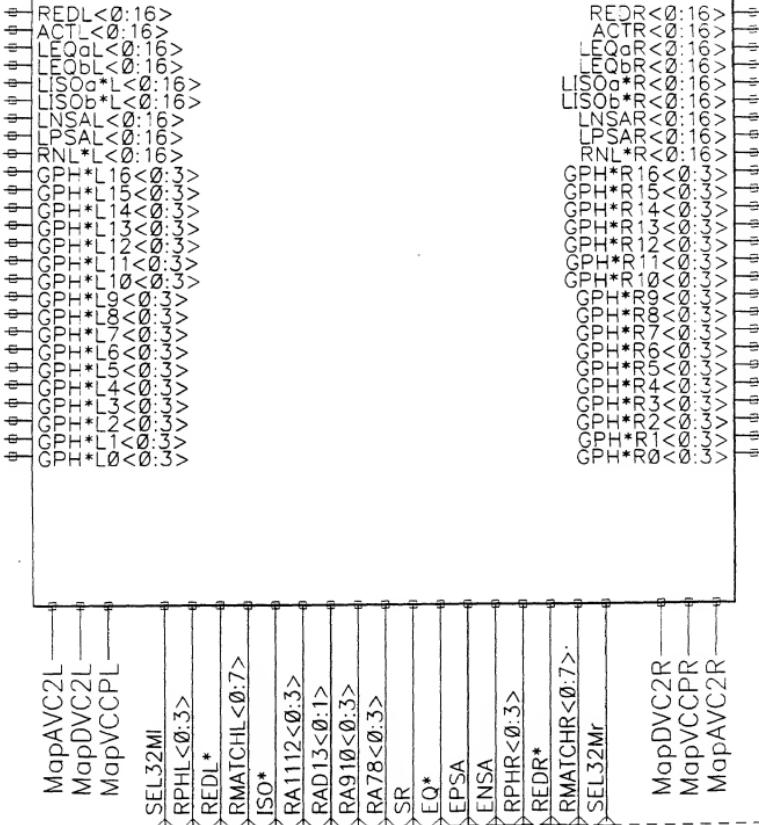


FIG. 3D

-27

32 Meg Array

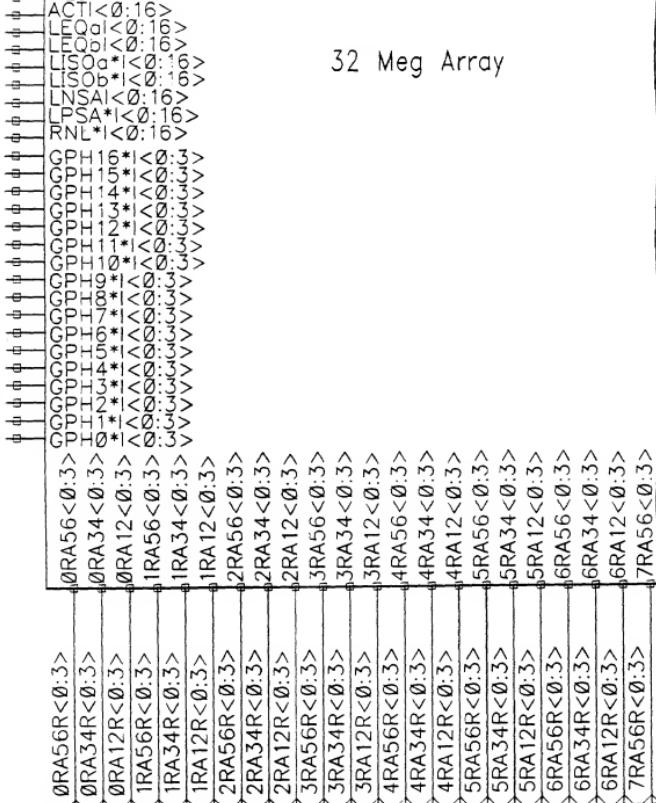
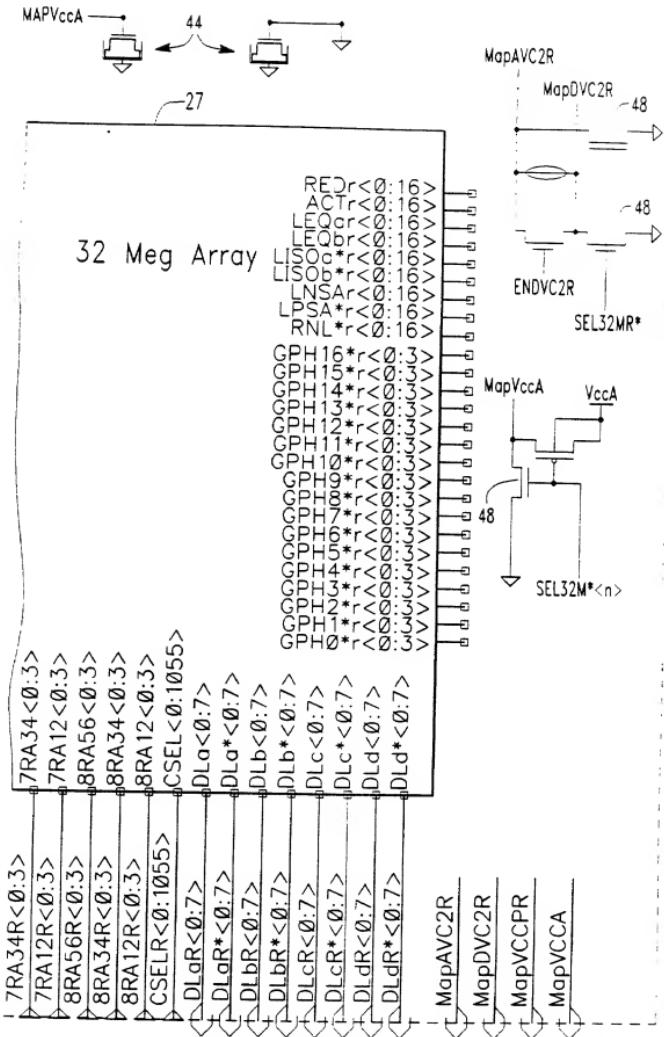


FIG. 3E

7/367



32MEG ARRAY

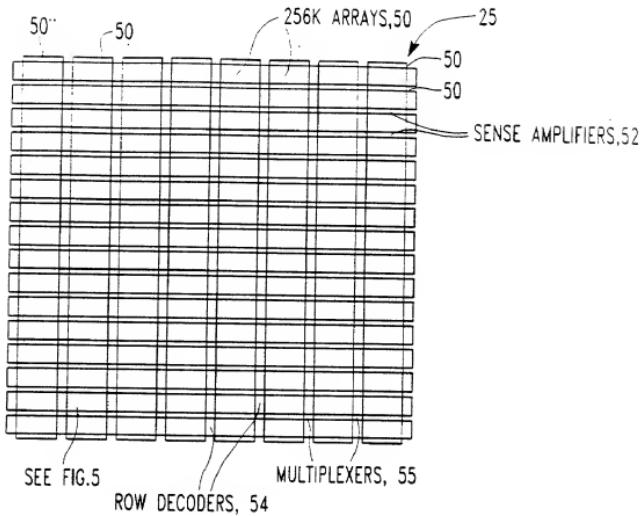
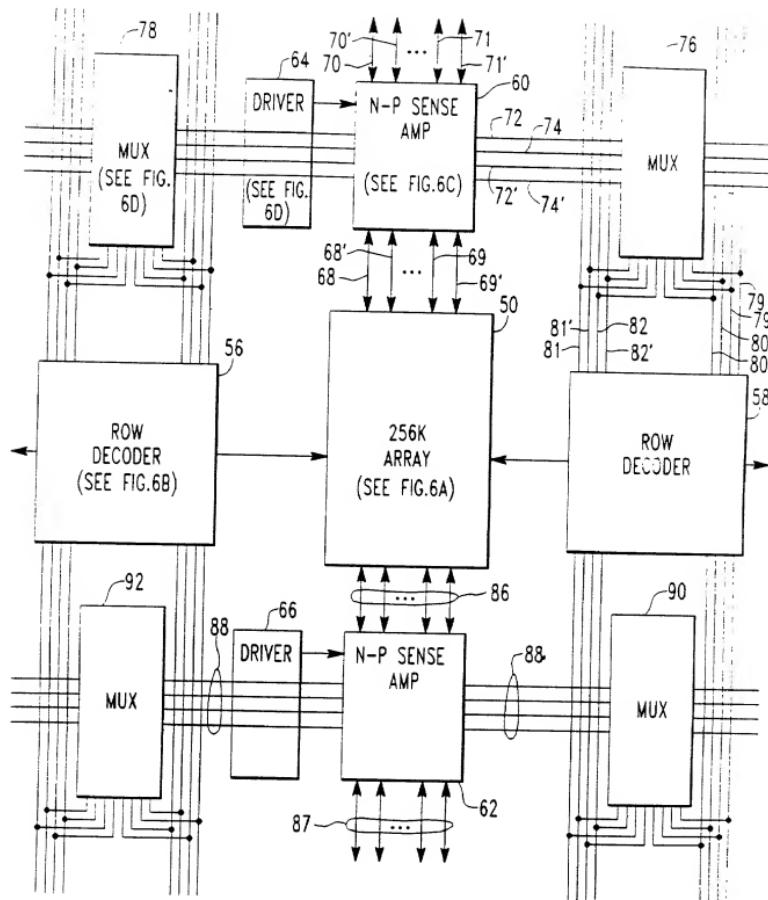


FIG. 4



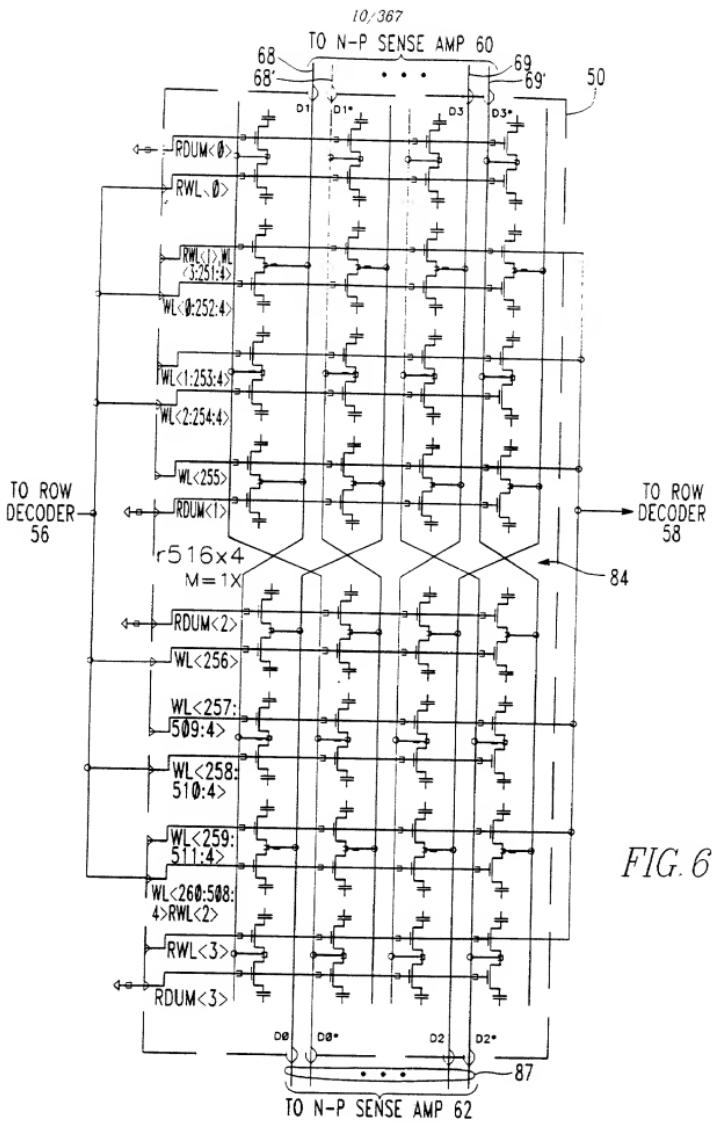


FIG. 6A

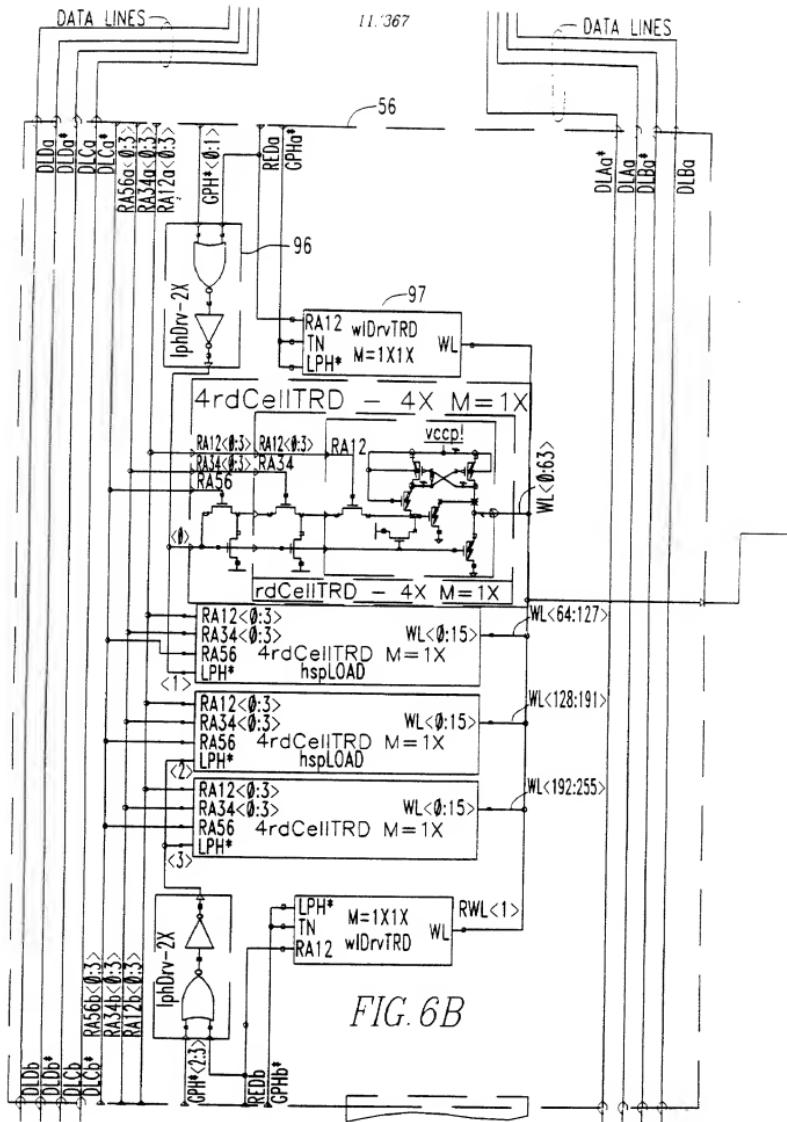
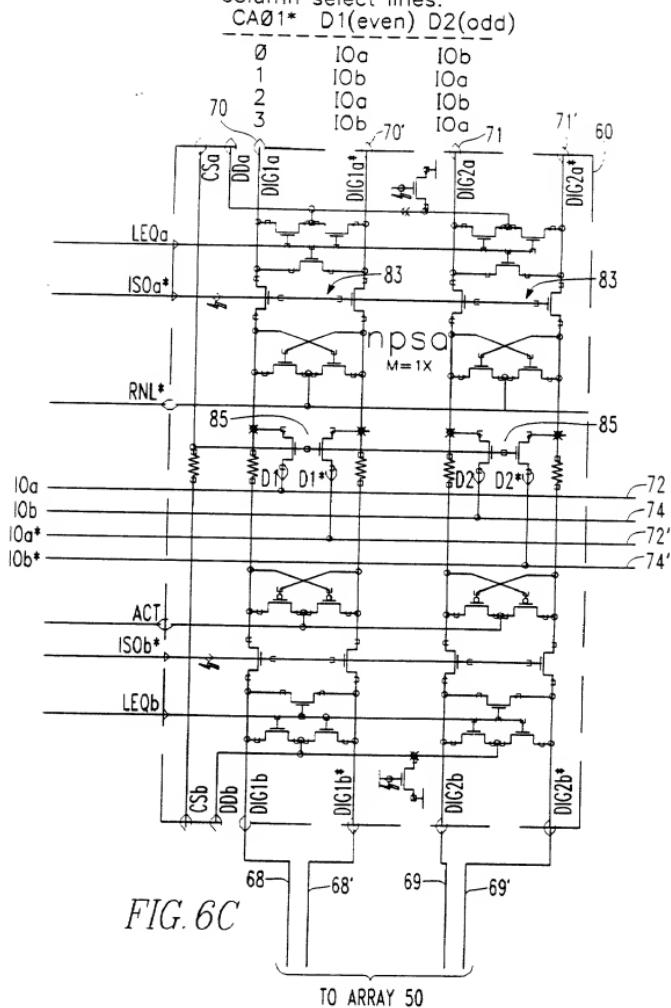
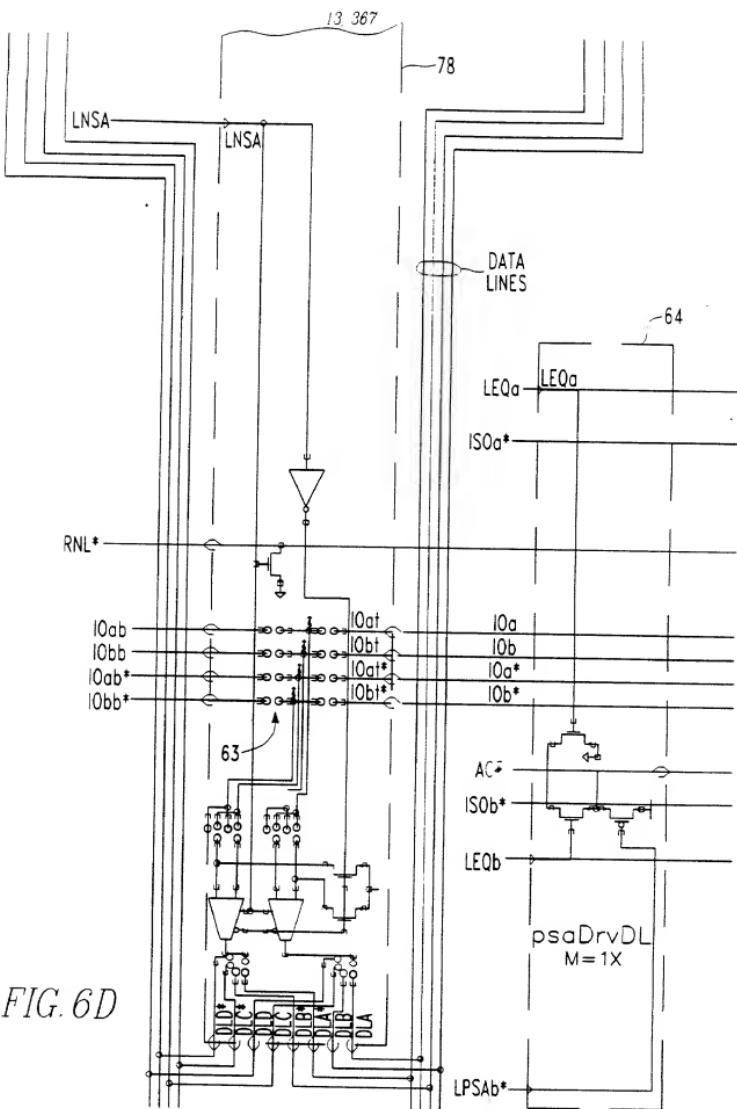


FIG. 6B

Connections of odd/even columns to IOa and IOb
alternates with odd/even
column select lines:





25

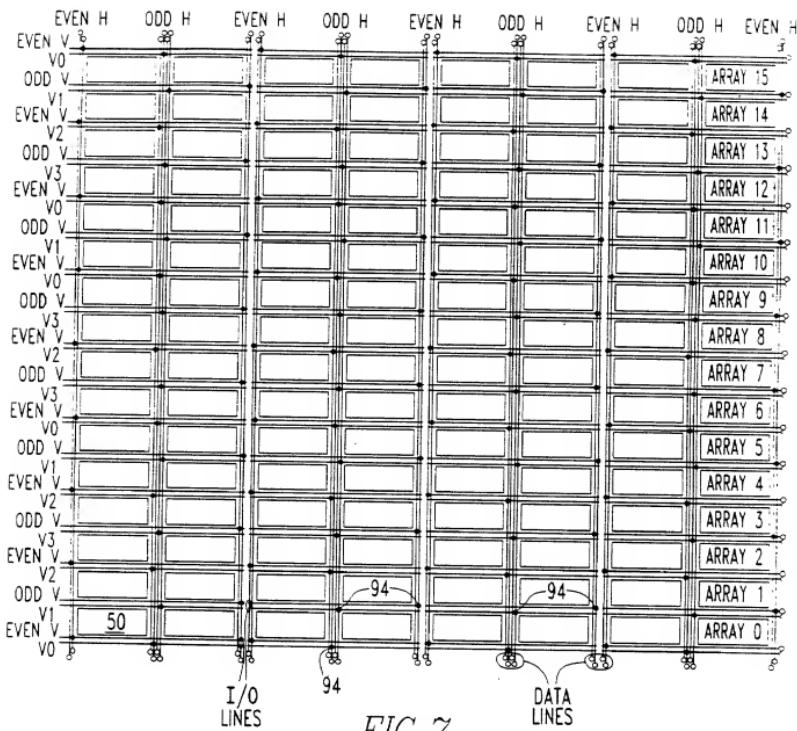
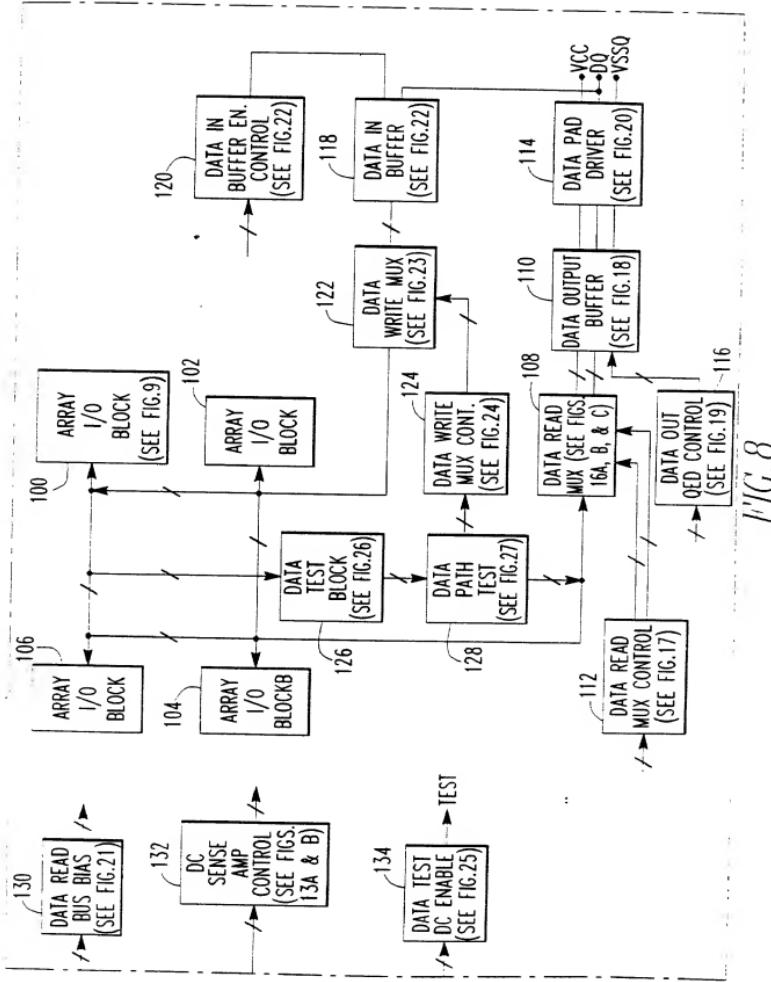


FIG. 7



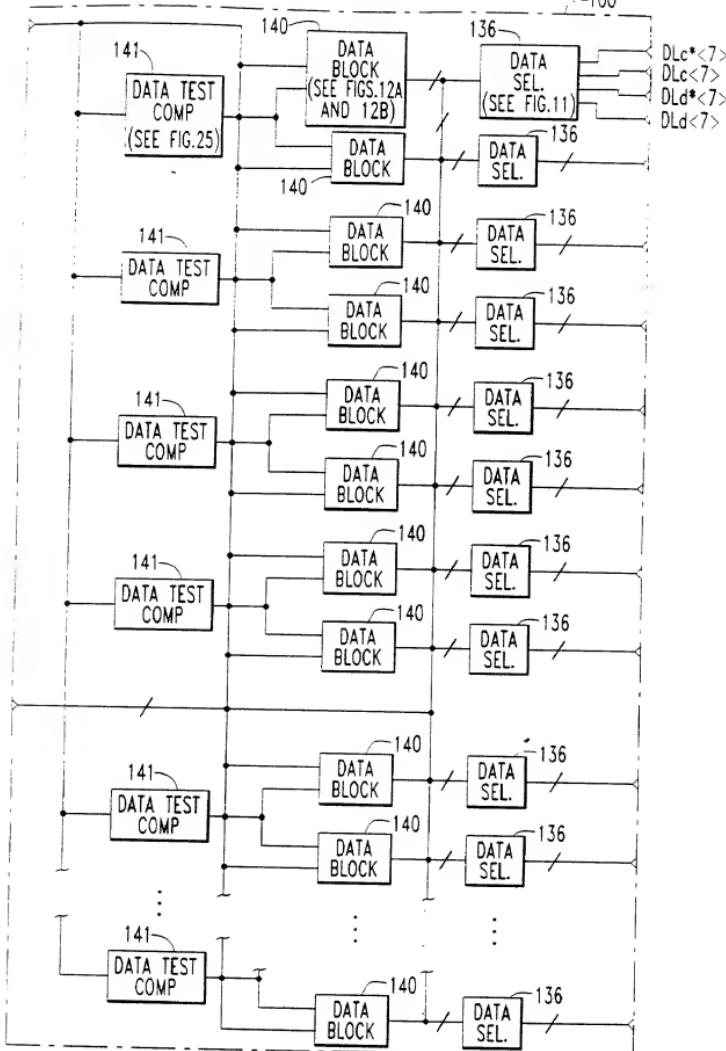


FIG. 9

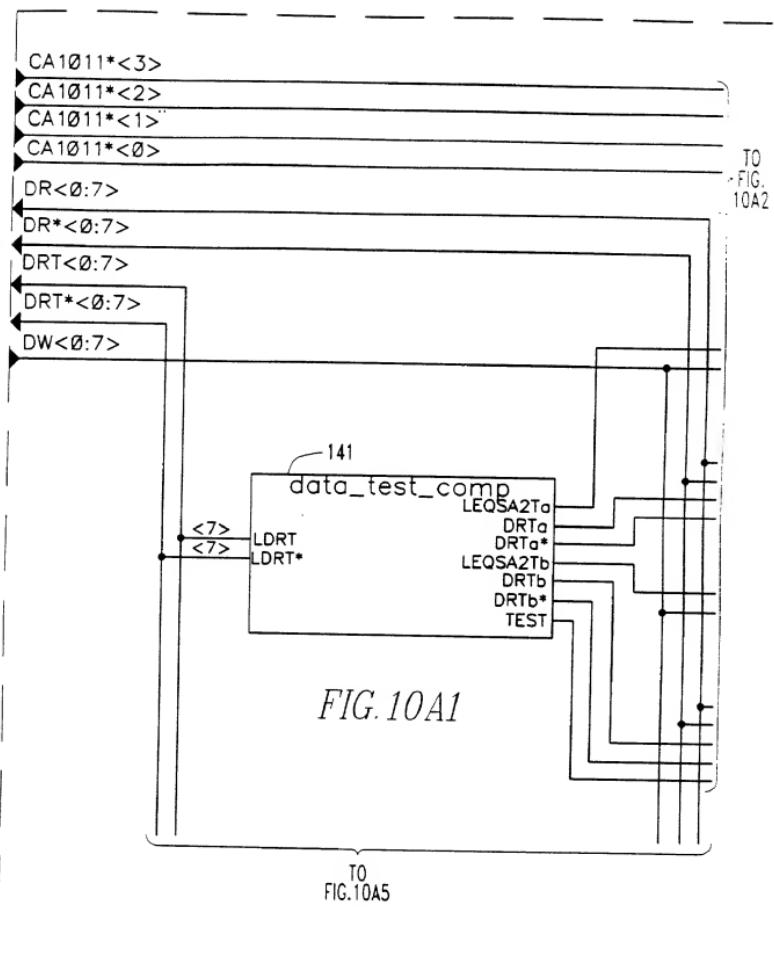


FIG. 10A2

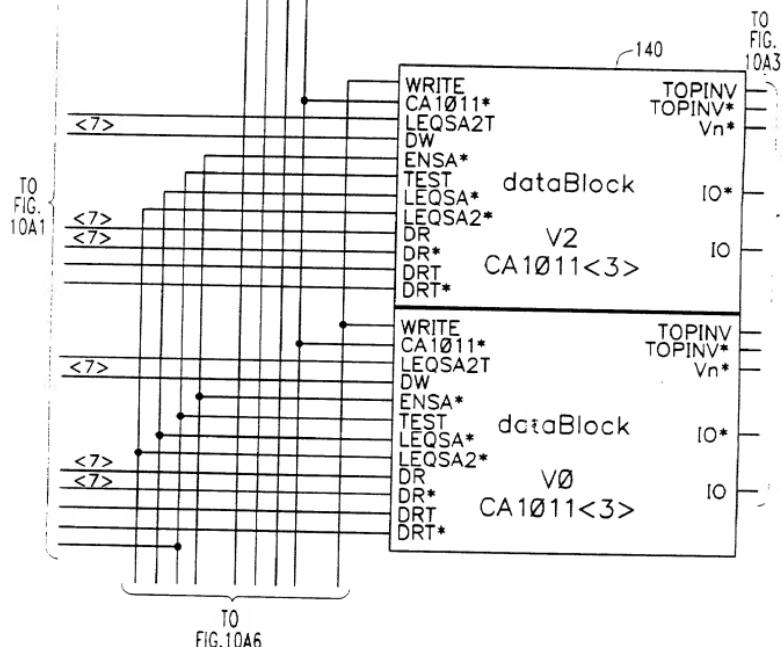
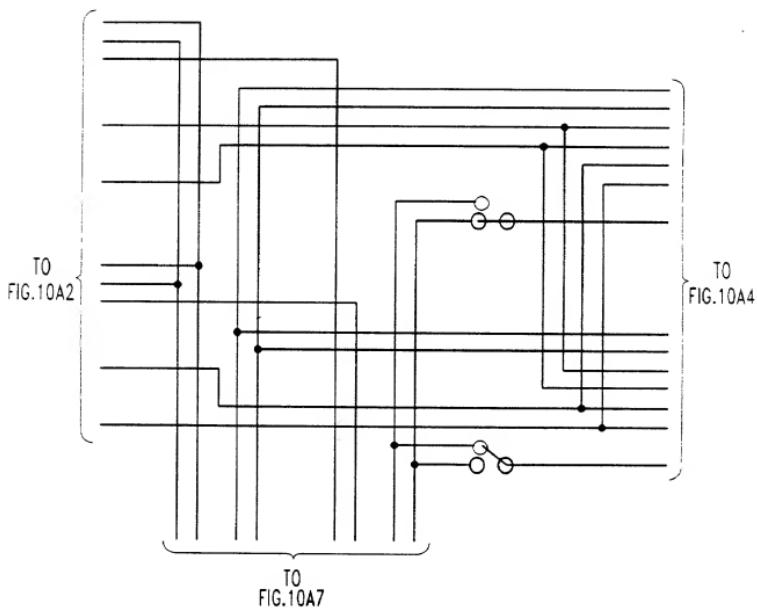


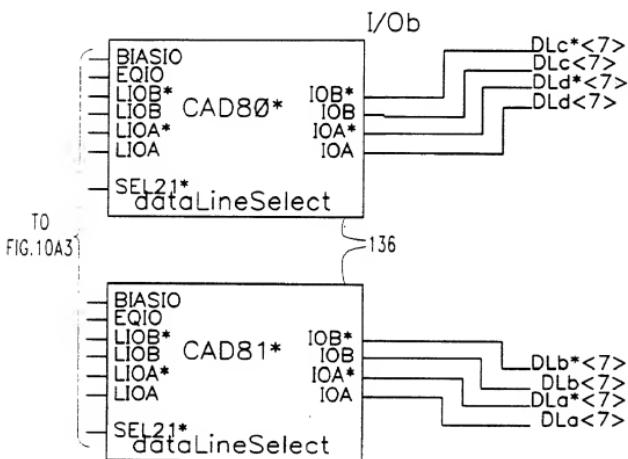
FIG. 10A3



arrayIOPBlock

100

FIG.10A4



TO
FIG.10A1

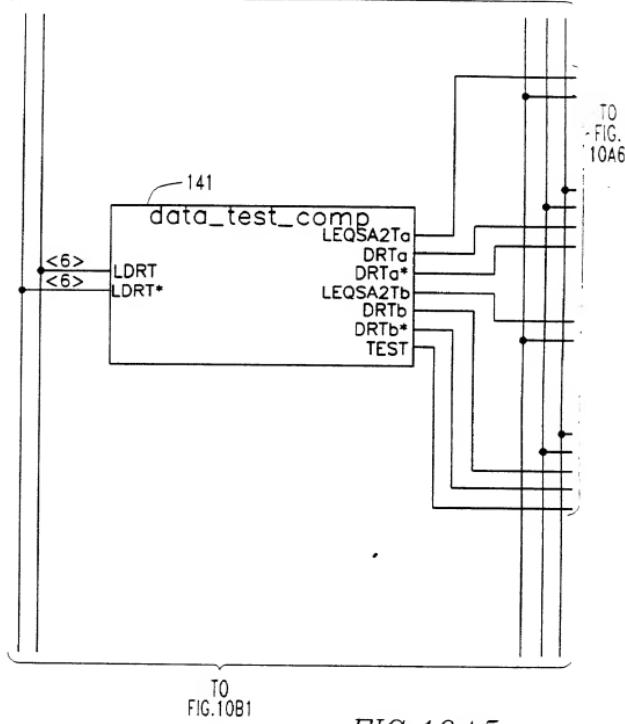


FIG.10A5

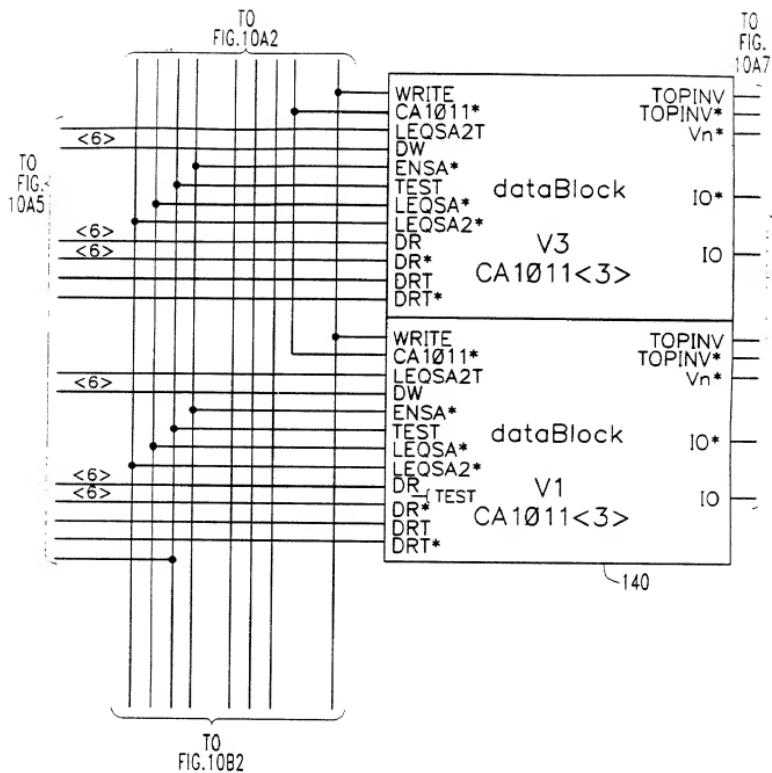


FIG. 10A6

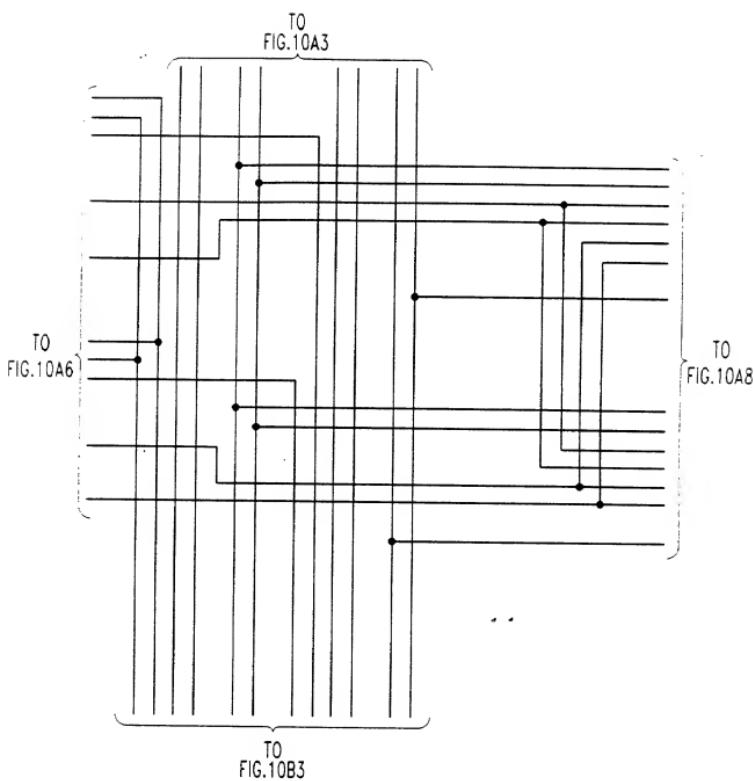


FIG.10A7

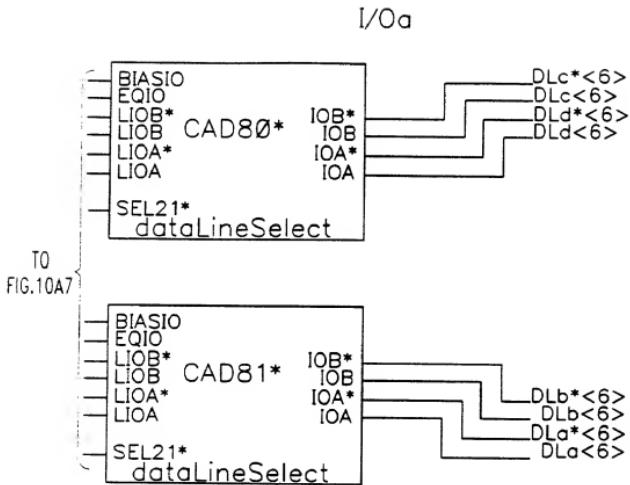


FIG.10A8

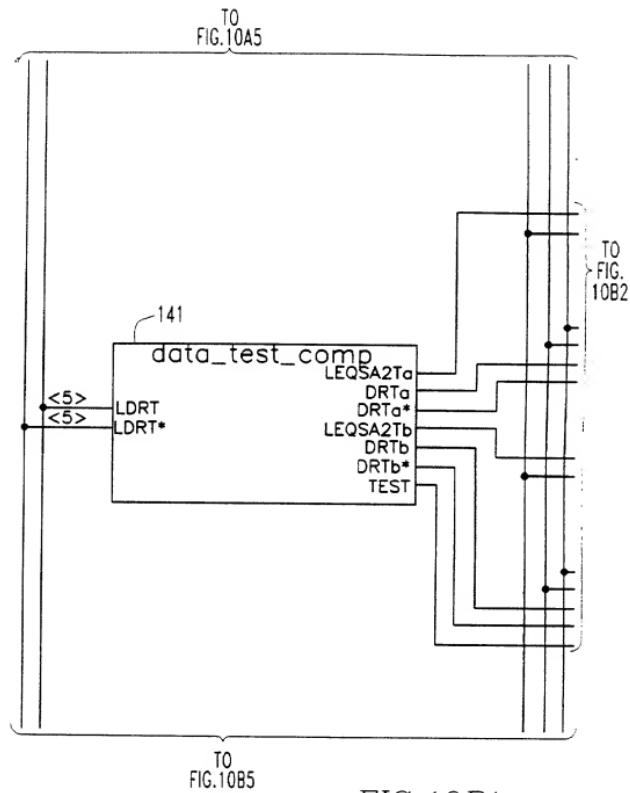


FIG.10B1

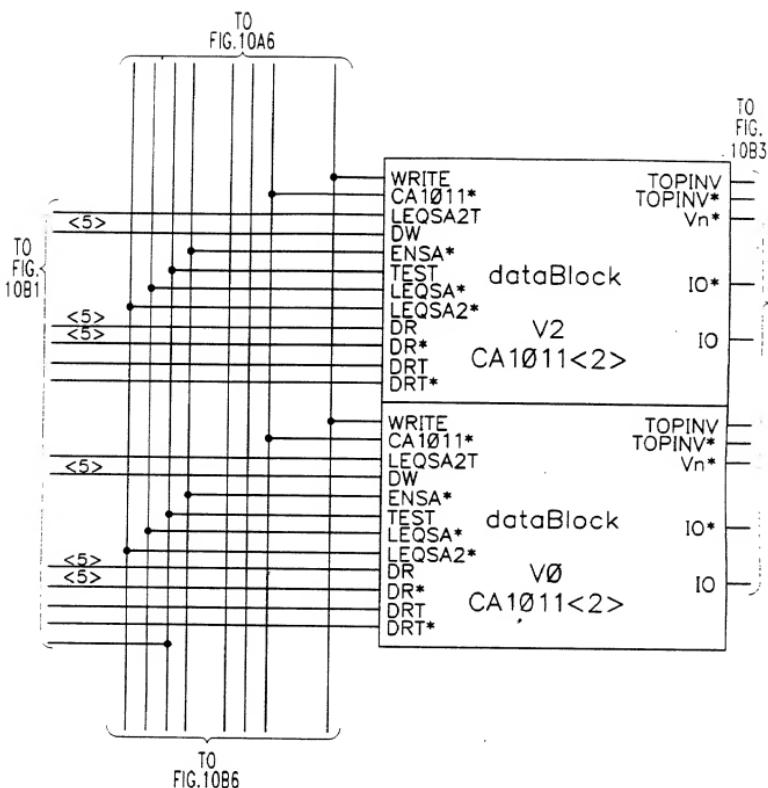


FIG.10B2

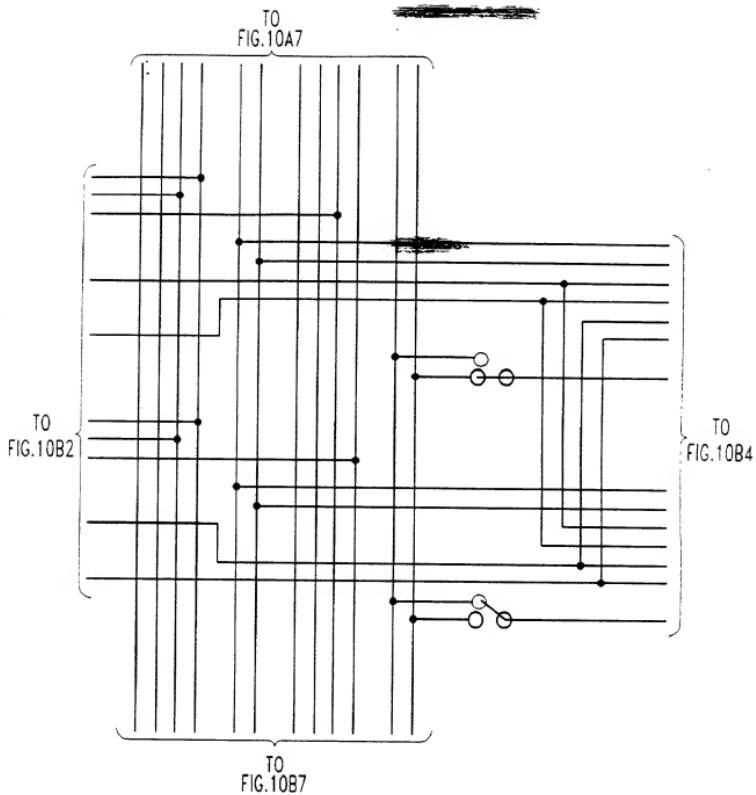


FIG. 10B3

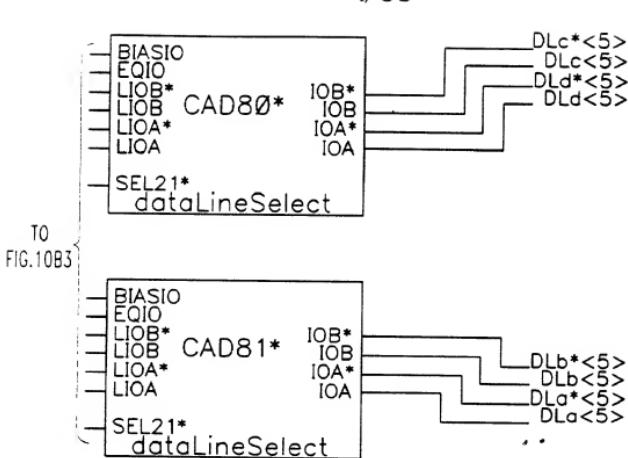


FIG. 10B4

FIG. 10B5

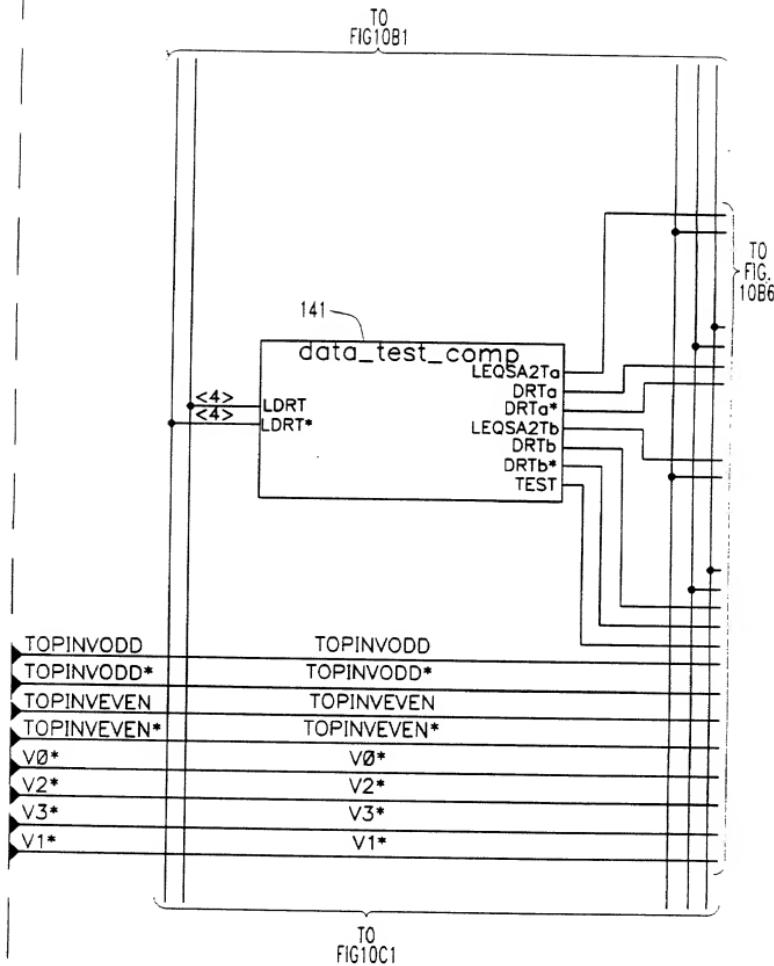


FIG. 10B6

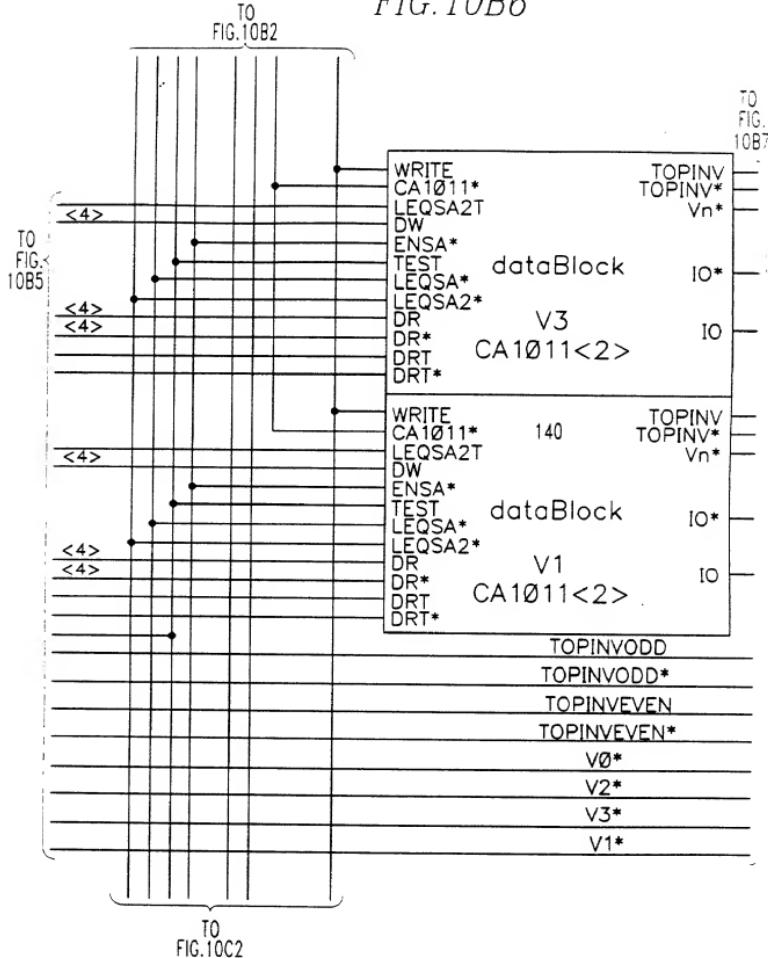


FIG. 10B7

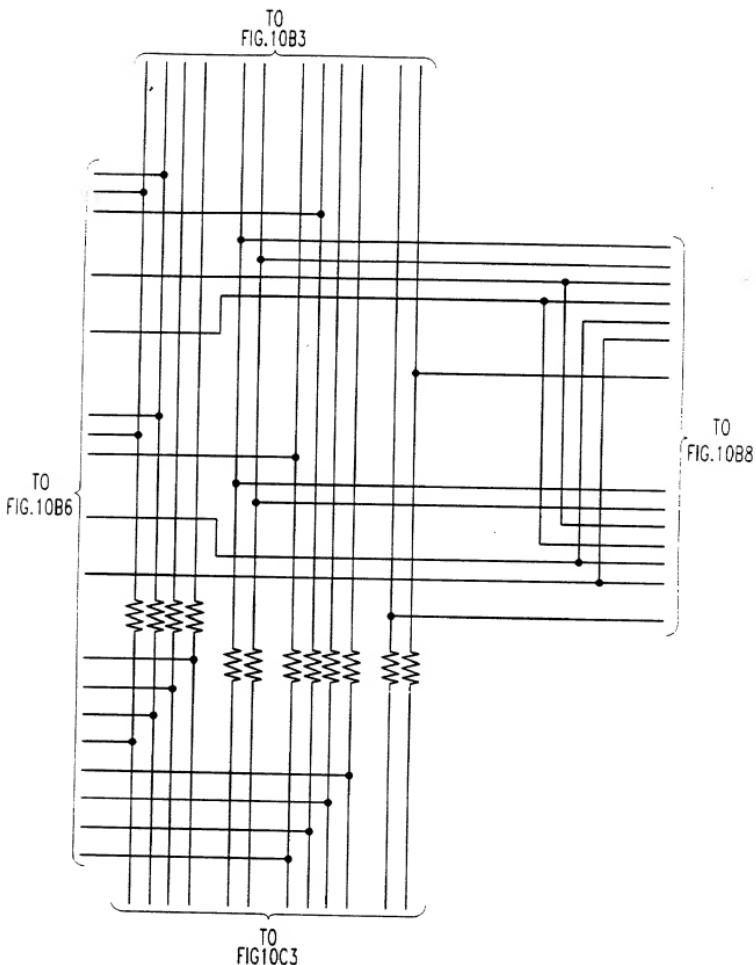
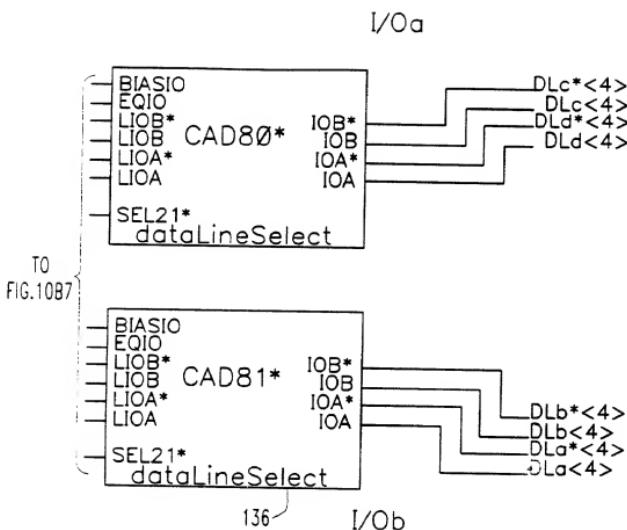


FIG. 10B8



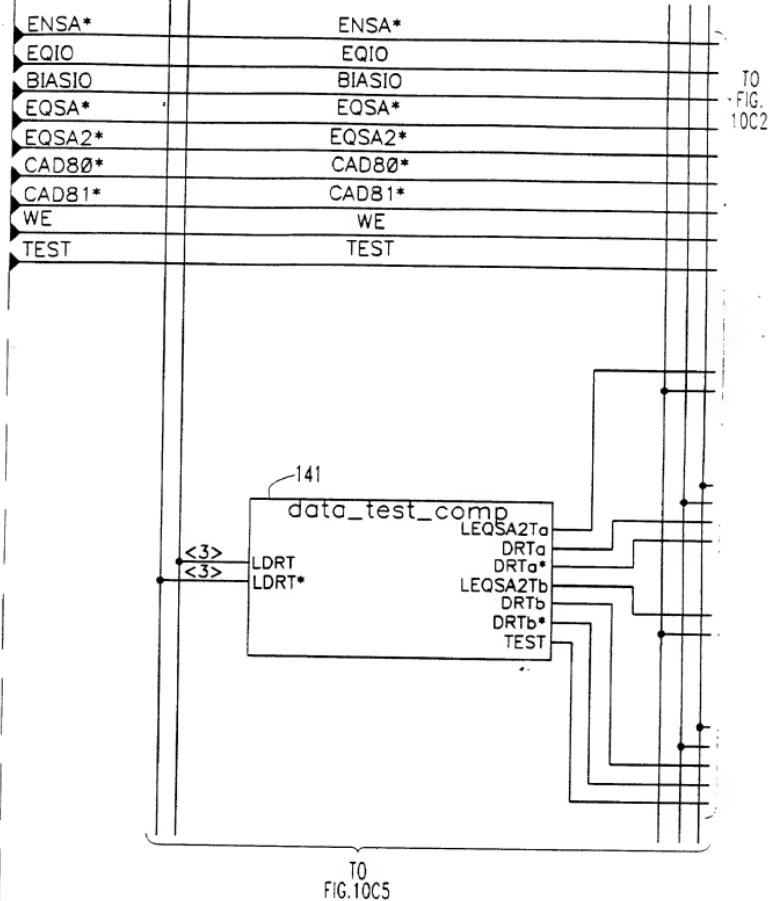
TO
FIG.10B5

FIG.10C1

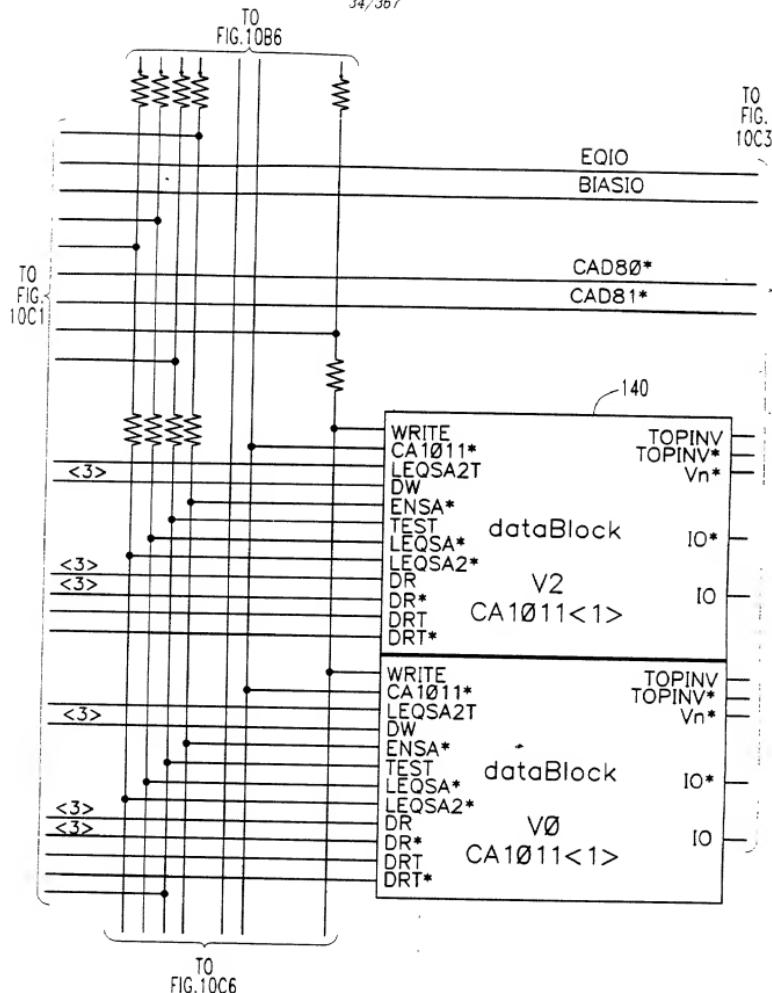


FIG.10C2

FIG.10B7

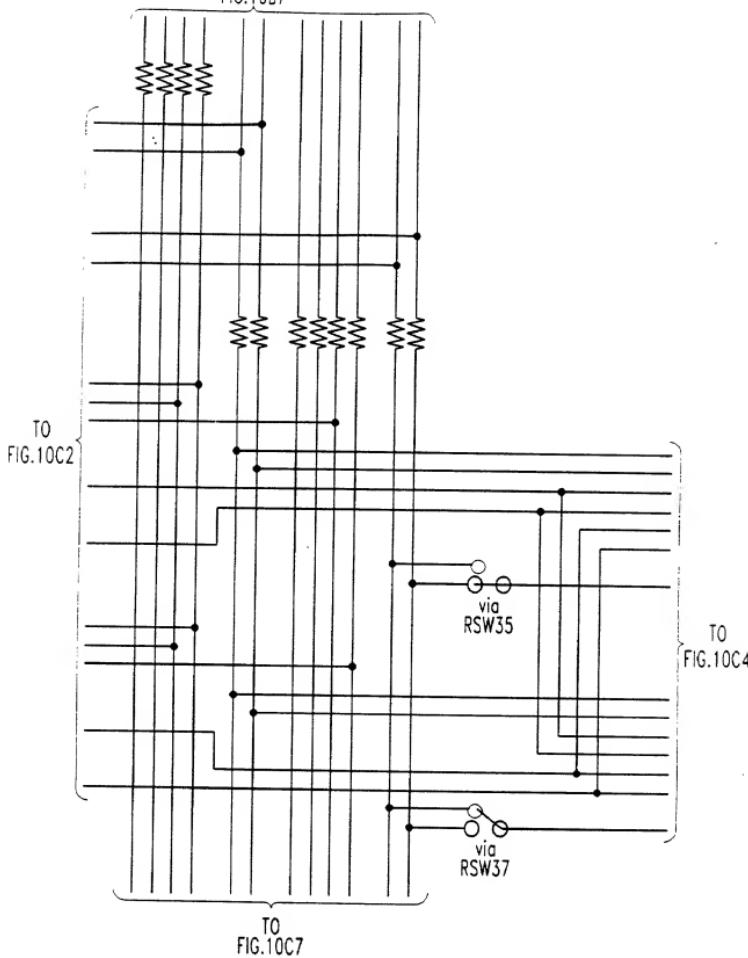
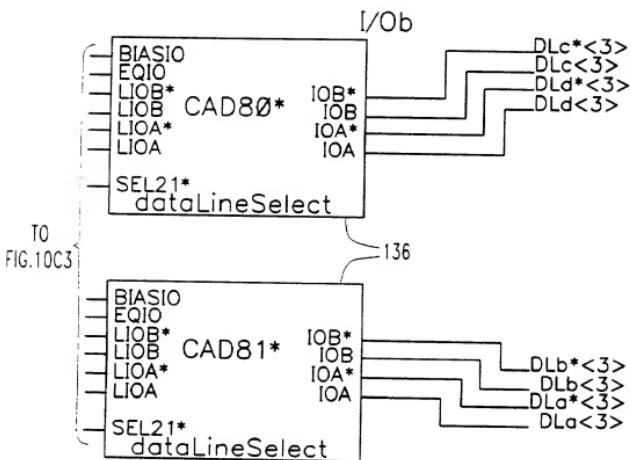


FIG.10C3

FIG. 10C4



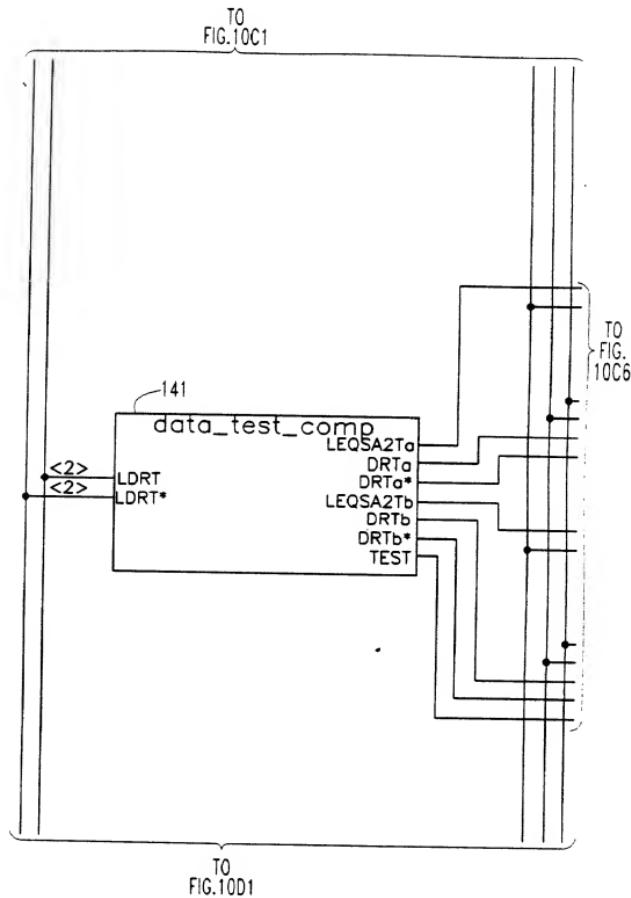


FIG.10C5

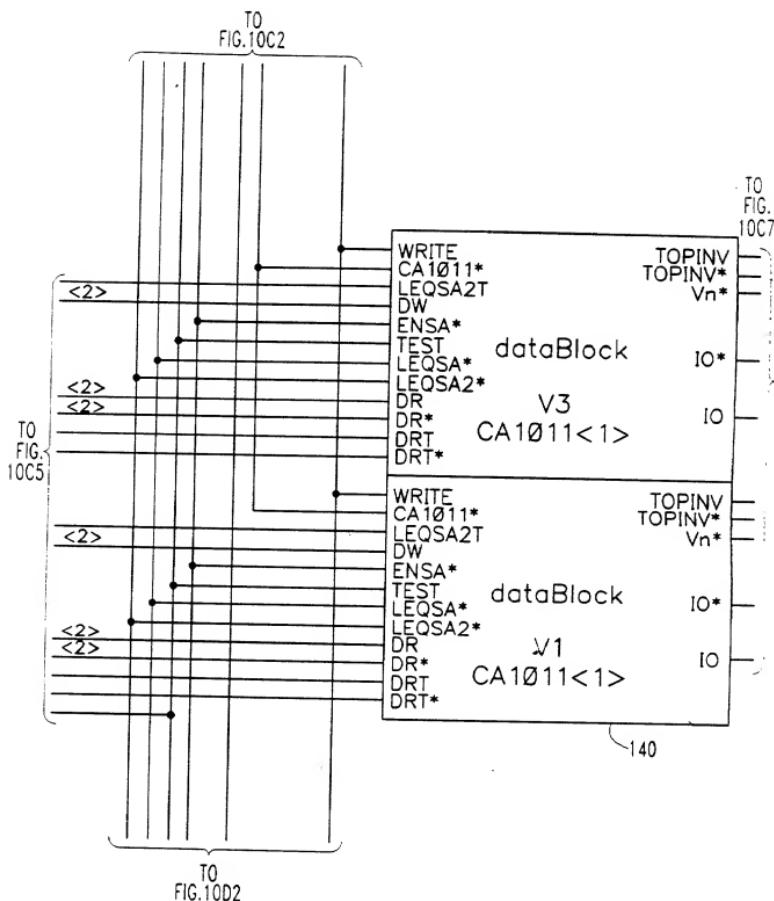


FIG.10C6

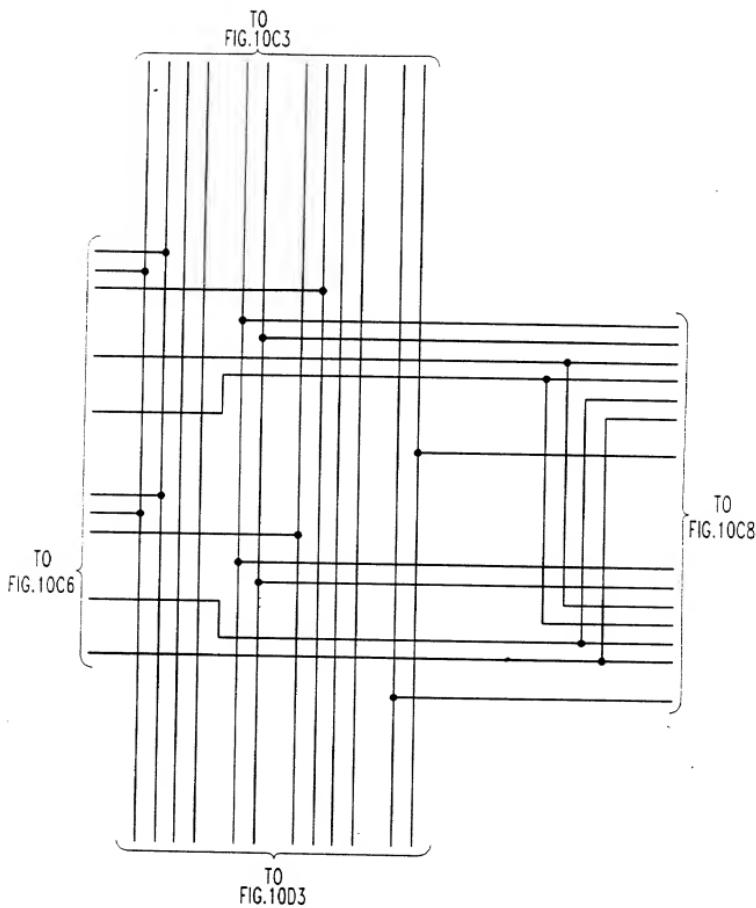


FIG.10C7

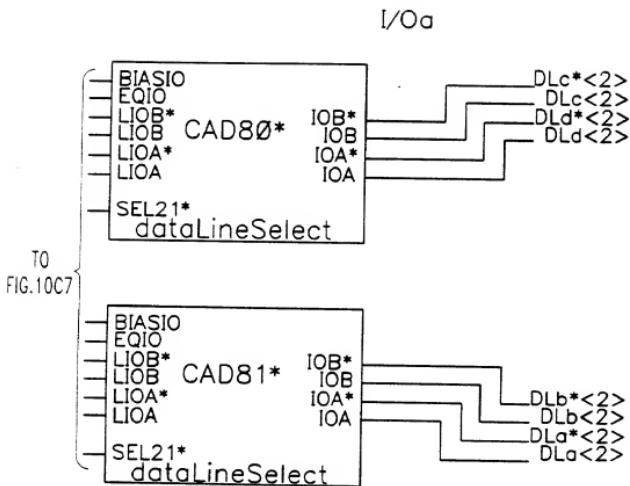


FIG.10C8

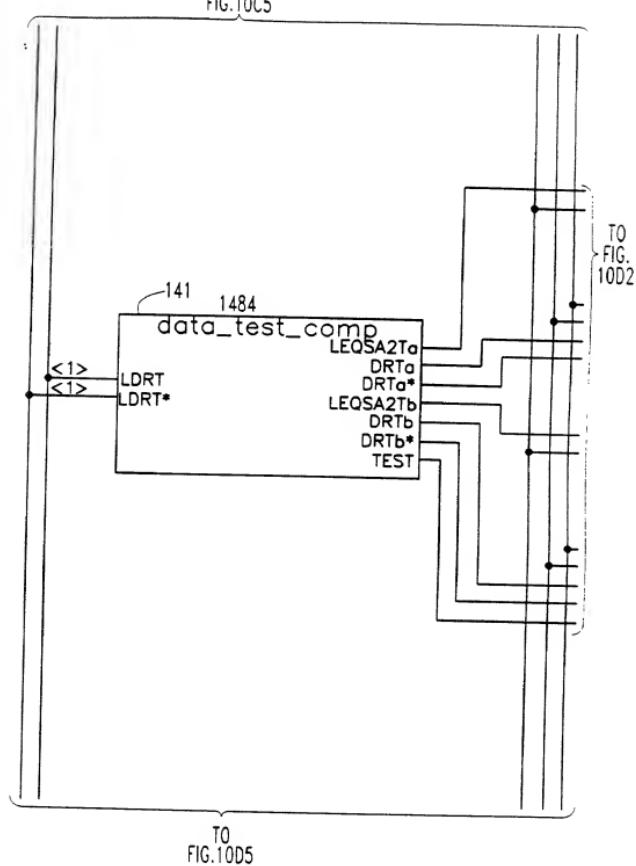
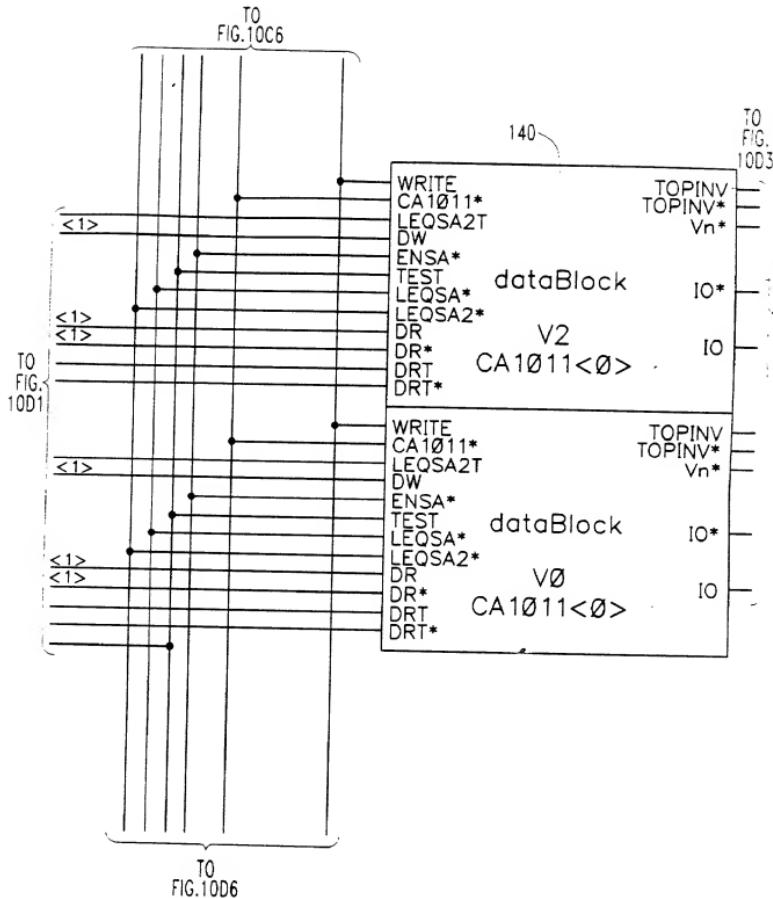
TO
FIG.10C5TO
FIG.10D5

FIG.10D1



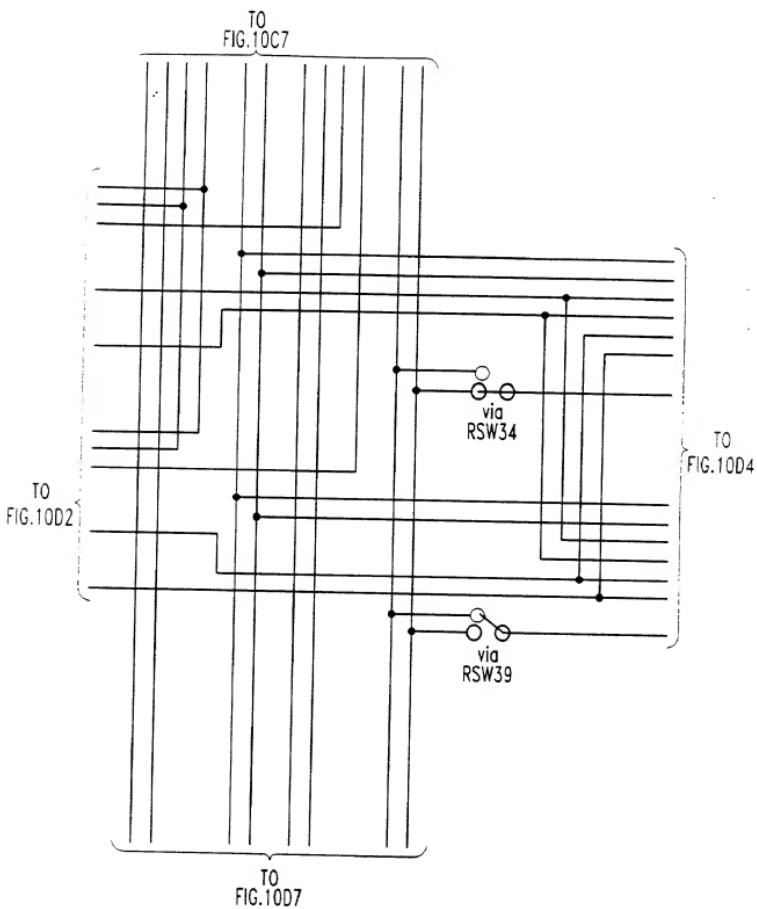


FIG.10D3

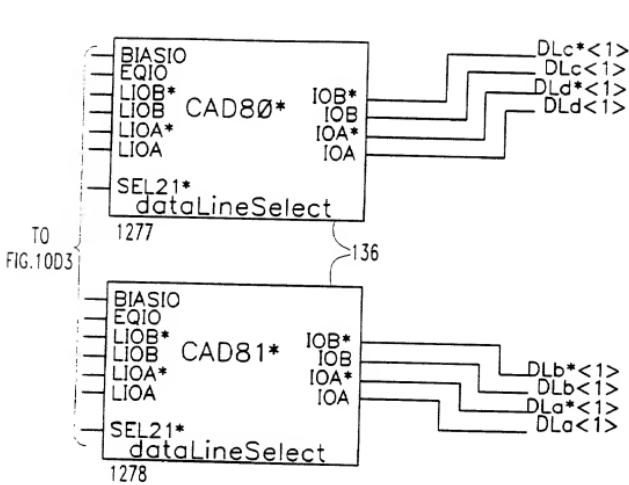


FIG. 10D4

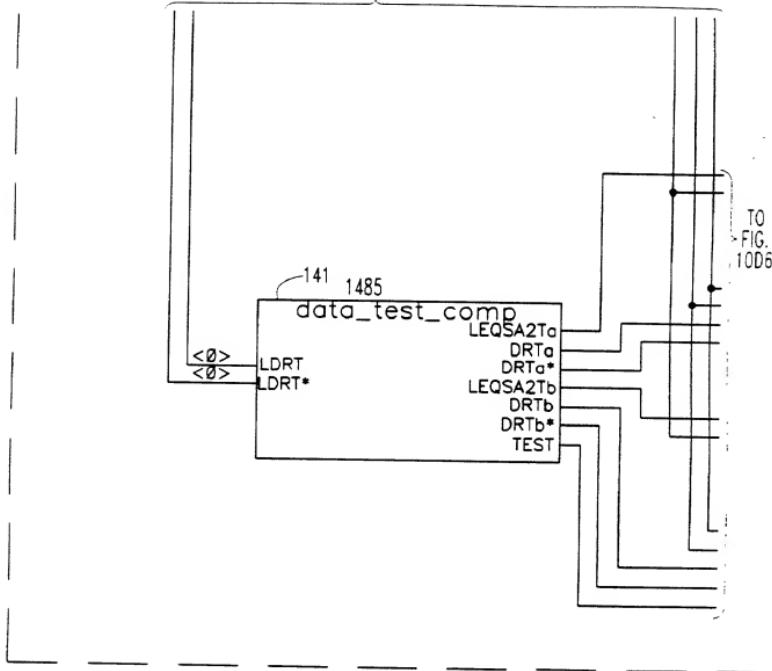
TO
FIG.10D1

FIG.10D5

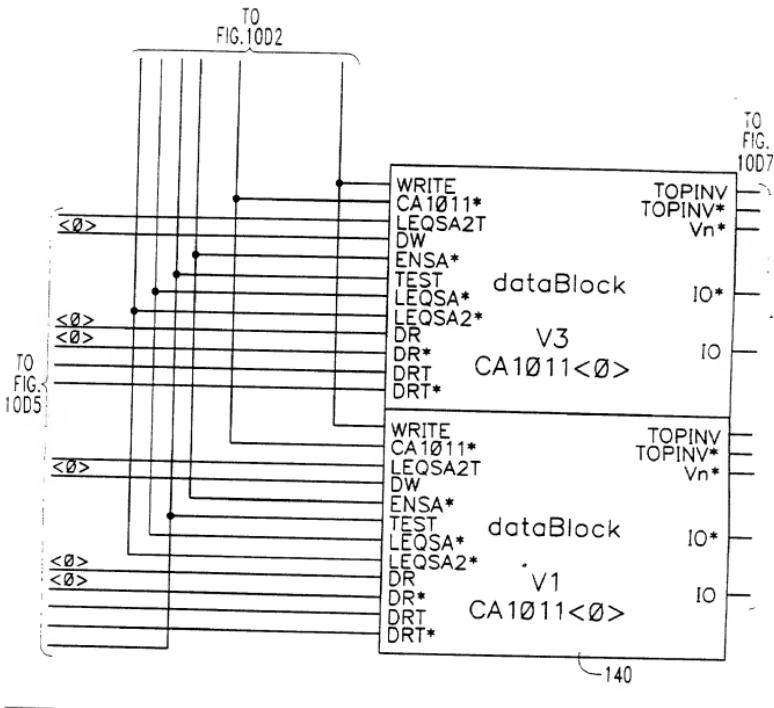


FIG. 10D6

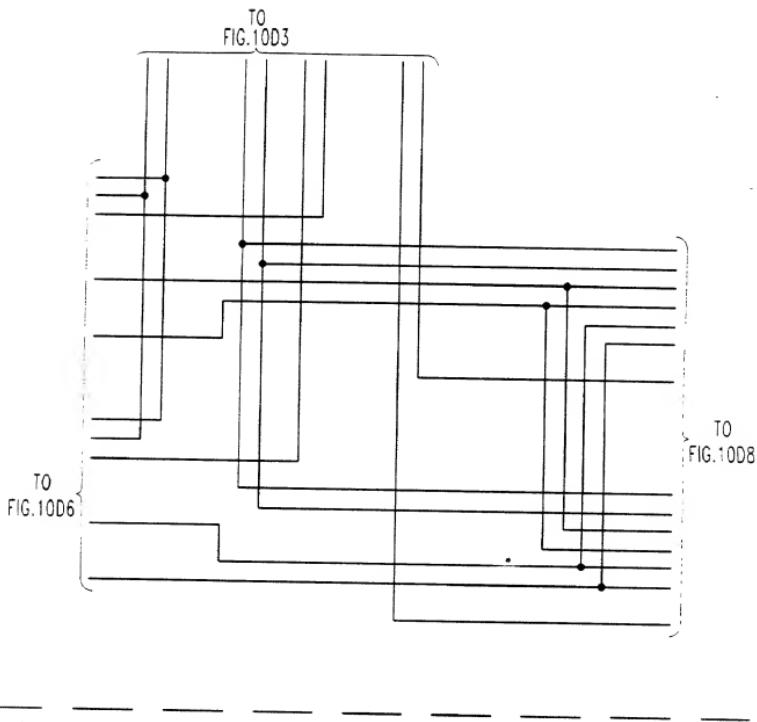


FIG.10D7

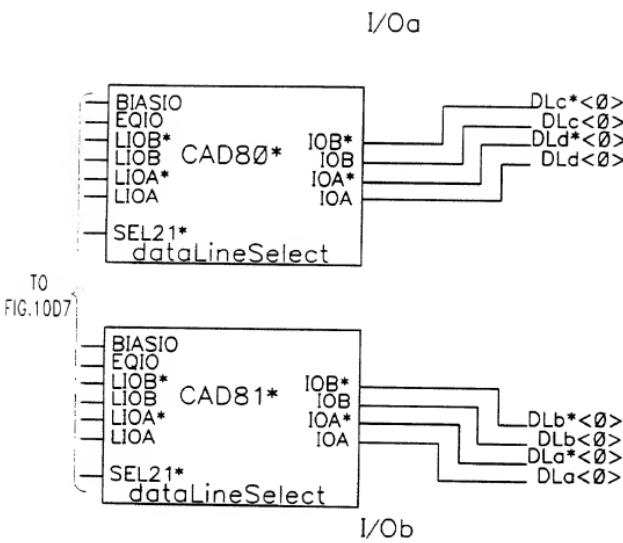


FIG.10D8

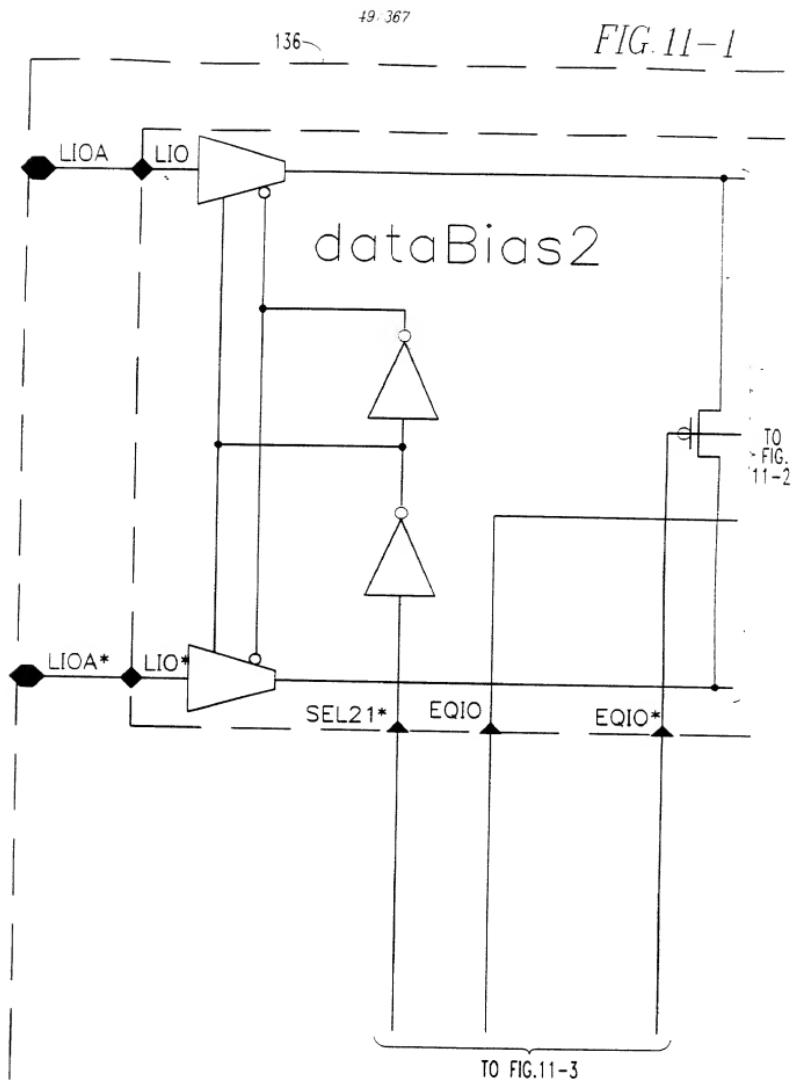
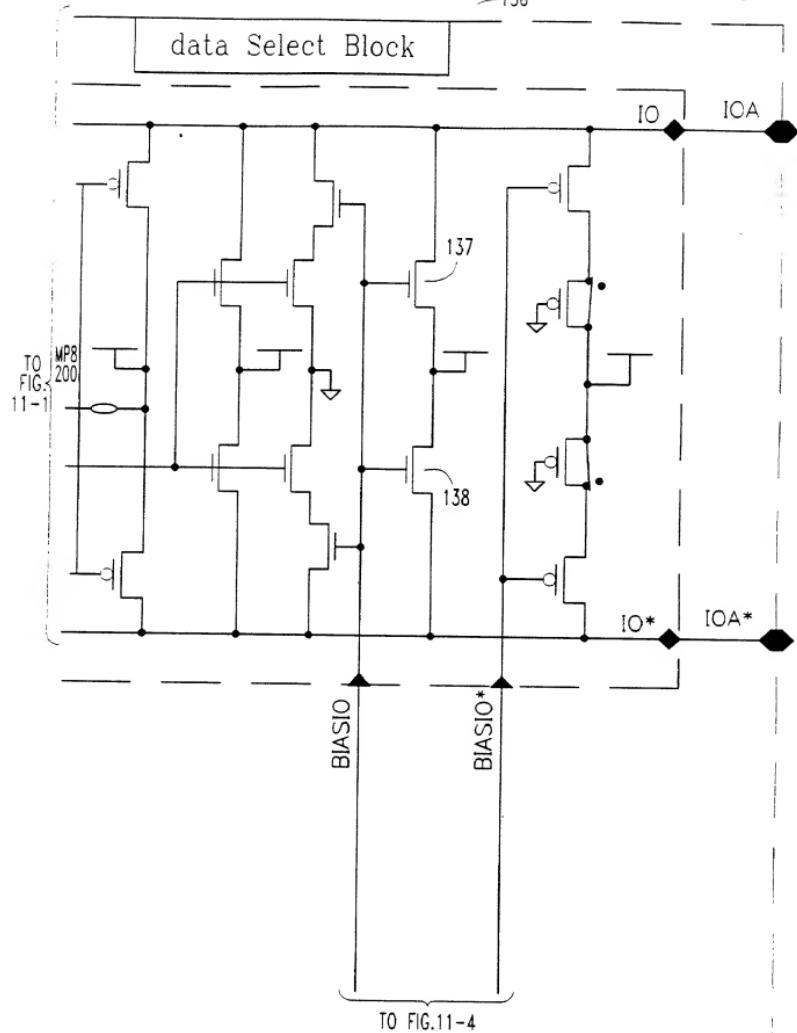


FIG. 11-2



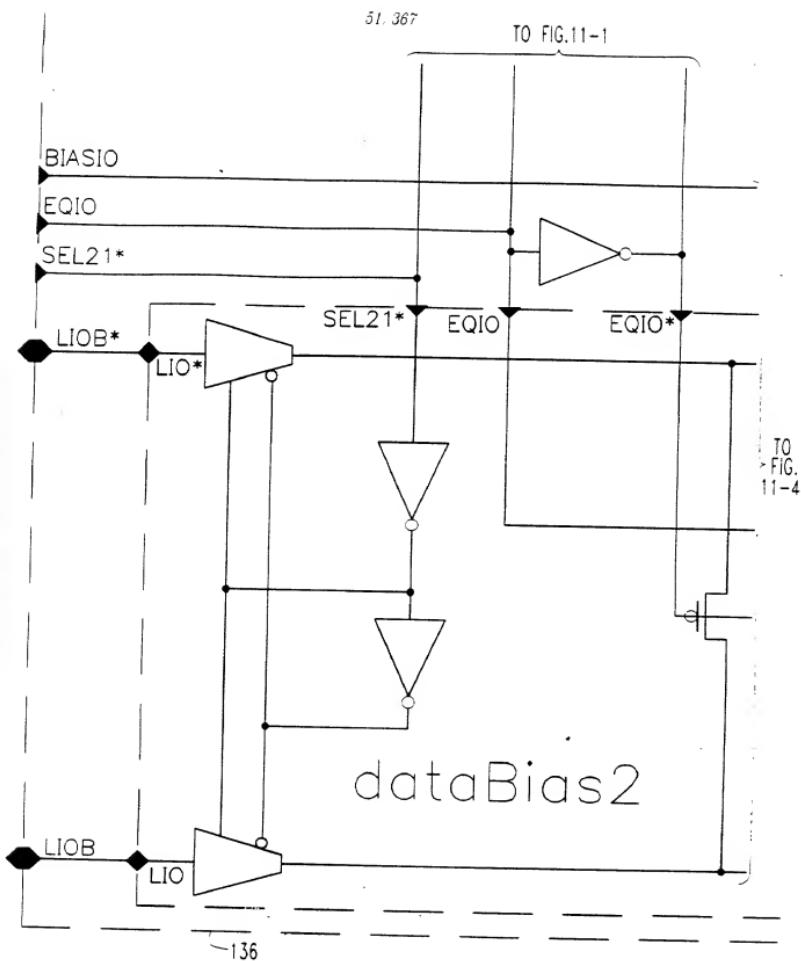


FIG.11-3

TO FIG.11-2

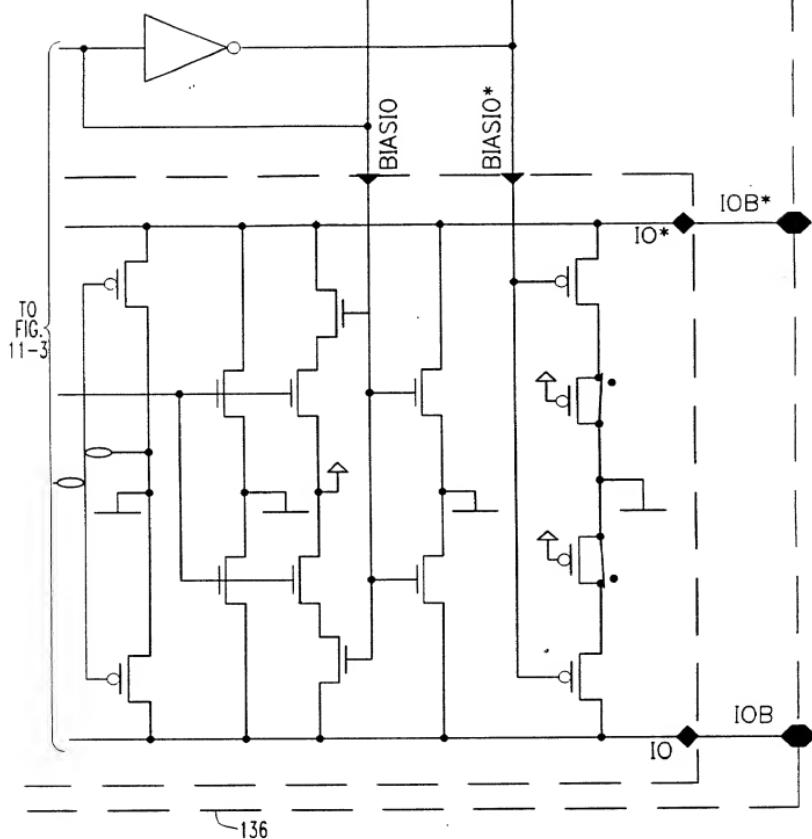
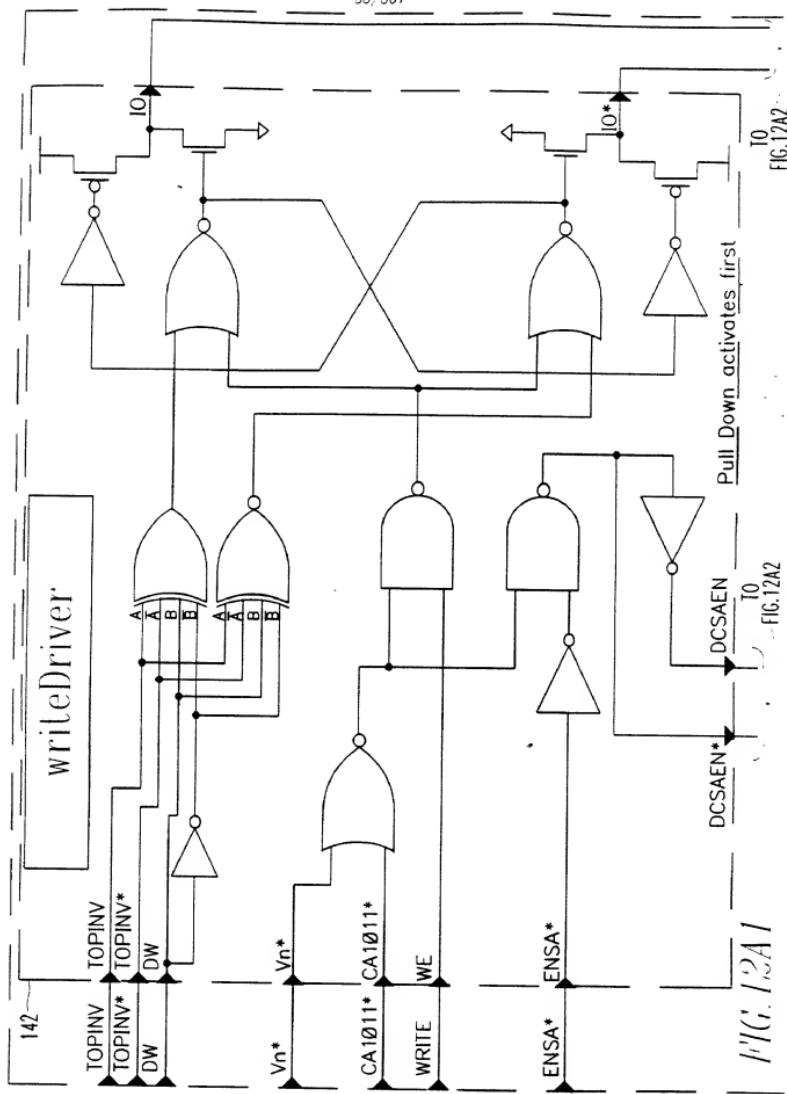


FIG.11-4



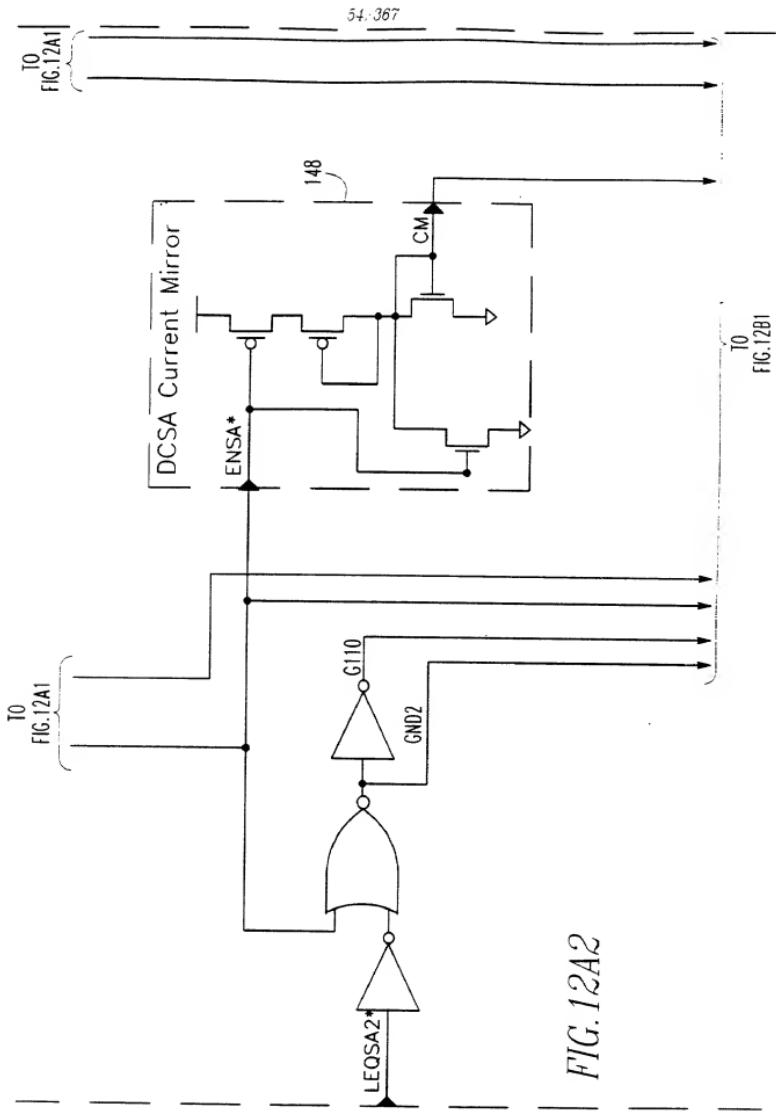
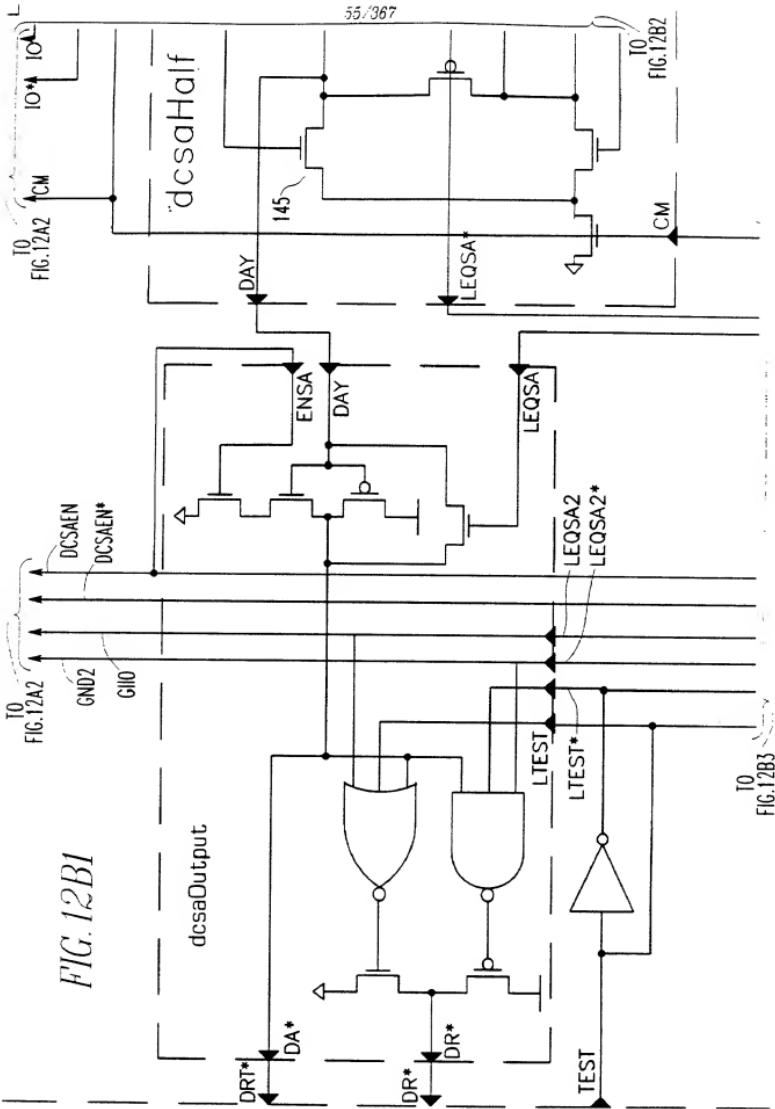


FIG. 12B1



55/367

10⁴

FIG.12B2

10⁴

FIG.12B3

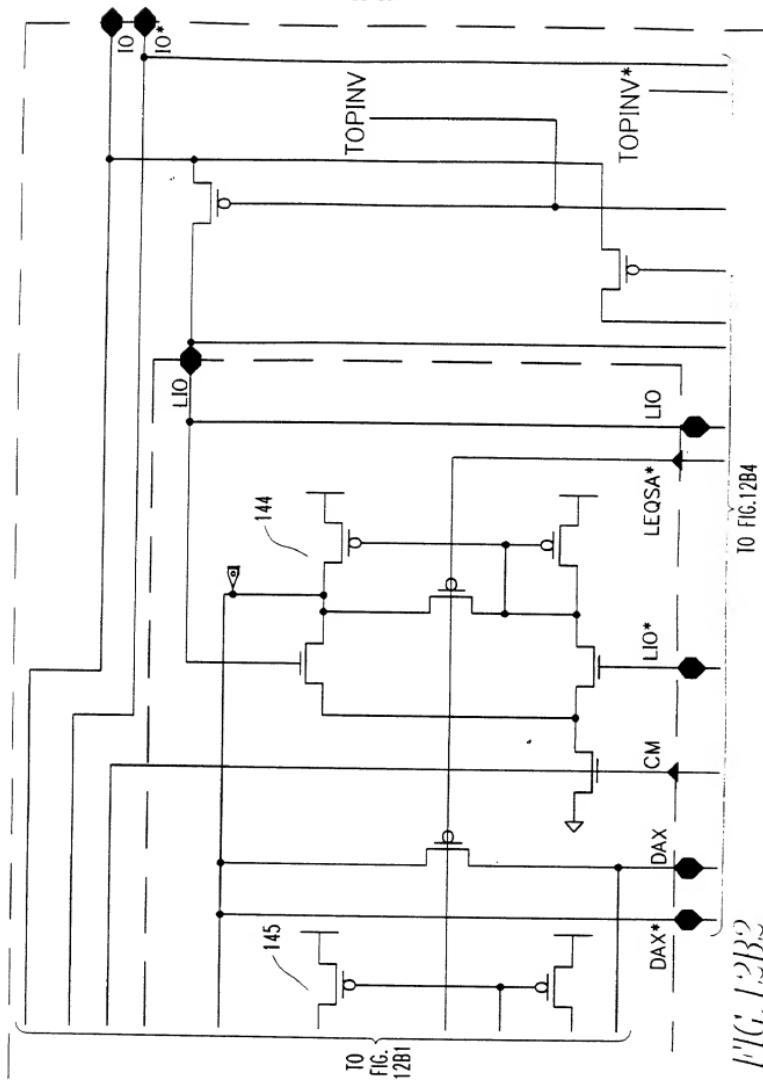


FIG. 12B2

FIG. 12B4

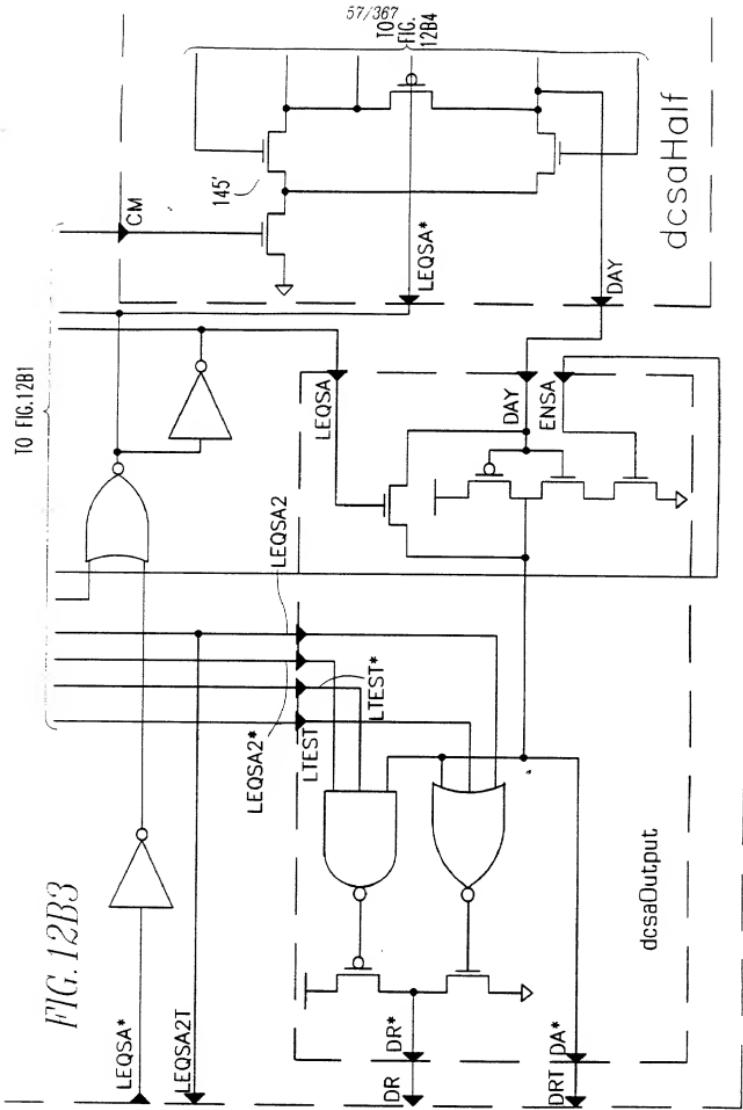
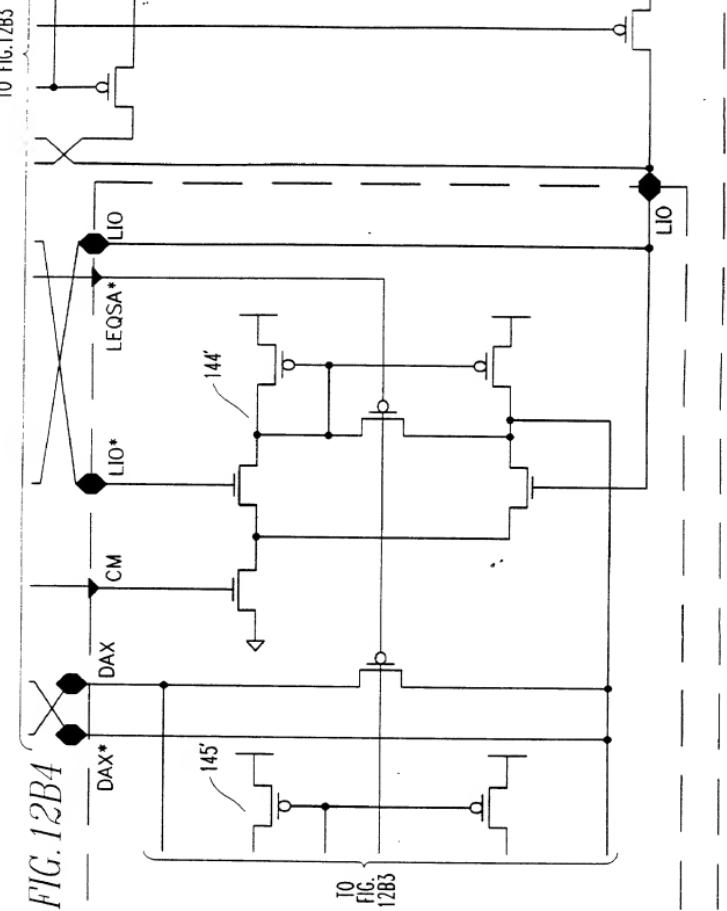
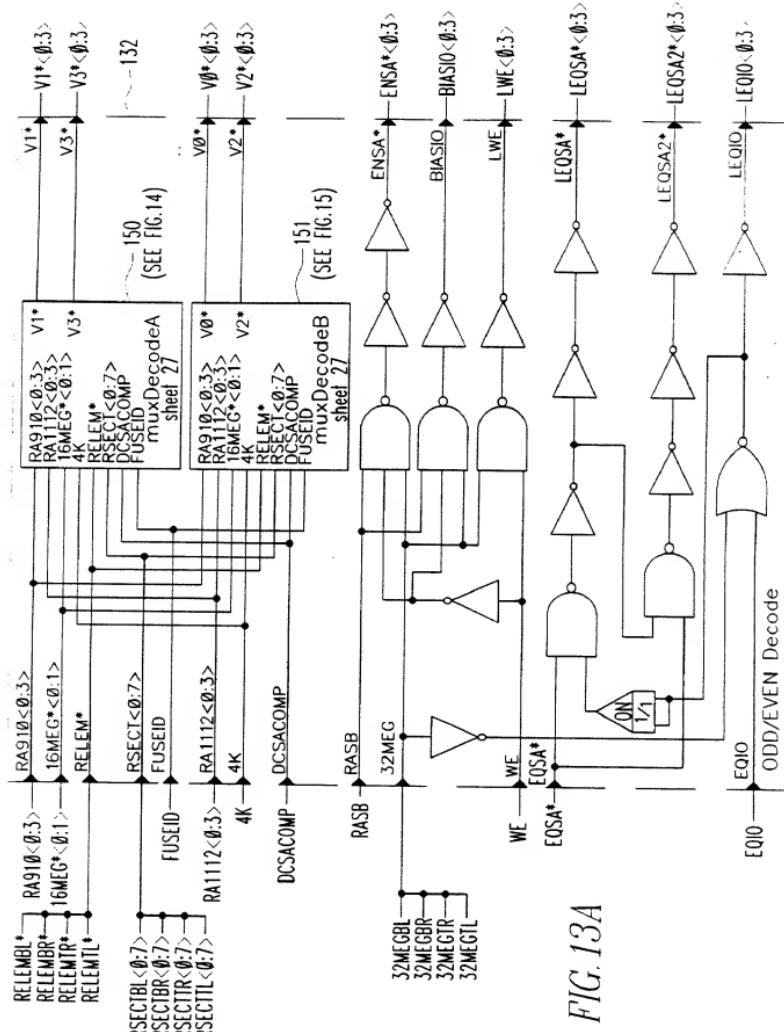


FIG.12B3

FIG.12B4





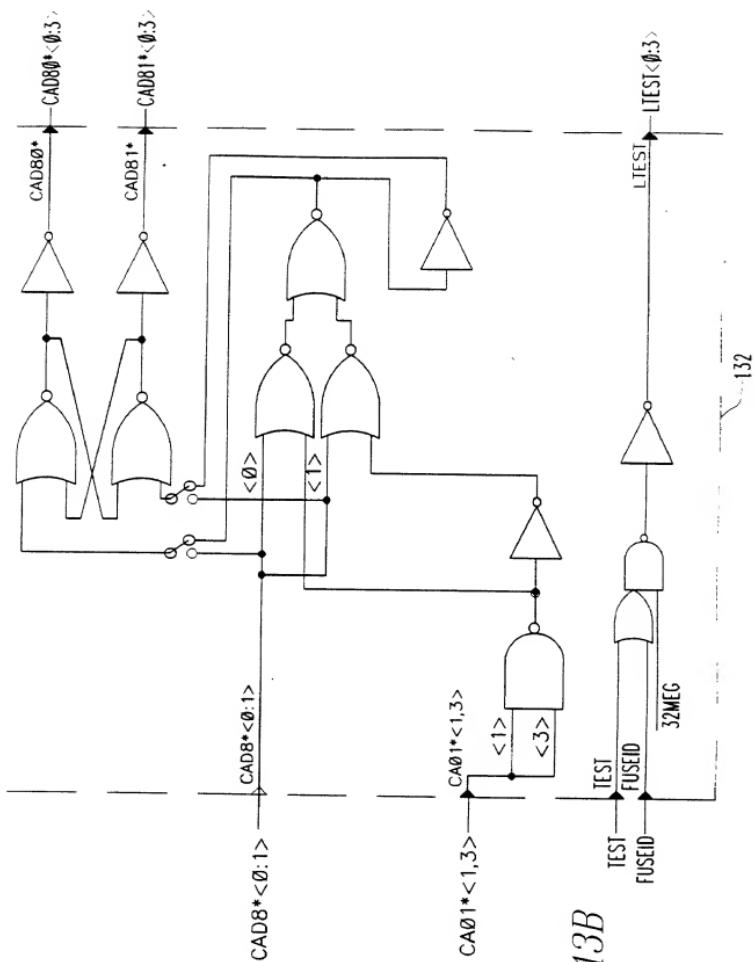
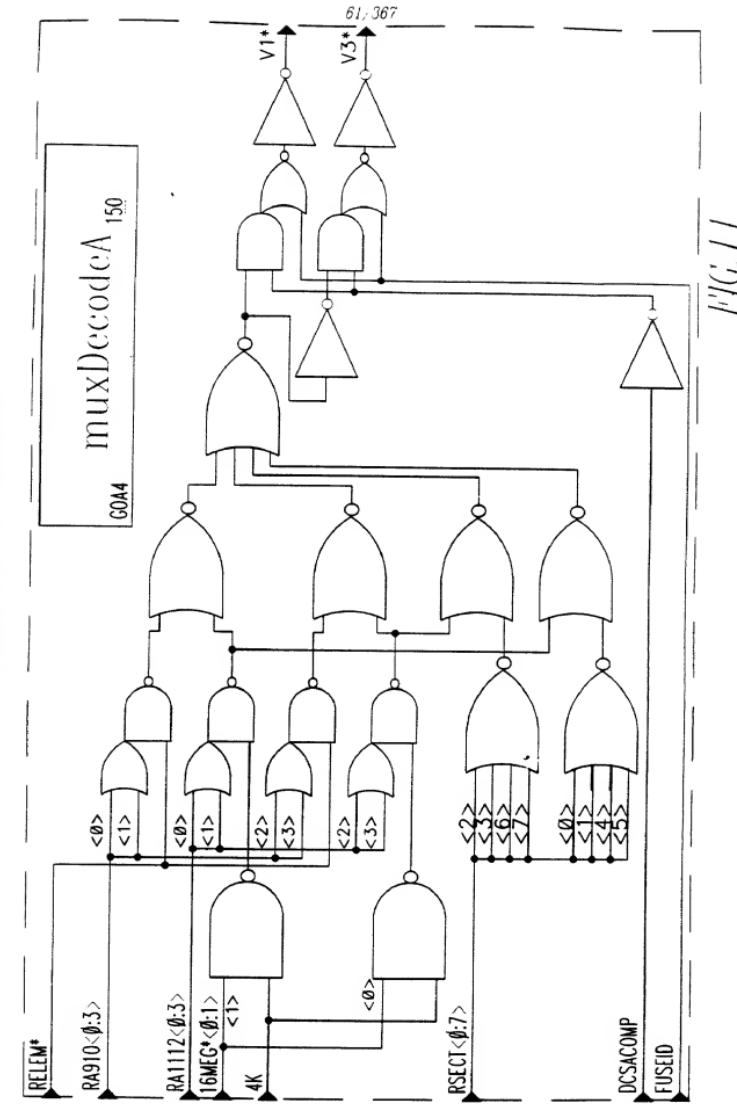


FIG. 13B

132

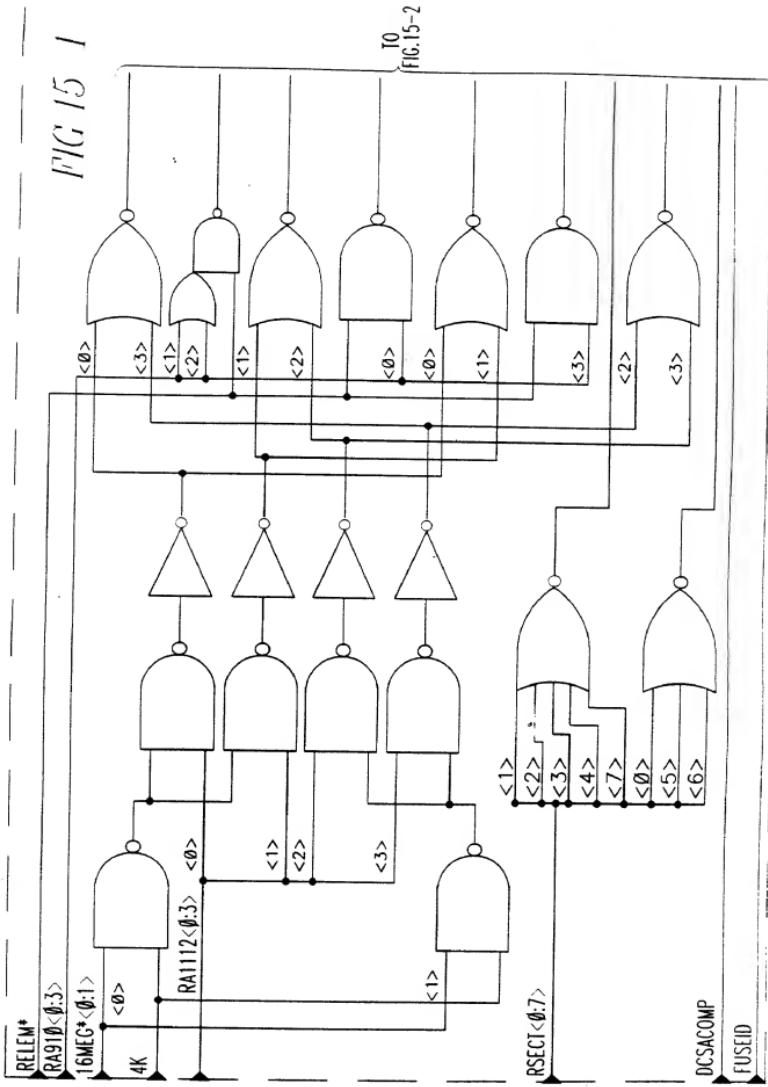


TOEASD "FRESCO 60

62 367

10
FIG. 15-2

FIG 15 1



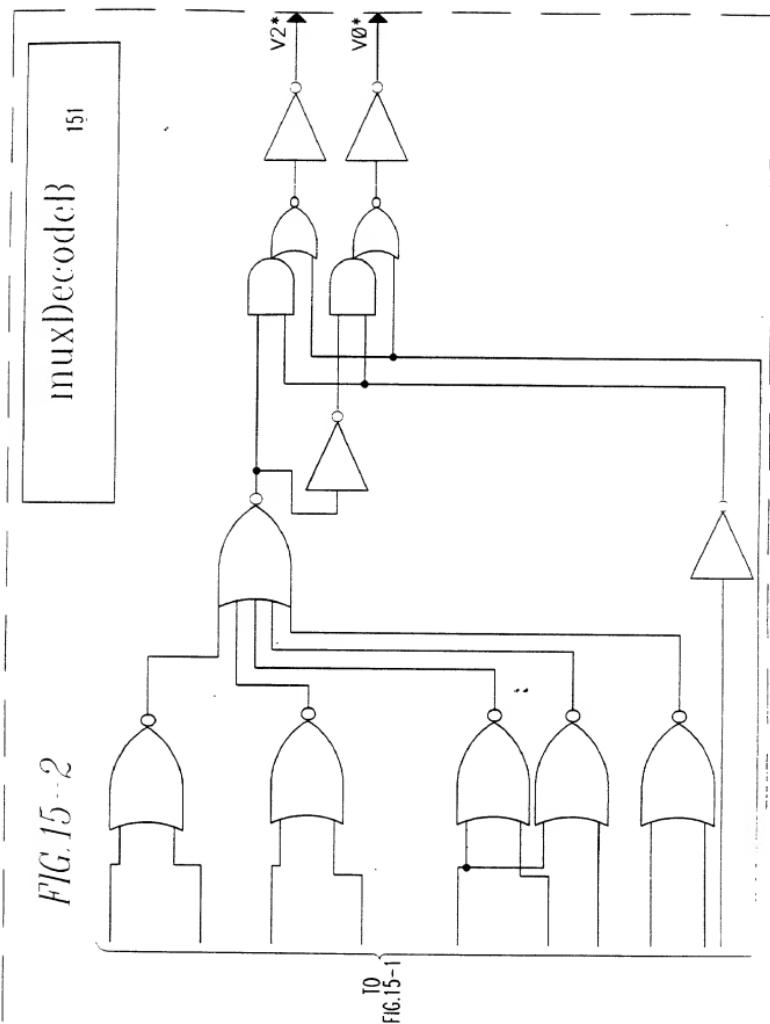
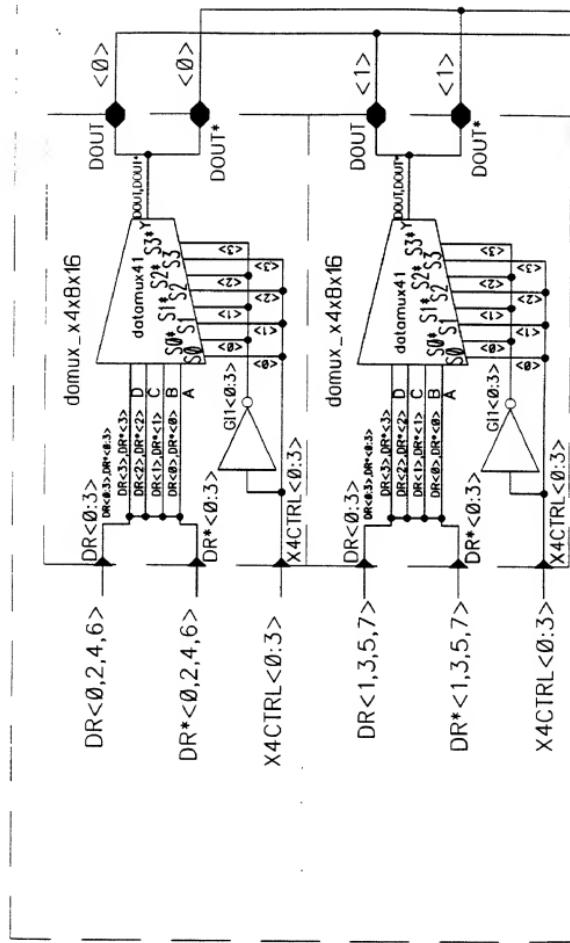
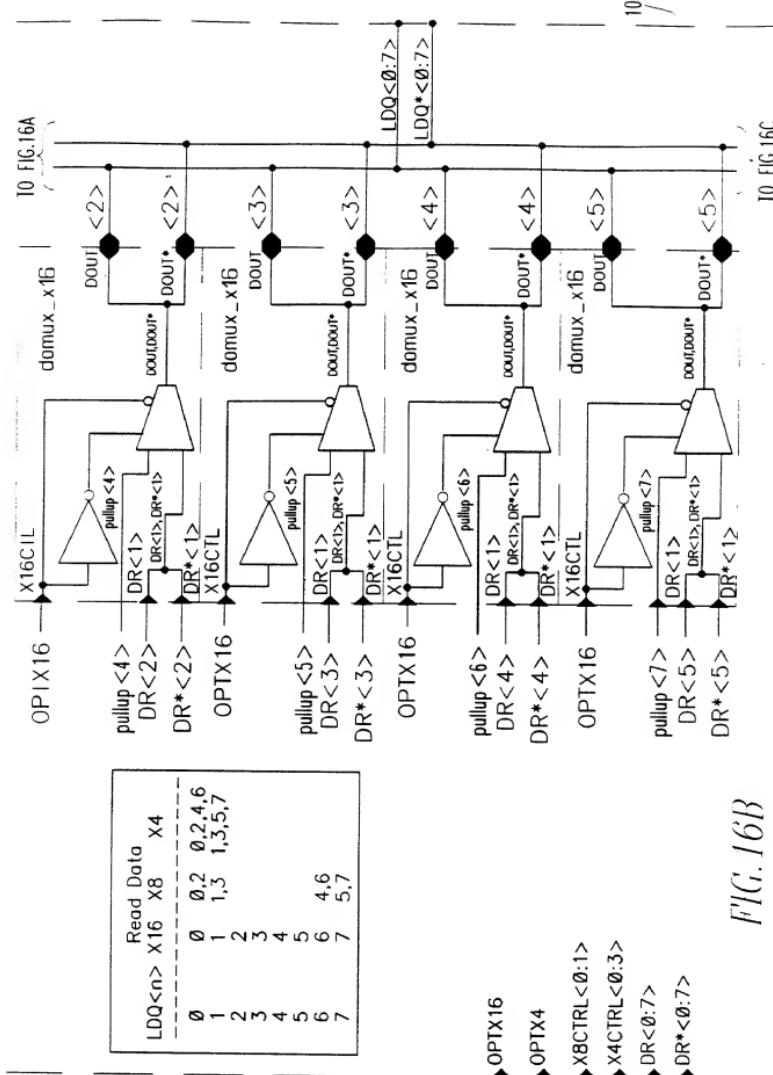
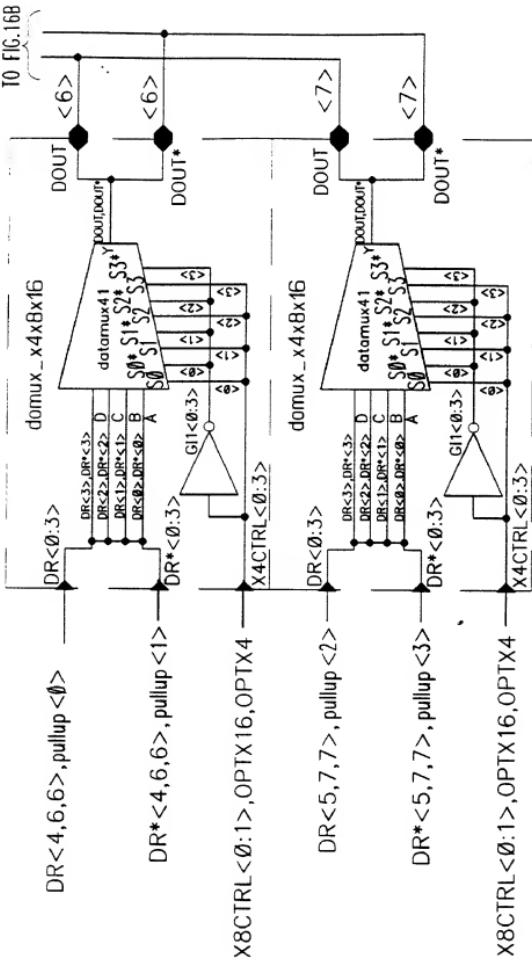


FIG. 16A



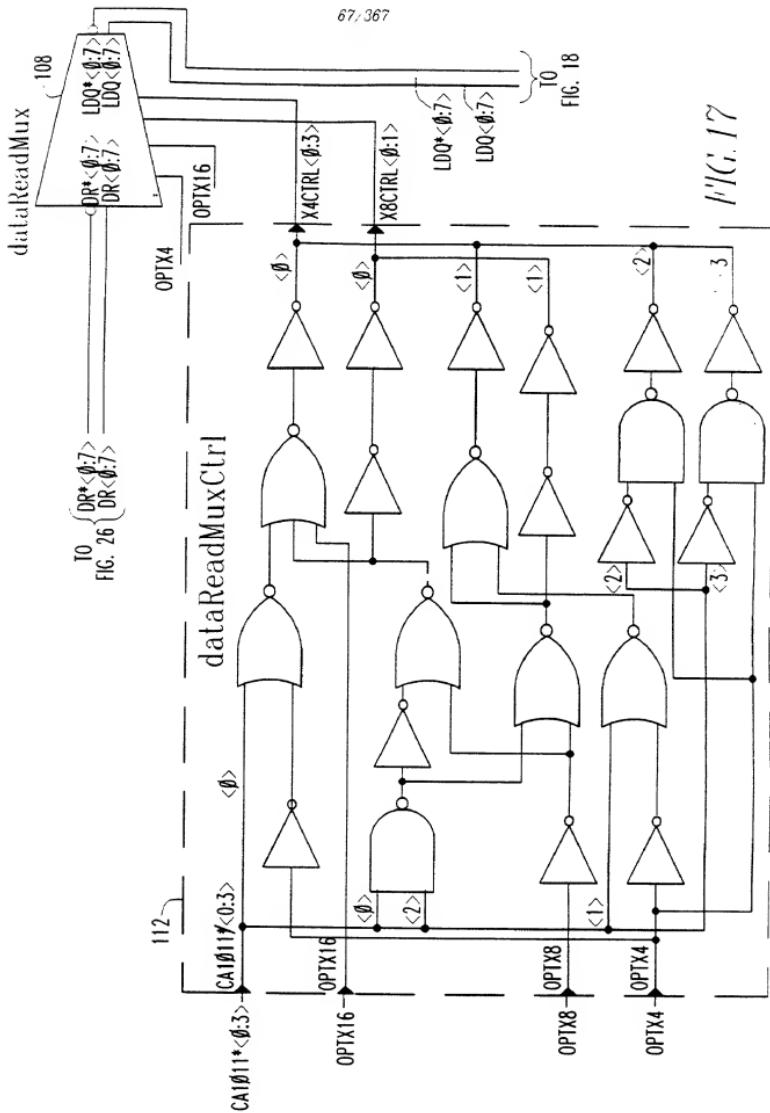
TO FIG. 16B

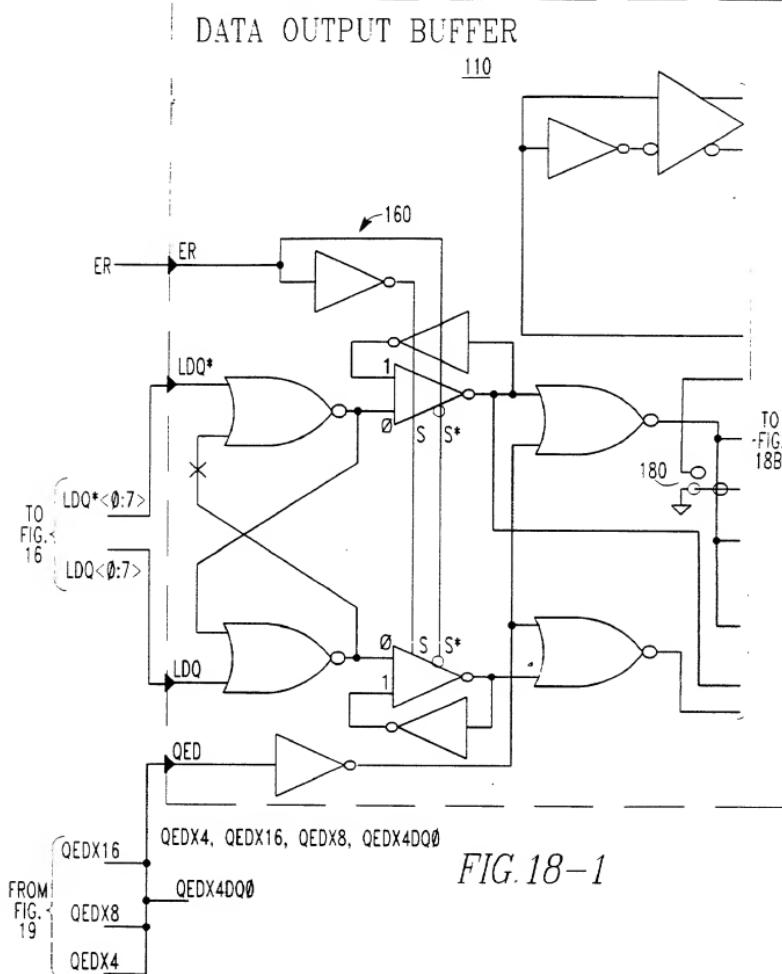




dataReadMux

16C





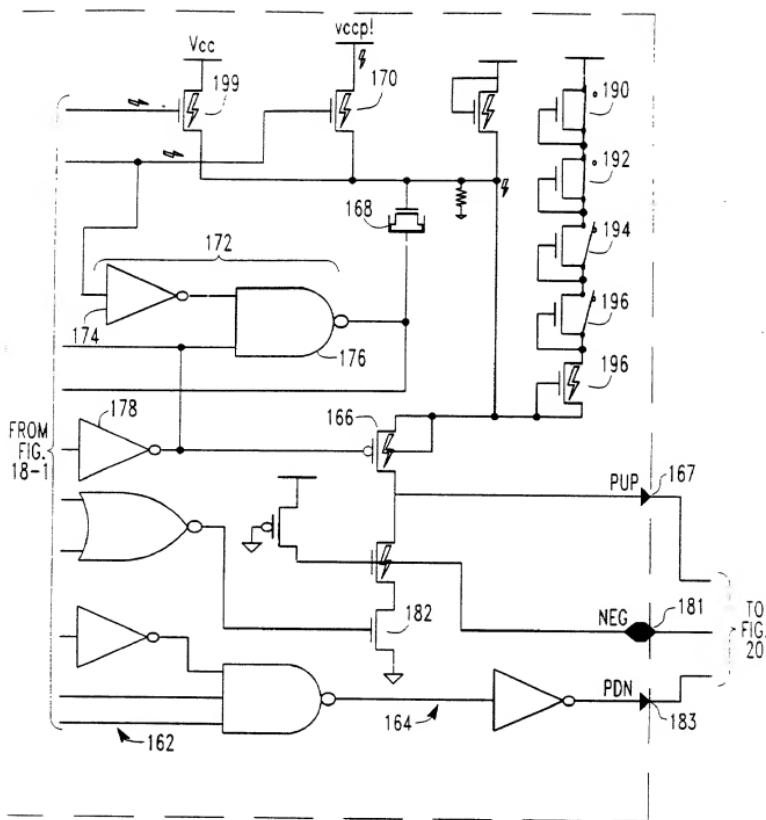


FIG. 18-2

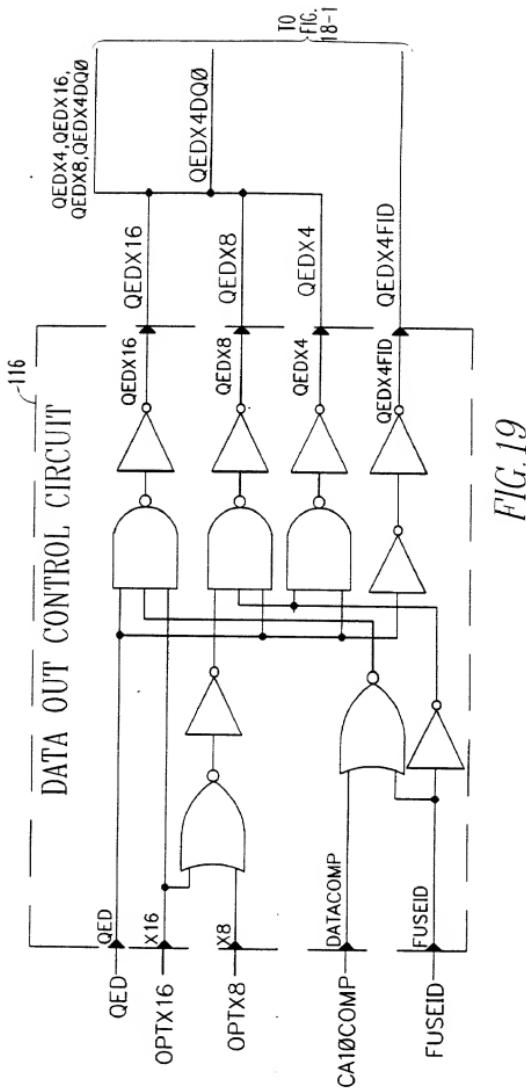


FIG. 19

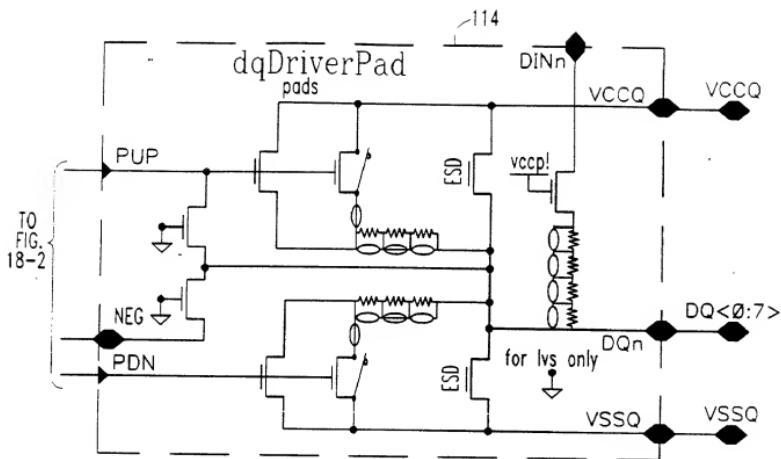


FIG. 20

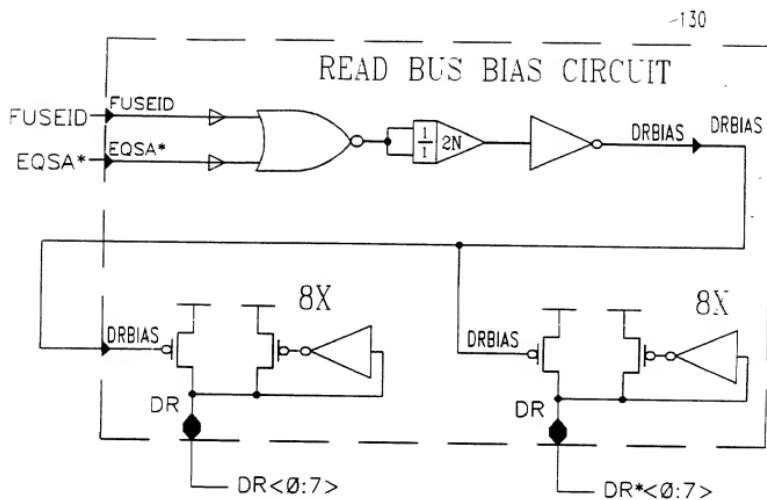


FIG. 21

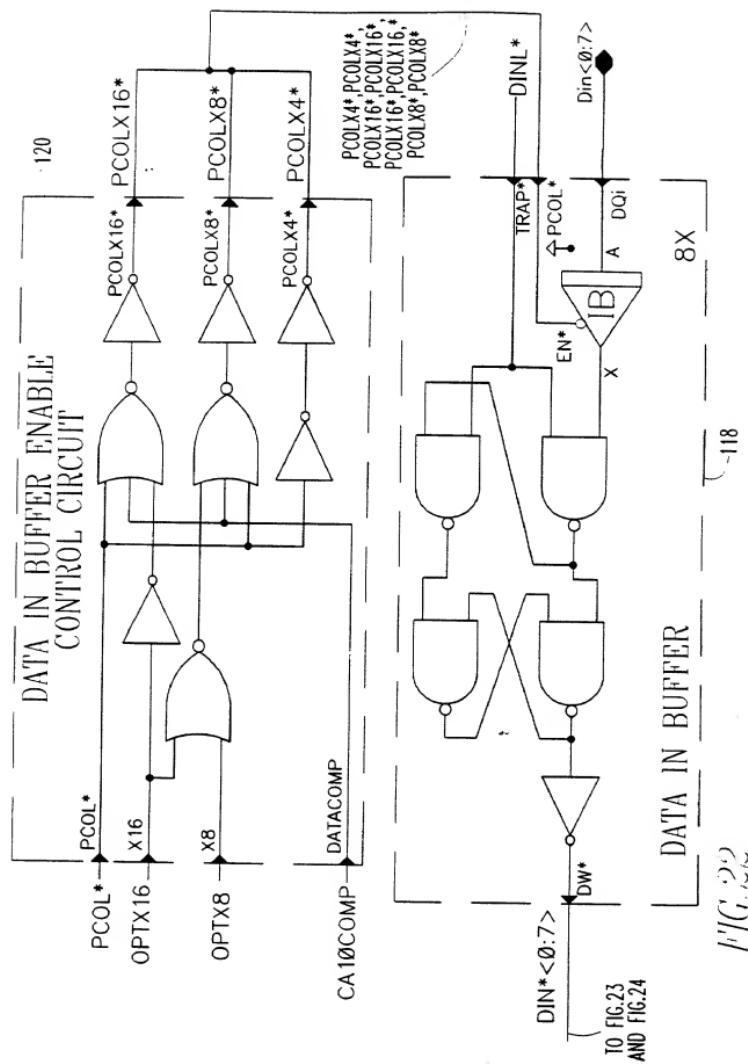


FIG.23-1

DIN*<0:7>

MUX1X

MUX2X

MUX4X

DIN*<0>

DIN*<1>

DIN*<2>

DIN*<3>

dataWriteMux

dataWriteMux1X

dataWriteMux1X

dataWriteMux2X

dataWriteMux2X

dataWriteMux2X

dataWriteMux2X

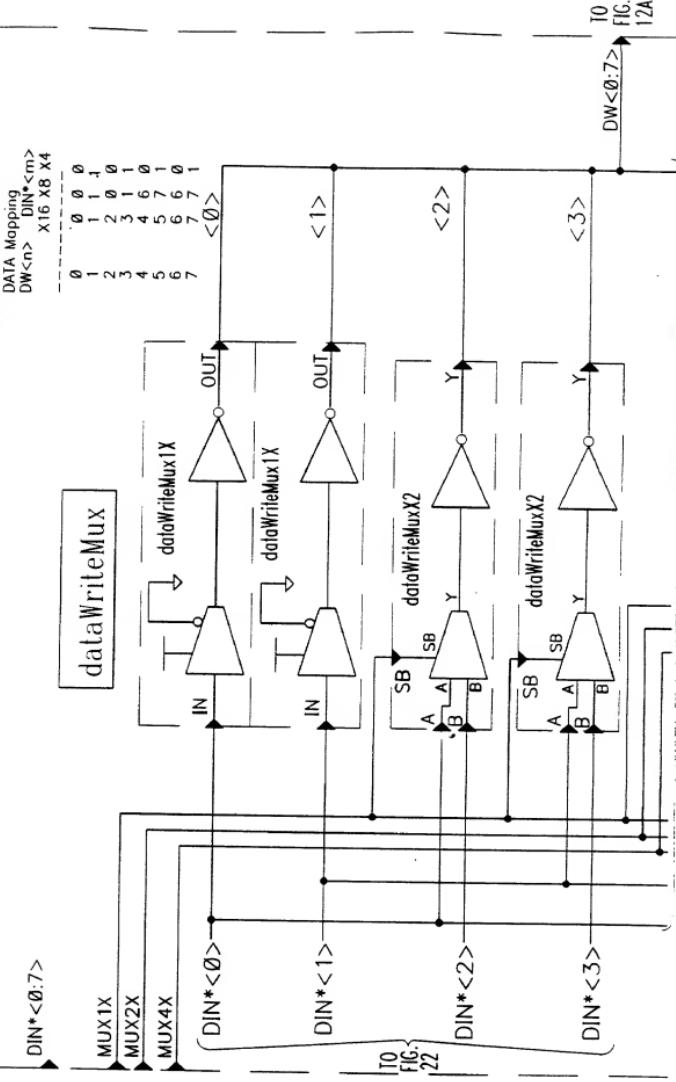
dataWriteMux2X

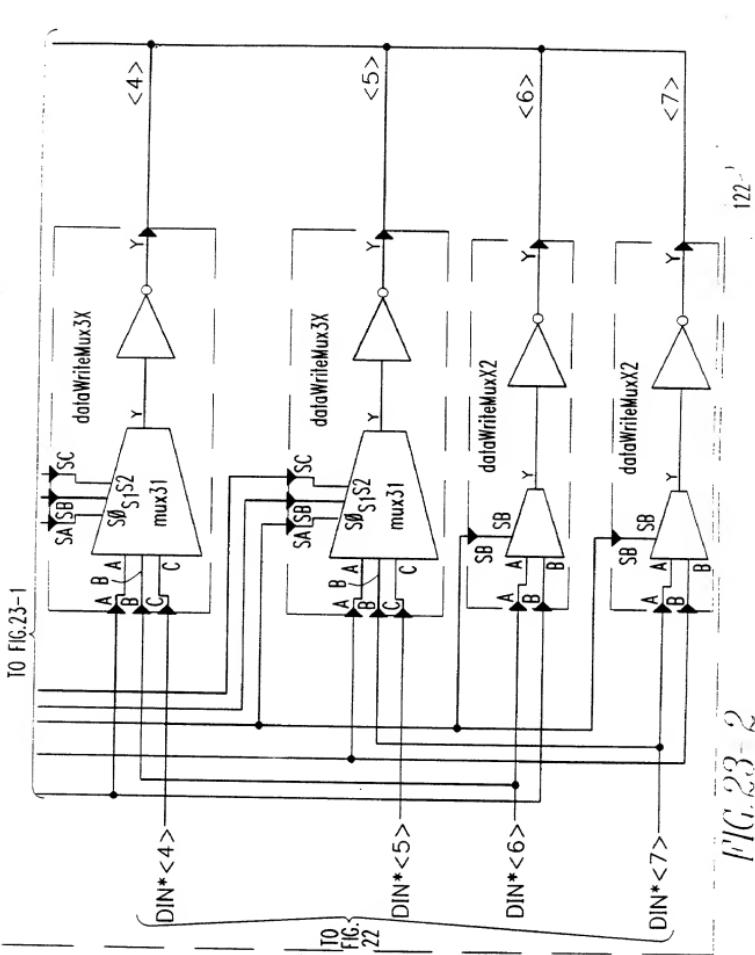
dataWriteMux2X

To FIG.22

To FIG.23-2

74, 367





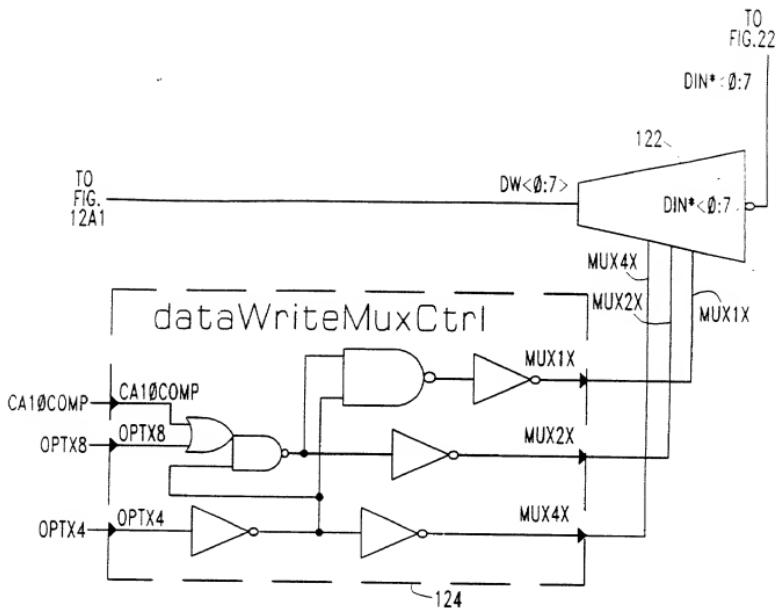
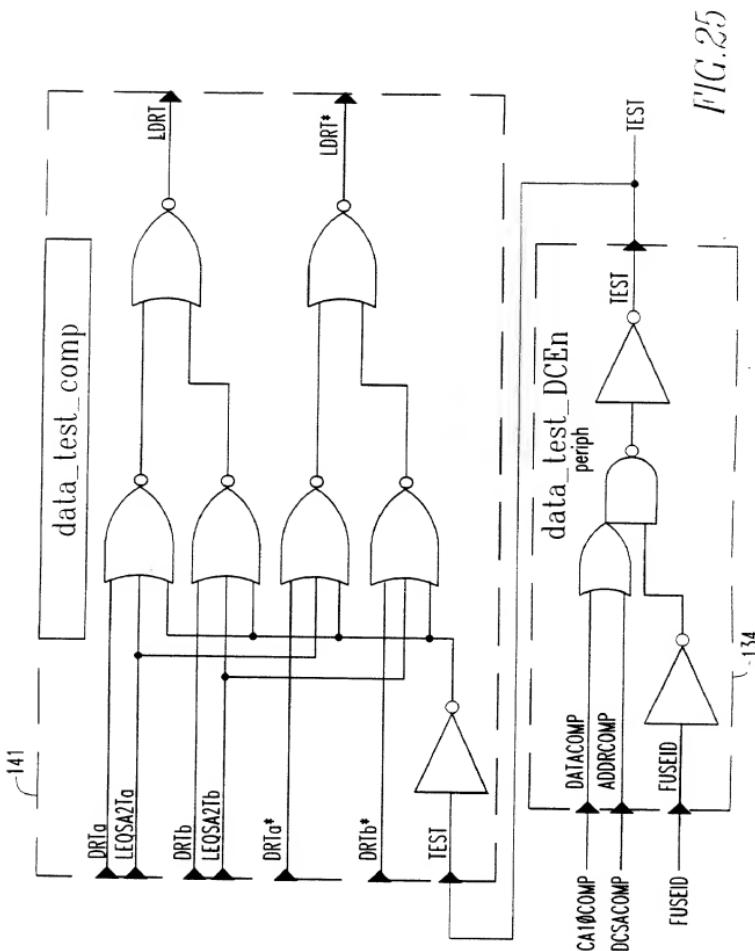


FIG.24



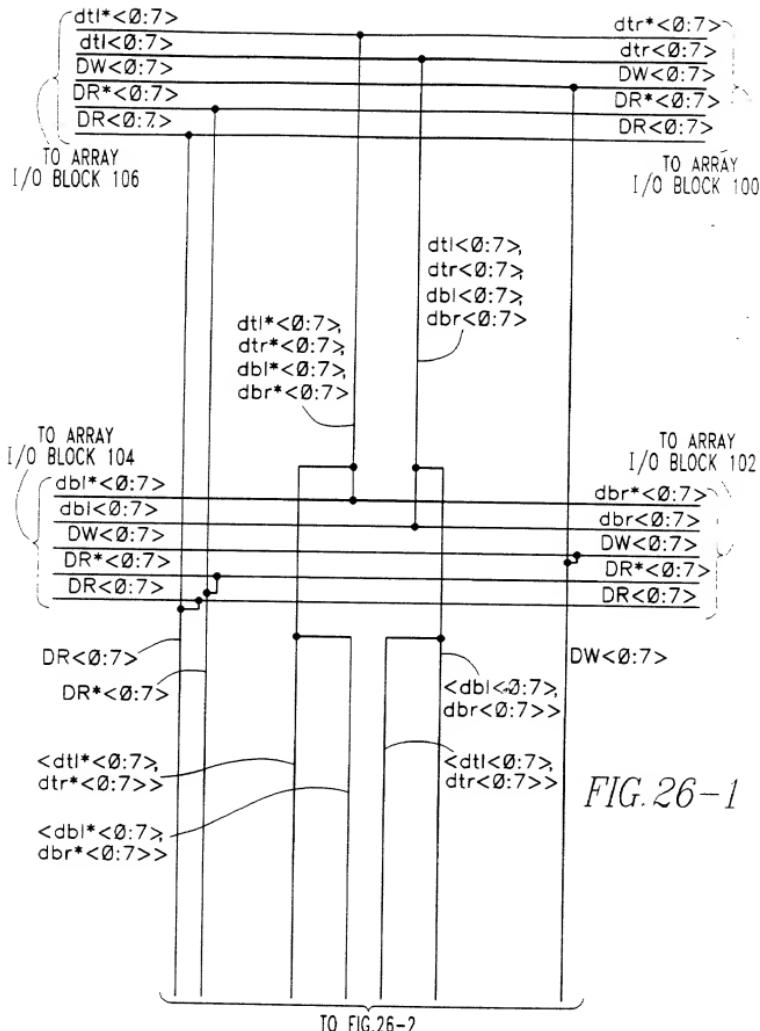


FIG. 26-1

TO FIG.26-2

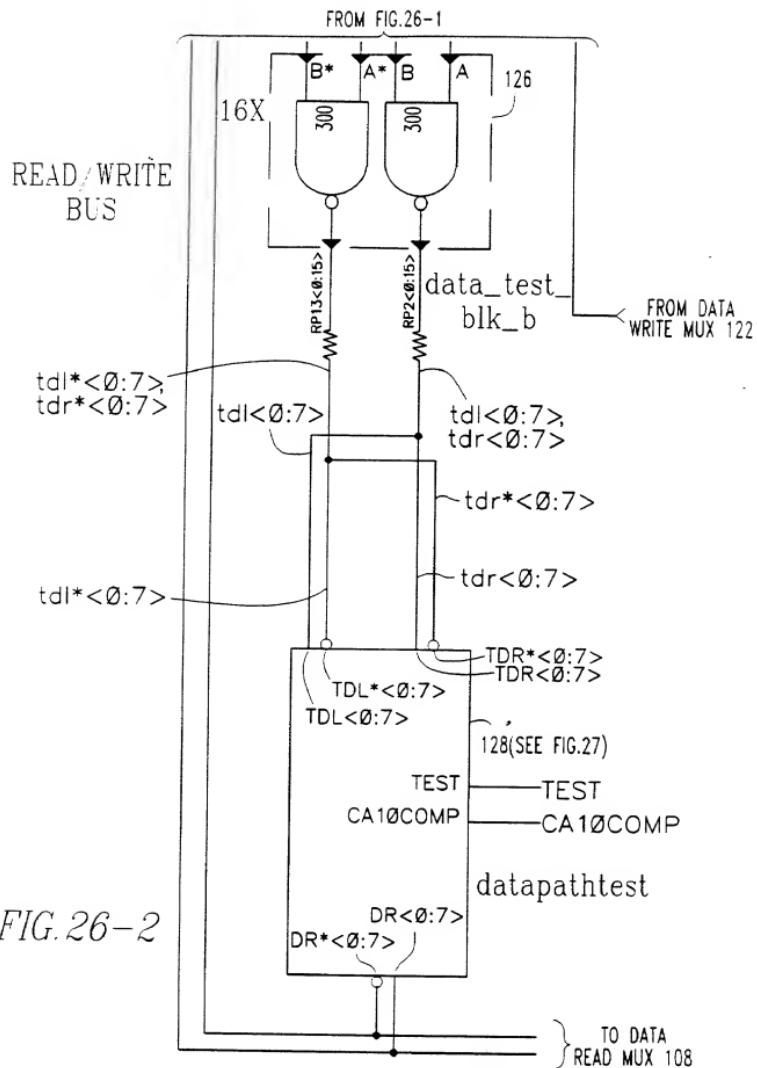


FIG.26-2

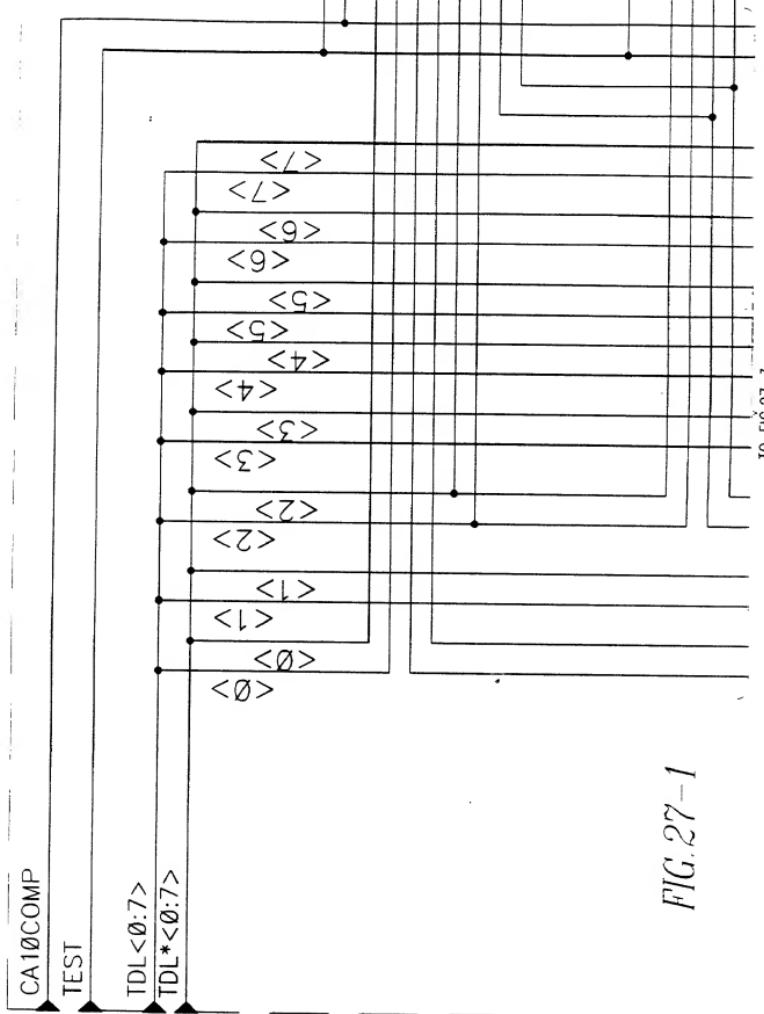
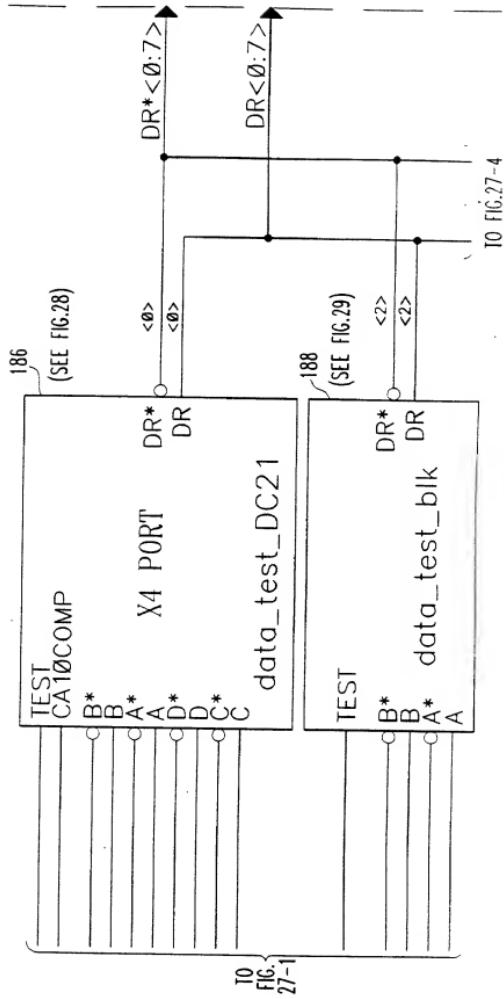


FIG. 27-1

FIG. 27-2

dataPathTest
Block 128



82/367

10
FIG.
27-4

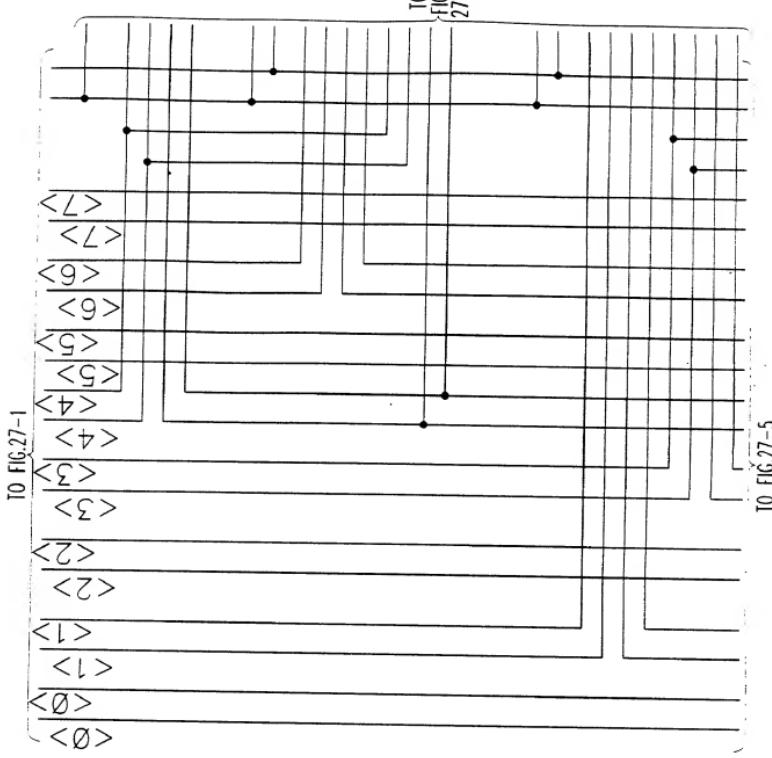
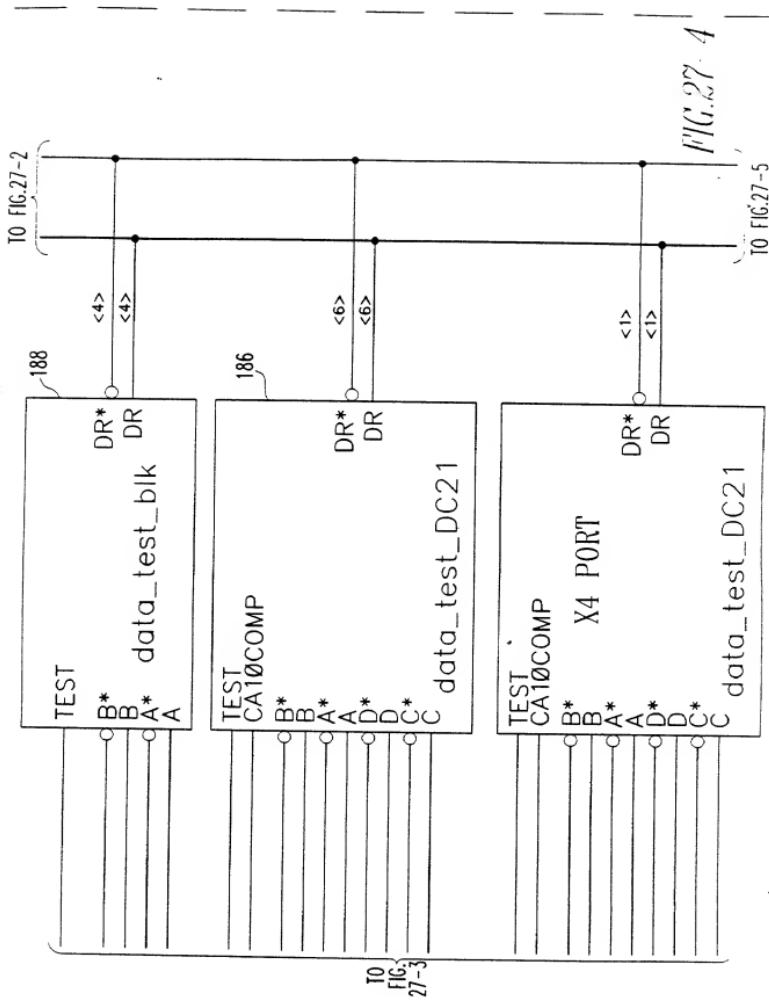


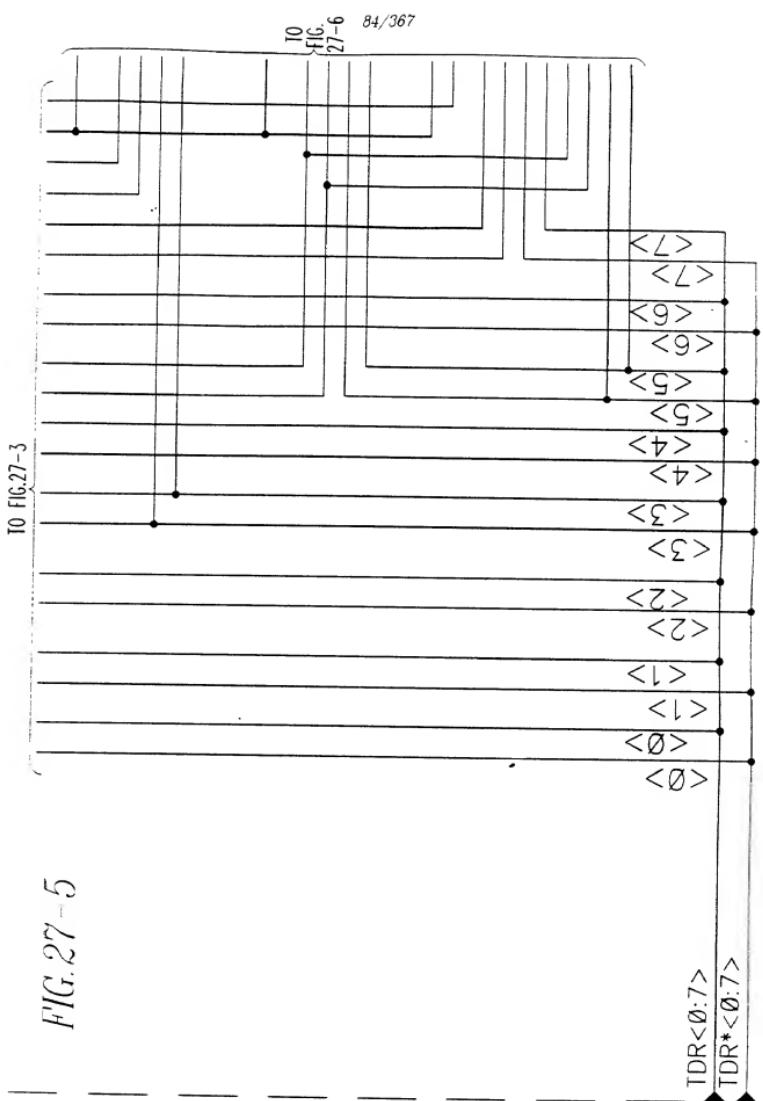
FIG. 27-3

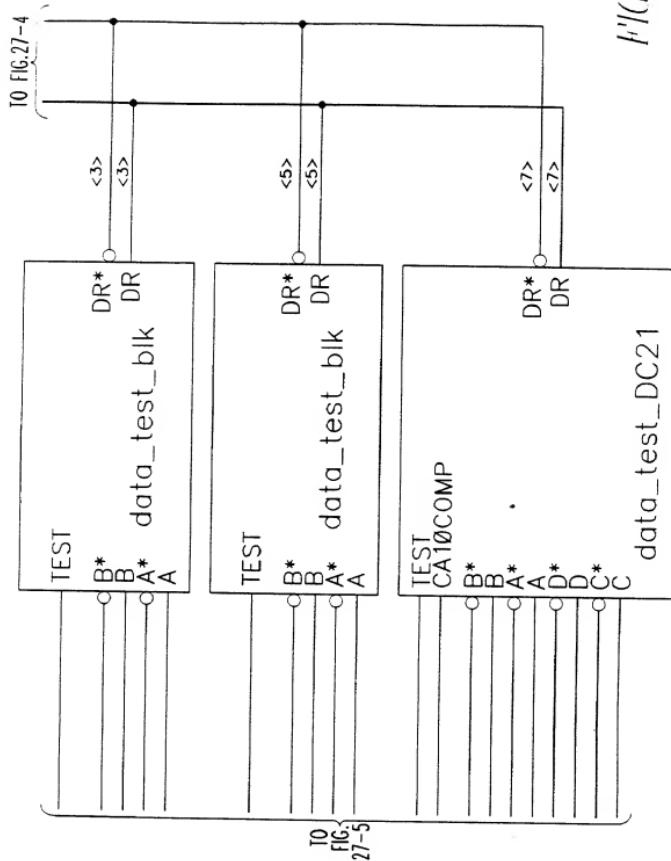
T082904 6882262612



10 FIG.27-3

FIG.27-5





H/G 27.6

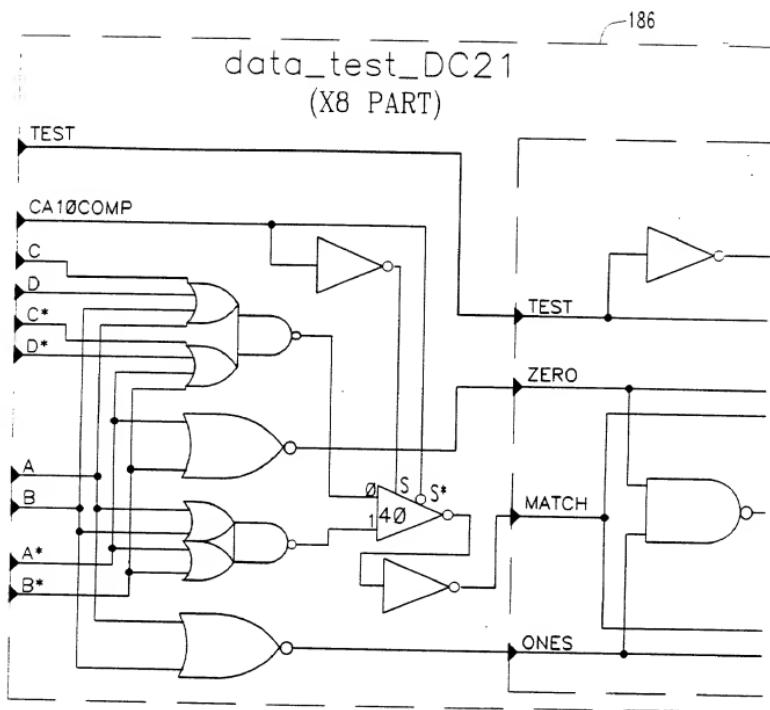


FIG. 28-1

186

data_test_decode

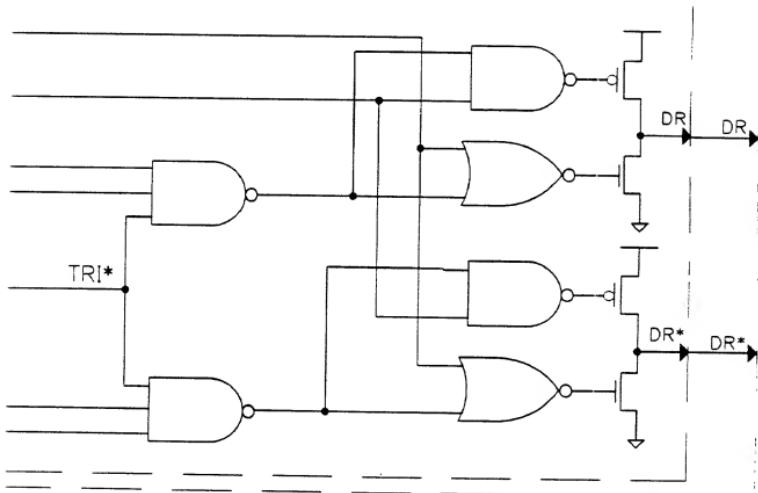


FIG. 28-2

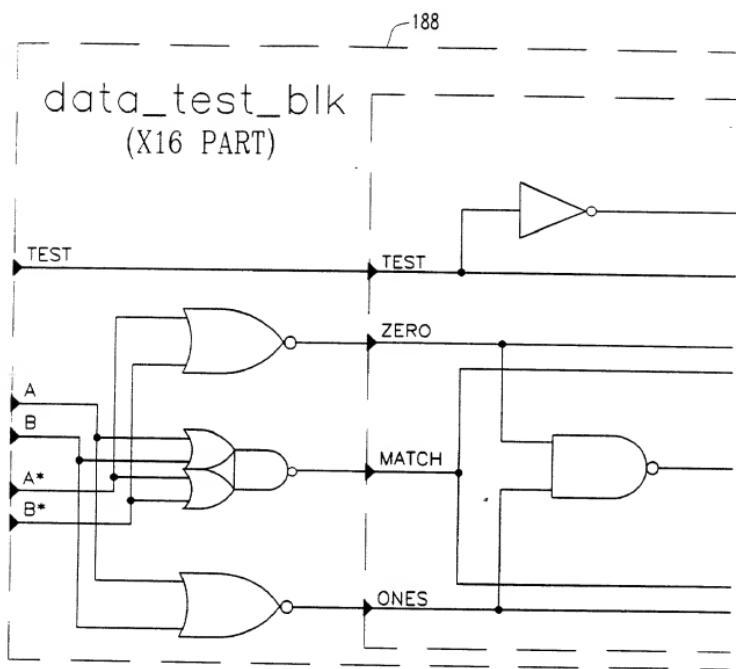


FIG. 29-1

TBD250-Datasheet

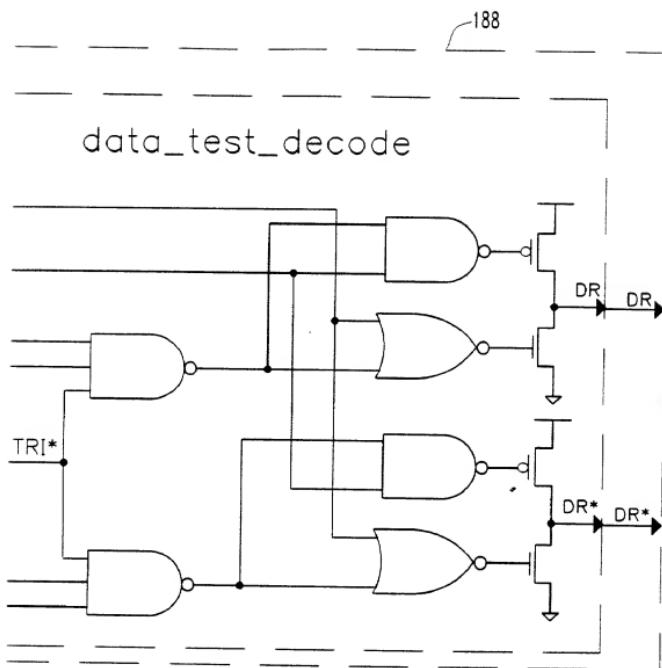
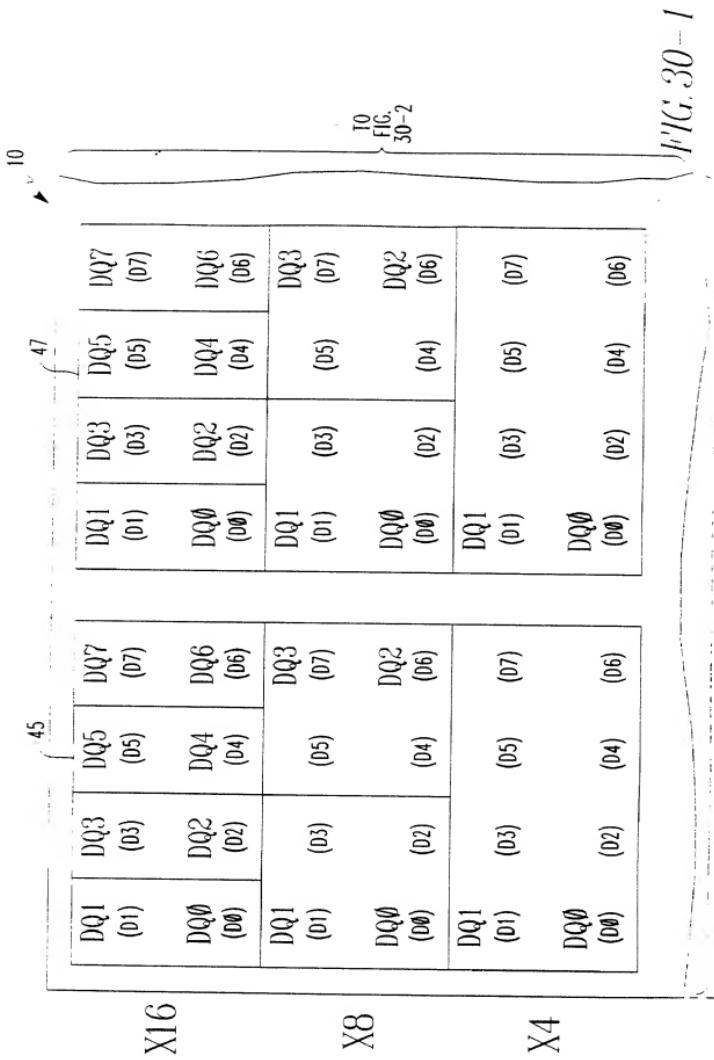


FIG. 29-2



10 FIG. 30-3

FIG. 30-1

 $\frac{T_0}{30^2}$

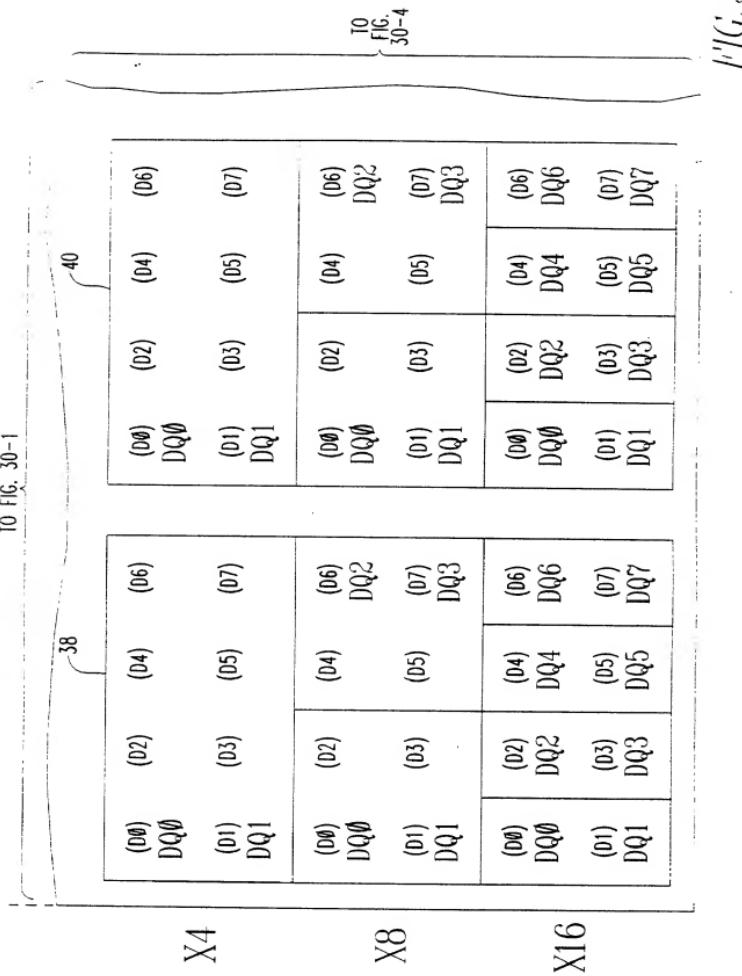
DQ9 (p1)	DQ11 (p3)	DQ13 (p5)	DQ15 (p7)	DQ9 (p1)	DQ11 (p3)	DQ13 (p5)	DQ15 (p7)
DQ8 (p0)	DQ10 (p2)	DQ12 (p4)	DQ14 (p6)	DQ8 (p0)	DQ10 (p2)	DQ12 (p4)	DQ14 (p6)
DQ5 (p1)	DQ7 (p3)	DQ7 (p5)	DQ7 (p7)	DQ5 (p1)	DQ7 (p3)	DQ7 (p5)	DQ7 (p7)
DQ4 (p0)	DQ4 (p2)	DQ4 (p4)	DQ6 (p6)	DQ4 (p0)	DQ4 (p2)	DQ6 (p4)	DQ6 (p6)
DQ3 (p1)	DQ3 (p3)	DQ5 (p5)	DQ7 (p7)	DQ3 (p1)	DQ3 (p3)	DQ5 (p5)	DQ7 (p7)
DQ2 (p0)	DQ2 (p2)	DQ4 (p4)	DQ6 (p6)	DQ2 (p0)	DQ2 (p2)	DQ4 (p4)	DQ6 (p6)

FIG.
30-1

FIG. 30-2

FIG. 30-4

TO FIG. 30-1



TO FIG. 30-2

(00)	(02)	(04)	(06)				
DQ2				(00)	(02)	(04)	(06)
(01)	(03)	(05)	(07)	DQ2			
DQ3				(01)	(03)	(05)	(07)
(00)	(02)	(04)	(06)	(00)	(02)	(04)	(06)
DQ4				DQ4			
(01)	(03)	(05)	(07)	(01)	(03)	(05)	(07)
DQ5				DQ5			
(00)	(02)	(04)	(06)	(00)	(02)	(04)	(06)
DQ8	DQ10	DQ12	DQ14	DQ8	DQ10	DQ12	DQ14
(01)	(03)	(05)	(07)	(01)	(03)	(05)	(07)
DQ9	DQ11	DQ13	DQ15	DQ9	DQ11	DQ13	DQ15

TO
FIG.
30-3

X4	VSSQ	N/C	VSSQ	N/C	N/C
X8	VSSQ	DQ3	VSSQ	DQ2	N/C
X16	VSSQ	DQ7	VSSQ	DQ6	DQ5
	1	2	3	4	5
				6	7
X16	VCCQ	DQ0	VCCQ	DQ1	DQ2
X8	VCCQ	DQ0	VCCQ	DQ1	N/C
X4	VCCQ	DQ0	VCCQ	DQ1	N/C

TO FIG. 31A2

FIG. 31A1

TO FIG. 31A1

	N/C	N/C	VSSQ	VSS	NCSV	
	N/C	N/C	VSSQ	VSS	NCSV	
VSSQ	DQ4	VSSQ	VSS	DVC2-L Probe	VCC1 Probe	
[12]	[13]	[14]	[15]	[16]	[17]	[23]
					[18]	[19]
					[20]	[21]
DQ3	VCCQ	VCCX			WE	
N/C	VCCQ	VCCX			WE	
N/C	VCCQ	VCCX			WE	

FIG. 31A2

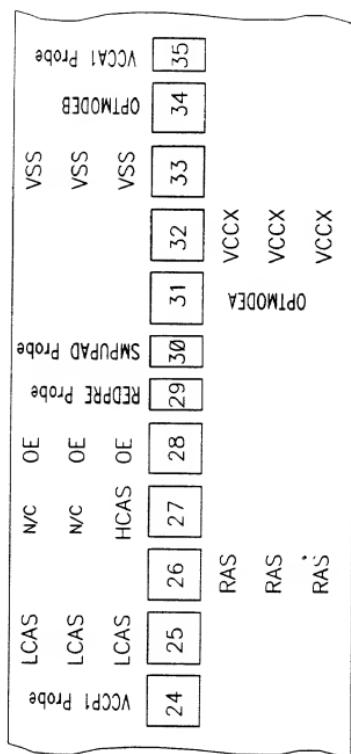


FIG. 31B1

TO FIG. 31B2

TO FIG. 31B1

A13	A12	A11	A10	A9
A13	A12	A11	A10	A9
A13	A12	A11	A10	A9
36	37	38	39	40
36	37	38	41	42
A0	A1	A2	A3	A4
A0	A1	A2	A3	A4
A0	A1	A2	A3	A4

FIG. 31B2

A8	A7	VSS	VSSQ	N/C
A8	A7	VSS	VSSQ	DQ7
A8	A7	VSS	VSSQ	DQ15
47	48	DVC2_R Probe		
47	49	DVC2_R Probe		
48	50	AVC2_V		
49	51	AVC2_V		
50	52	AVC2_V		
51	53	VCCX	VCCQ	
52	54	VCCX	VCCQ	
53	55	VCCX	VCCQ	
54	56	VCCX	VCCQ	
55	57	VCCX	VCCQ	
56	58	VCCX	VCCQ	
57	59	VCCX	VCCQ	
58	59	VCCX	VCCQ	
A6	A6	VCCQ	DQ8	VCCQ
A6	A6	VCCQ	DQ4	N/C
A6	A6	VCCQ	DQ2	N/C

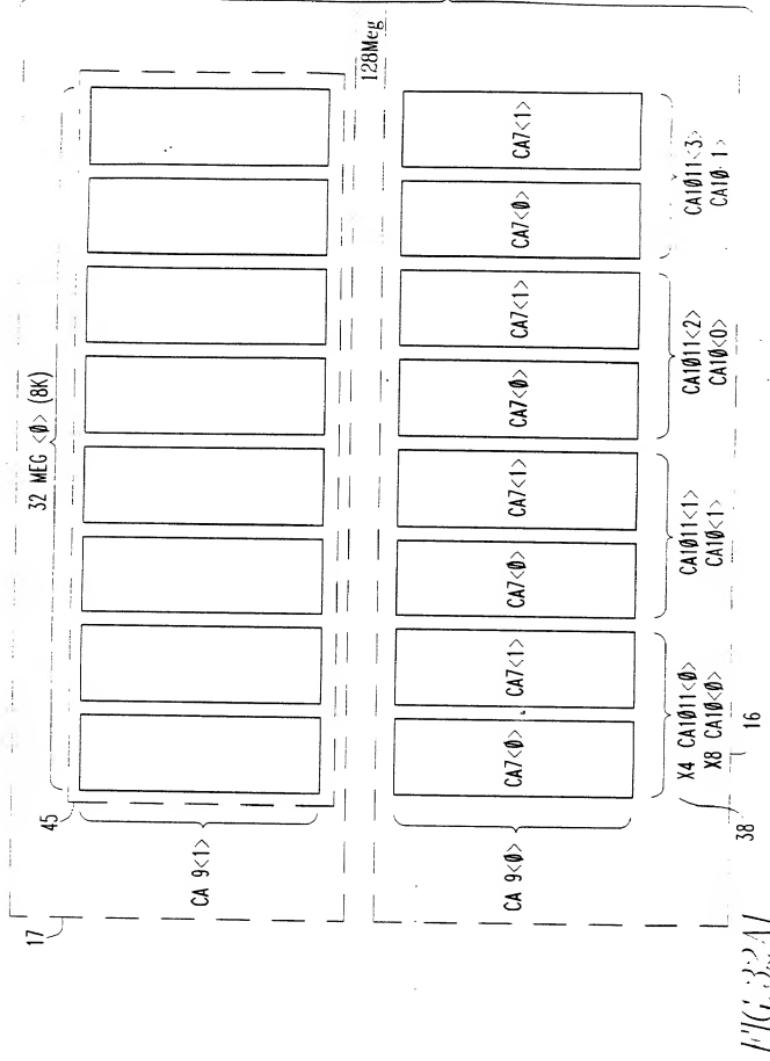
10 FIG. 31C2

FIG. 31C1

10 FIG. 31C1

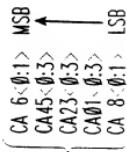
	N/C	N/C	N/C	VSSQ	N/C	VSSQ	VBB
	N/C	DQ6	N/C	VSSQ	N/C	VSSQ	VBB
VSSQ	DQ14	DQ13	VSSQ	DQ12	VSSQ	VBB	
60	61	62	63	64	65	66	67
68	69	70	71	72	73	74	75
DQ9	DQ10	VCCQ	DQ11	VCCQ	VCCQ	VBB	
DQ5	N/C	VCCQ	N/C	VCCQ	VCCQ	VBB	
DQ5'	N/C	VCCQ	N/C	VCCQ	VCCQ	VBB	

FIG. 31C2



10
FIG.
32A3

FIG. 32A2



32 MEG <1> (8K)

47

FROM
FIG.
32A1

PART TYPE	32MEG
ANY 16K	RA 13
X4 8K OR 4K	CA 12
X8 8K OR 4K	CA 11
X16 8K OR 4K	CA 10

40

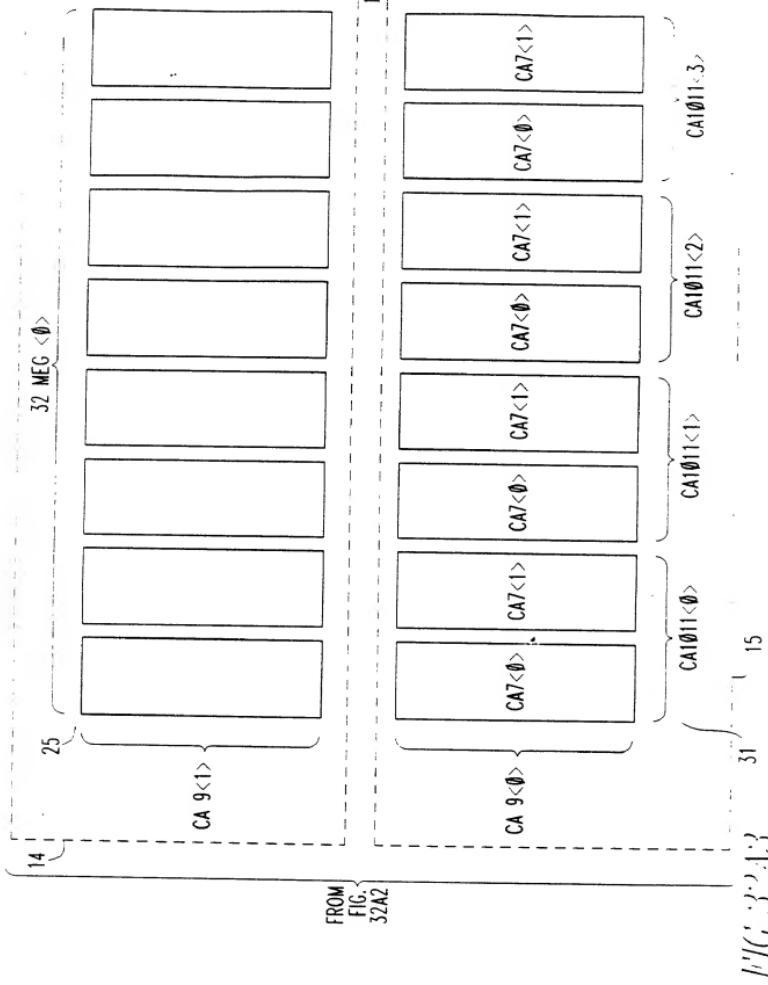
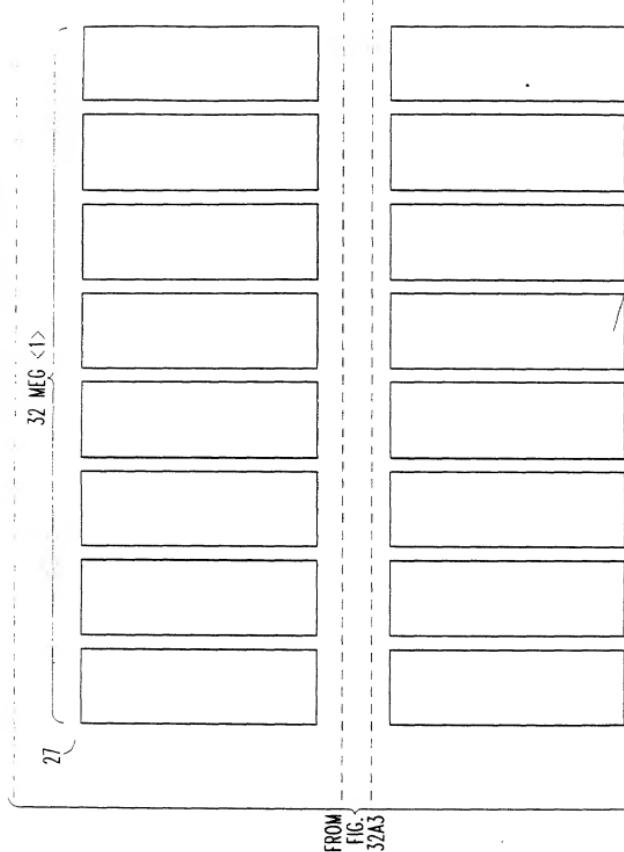


FIG. 32A4

CA 6<0:1>
 CAA5<0:3>
 CA23<0:3>
 CA01<0:3>
 CA 8<0:1>
 MSB
 LSB



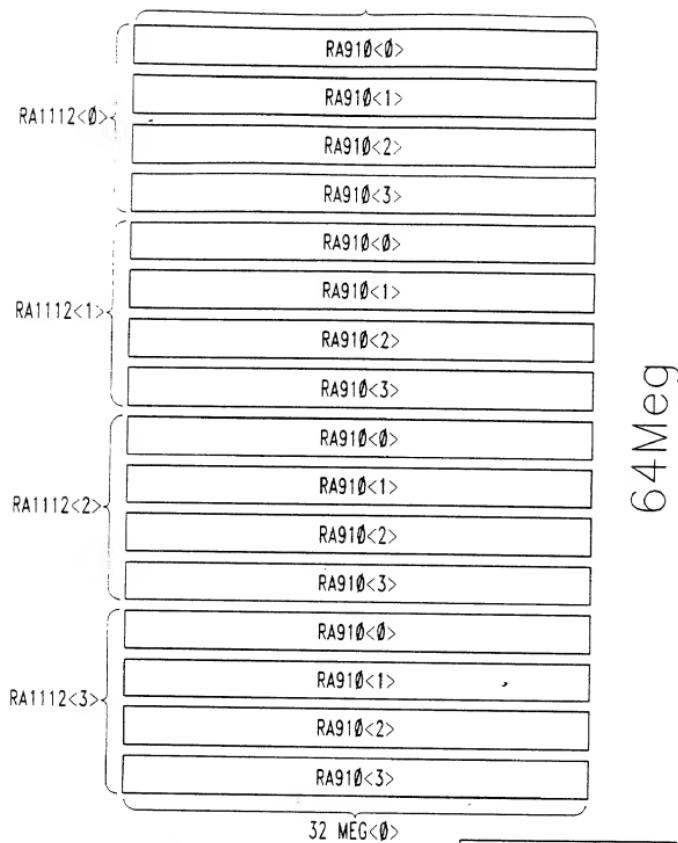


FIG. 32B1

PART TYPE	32MEG
ANY 16K	RA_13
X4 8K OR 4K	CA_12
X8 8K OR 4K	CA_11
X16 8K OR 4K	CA_10

0282950-93389-0082950

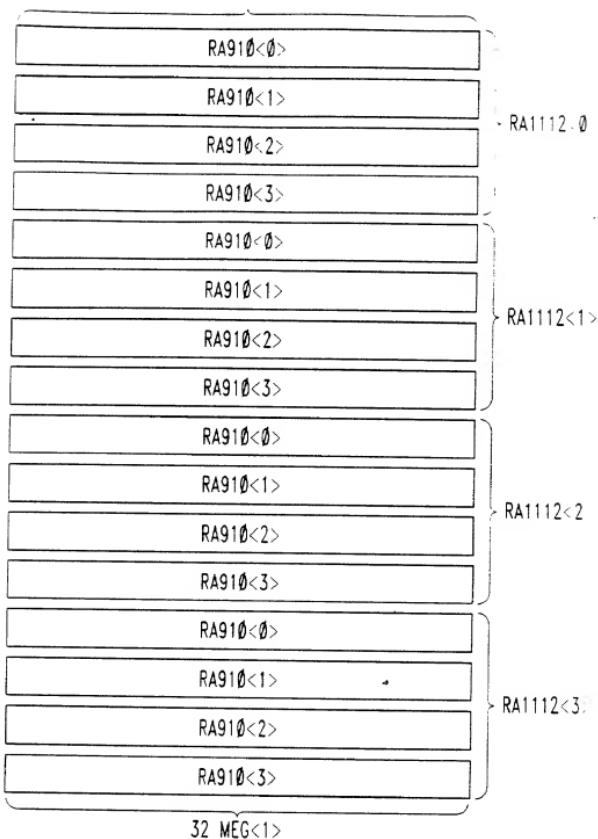
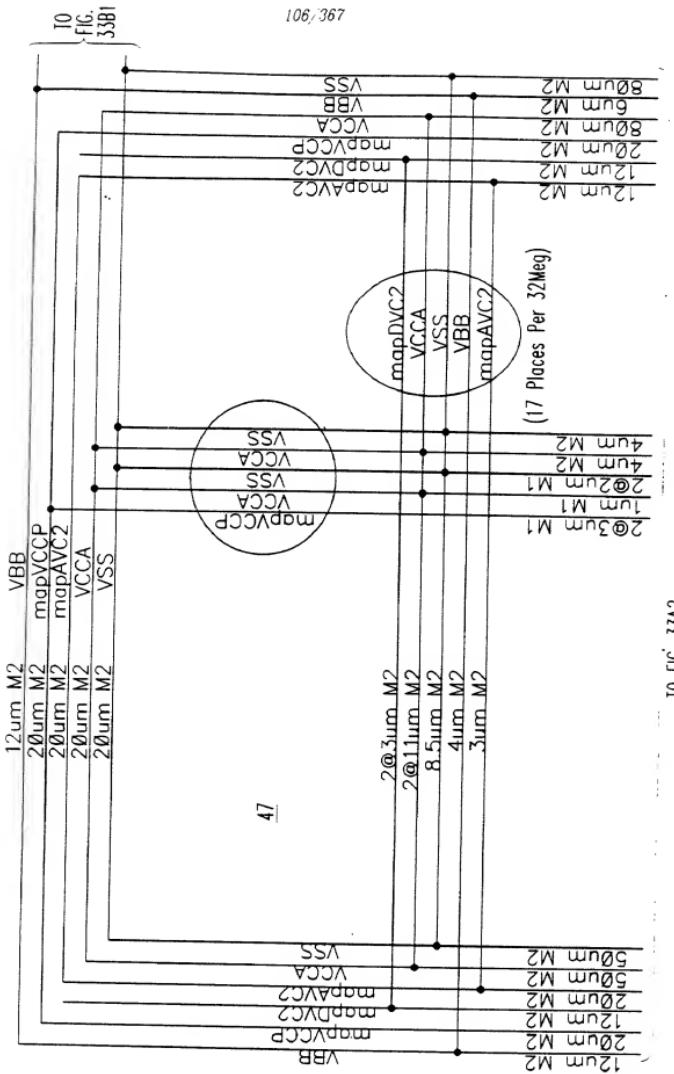
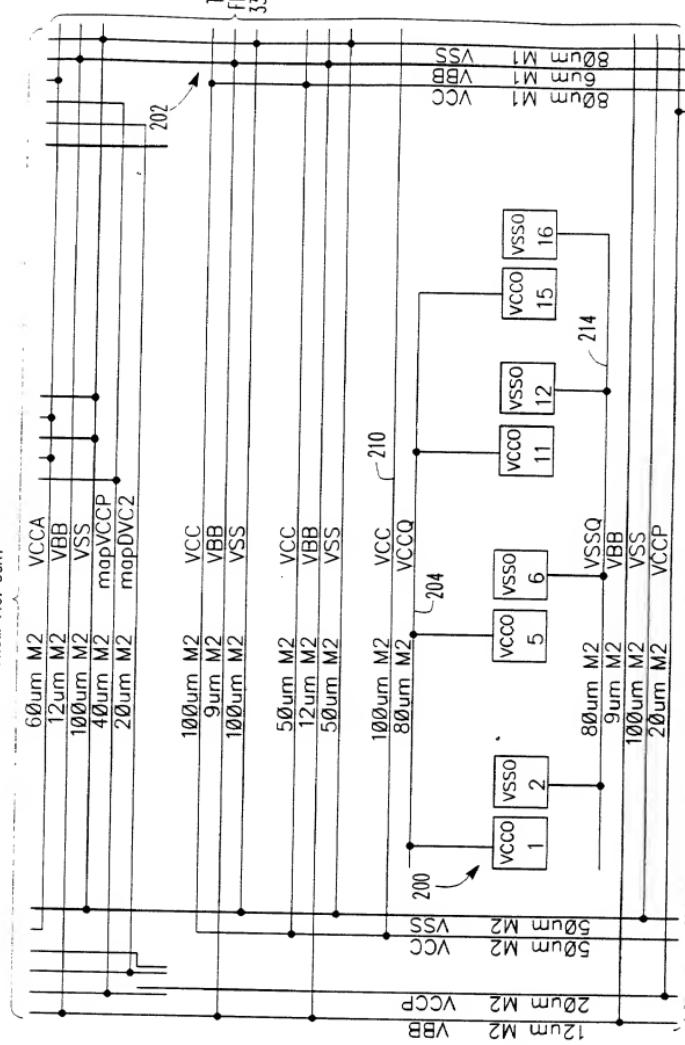


FIG. 32B2



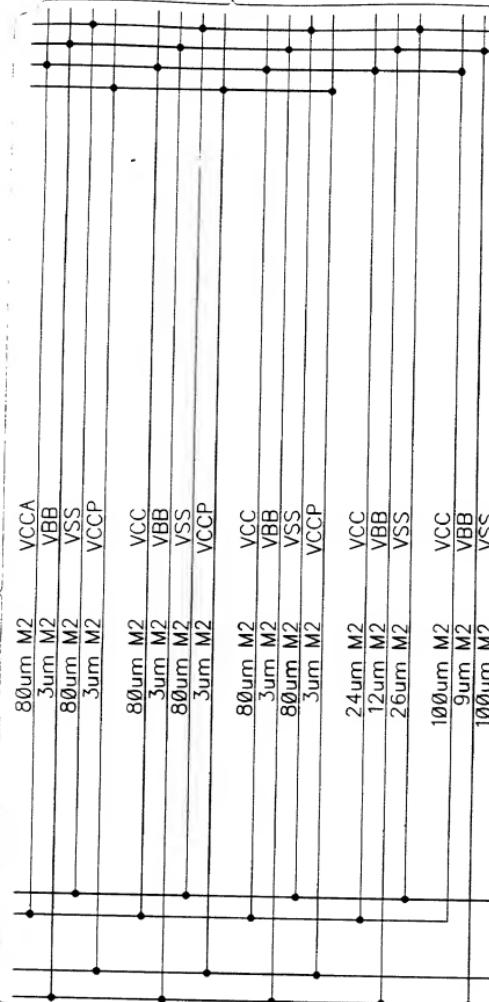
FROM FIG. 33A1



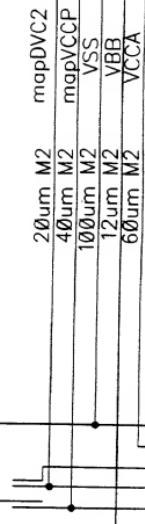
TO FIG. 33A3

107/367

FROM FIG. 33A2



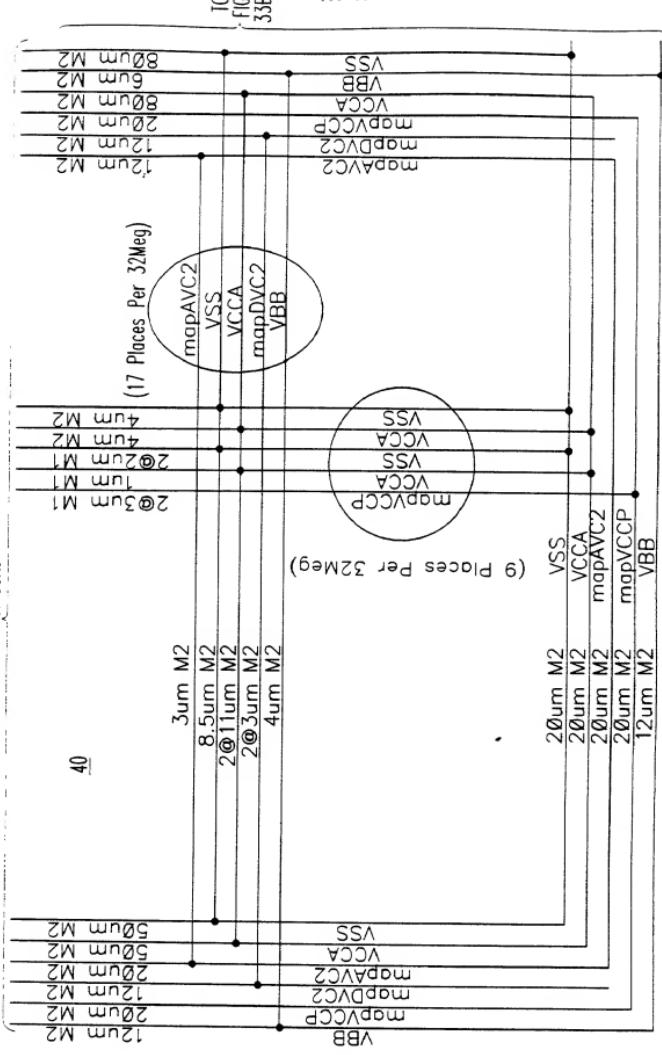
TO FIG. 33B5
 108, 367



TO FIG. 33A4

$\mu/(G \cdot R_L)$

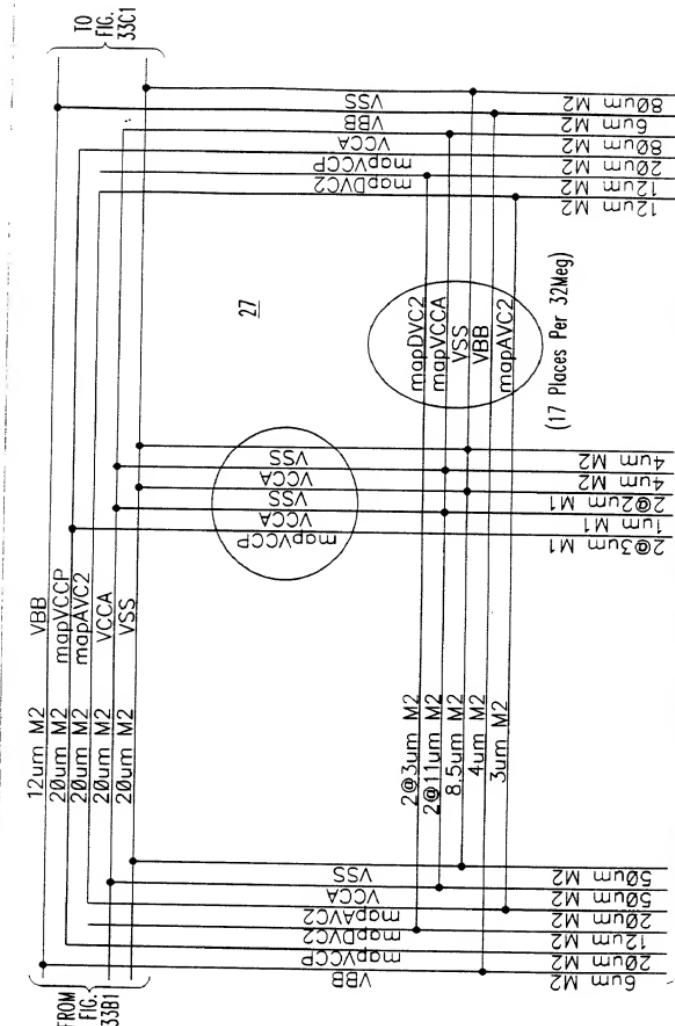
FROM FIG. 33A



17 367 3387

110.367

111.367



1171 G. '3313'.

FIG. 33B4

卷之三

FROM FIG. 33B1

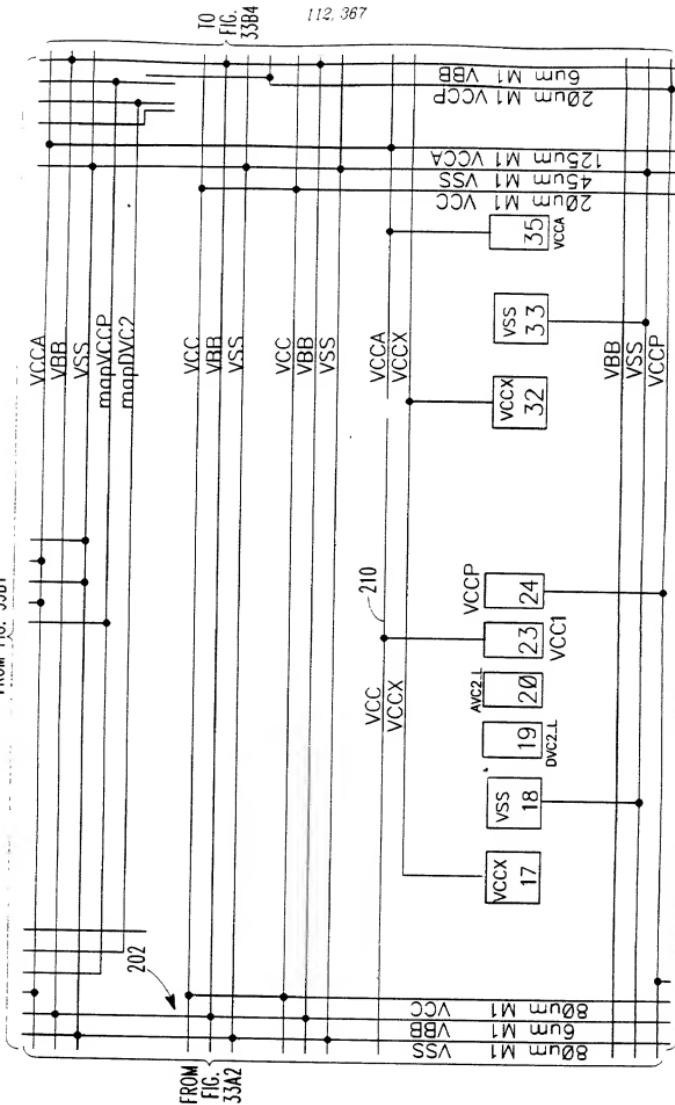


FIG. 33B3

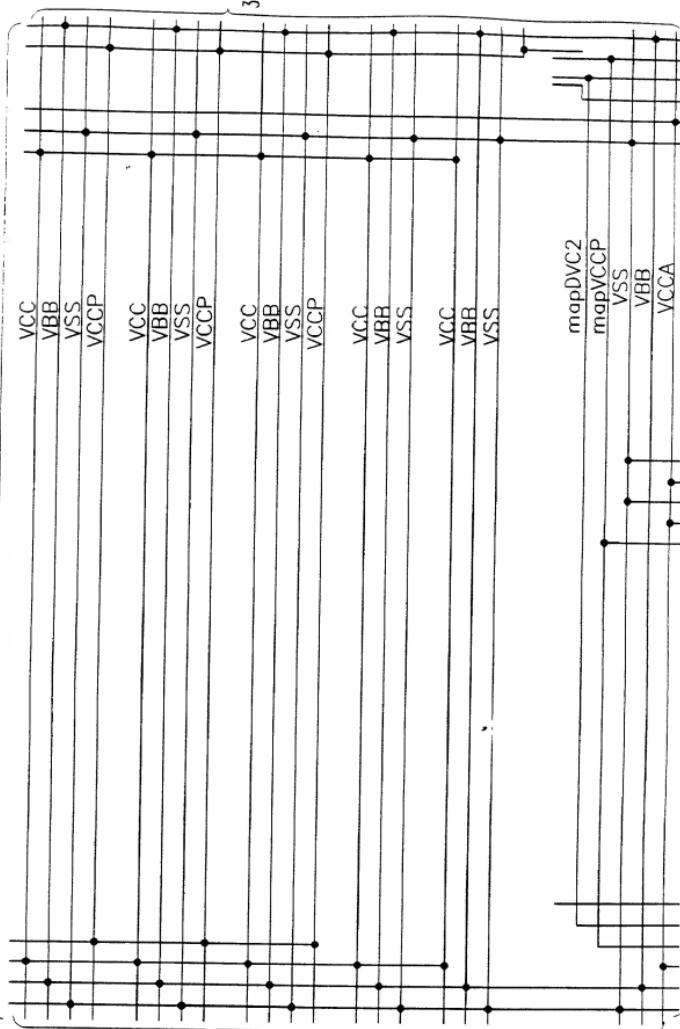
117

113, 367

FROM FIG. 33B2

FIGURE 33B3 * FIGURE 33B5

FROM FIG. 33B3

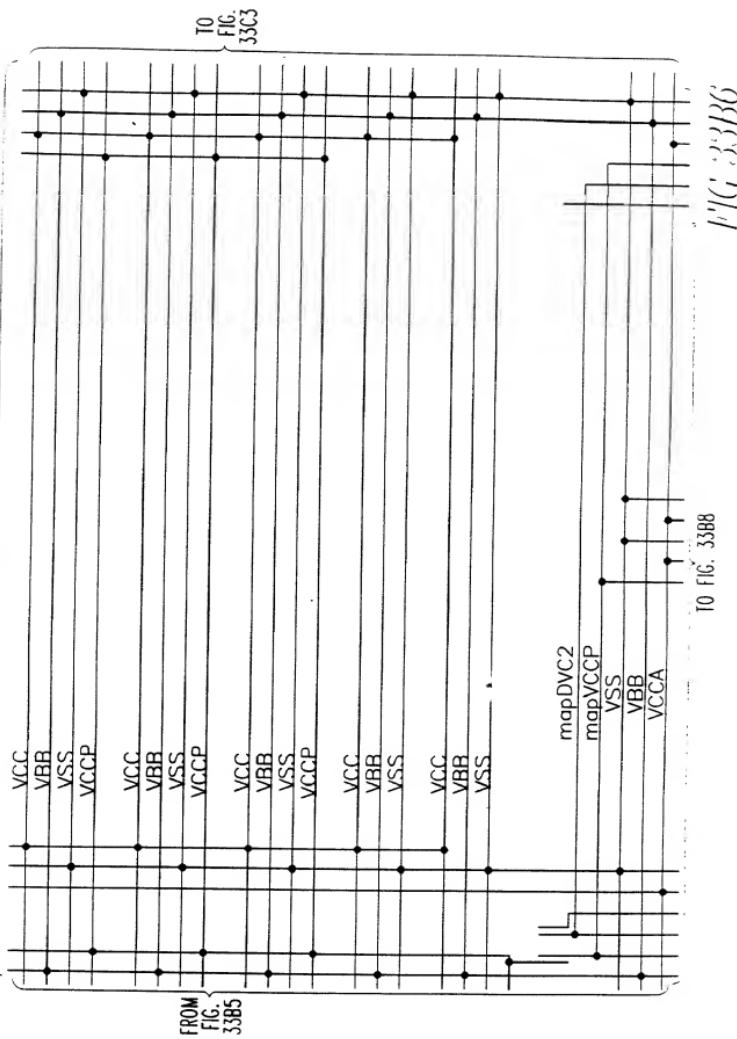


TO FIG. 33B7

114/33335

TOESEON® 63366666

FROM FIG. 33B4



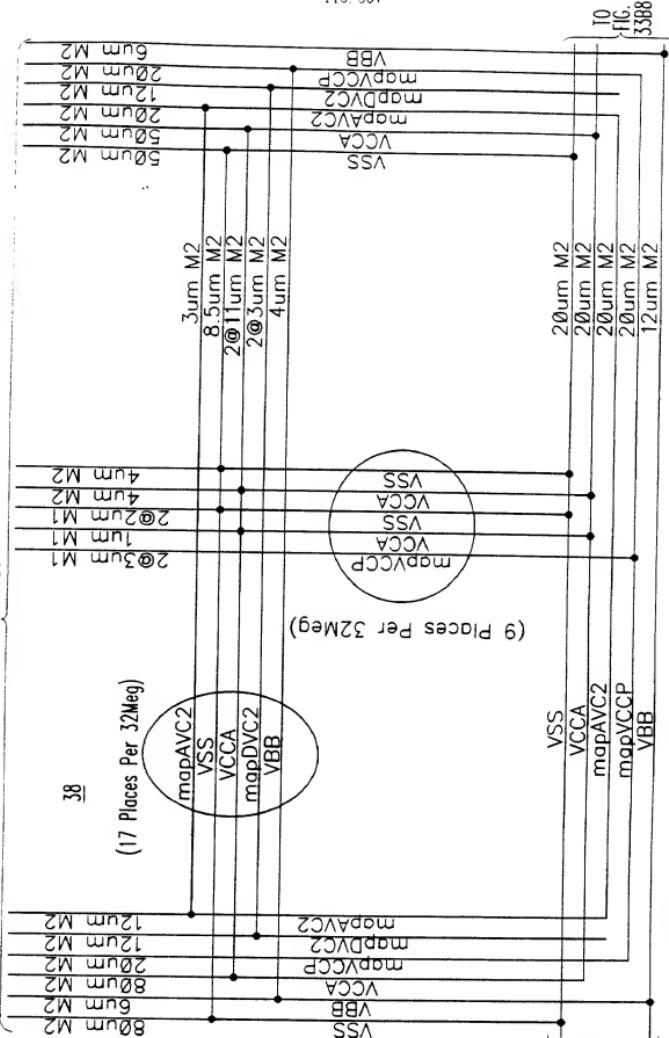
116, 367

FROM FIG. 3385

38

(17 Places Per 32Meg)

(9 Places Per 32Meg)

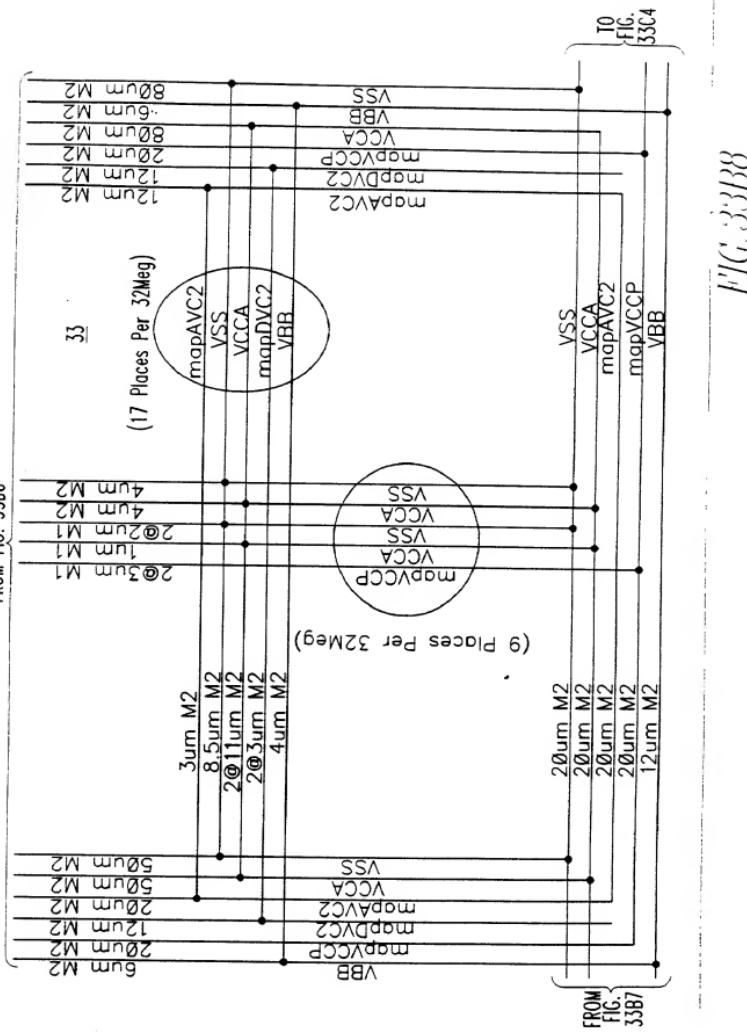


FROM
FIG.
33A4

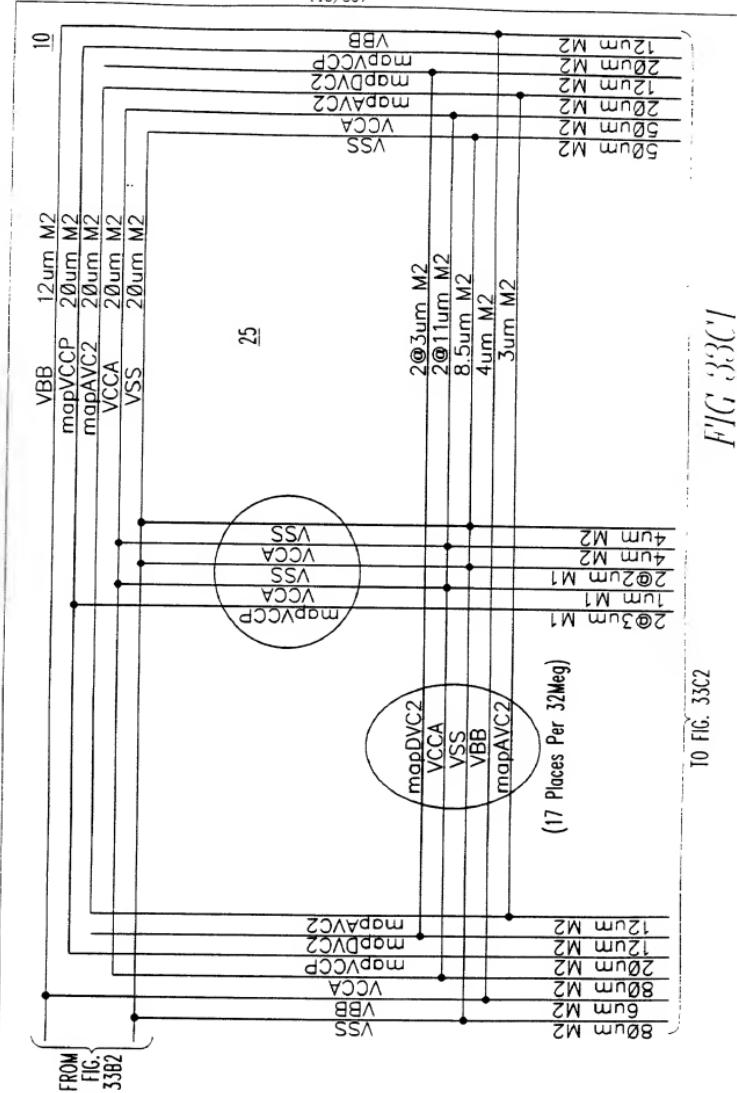
117

117:367

FROM FIG. 3386

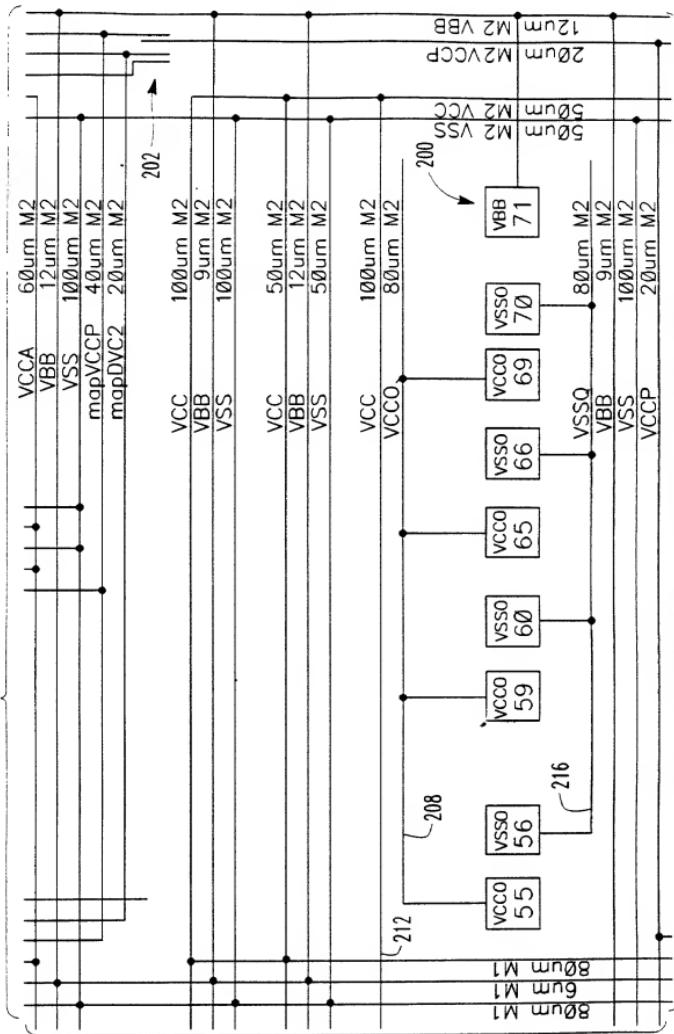


卷之三



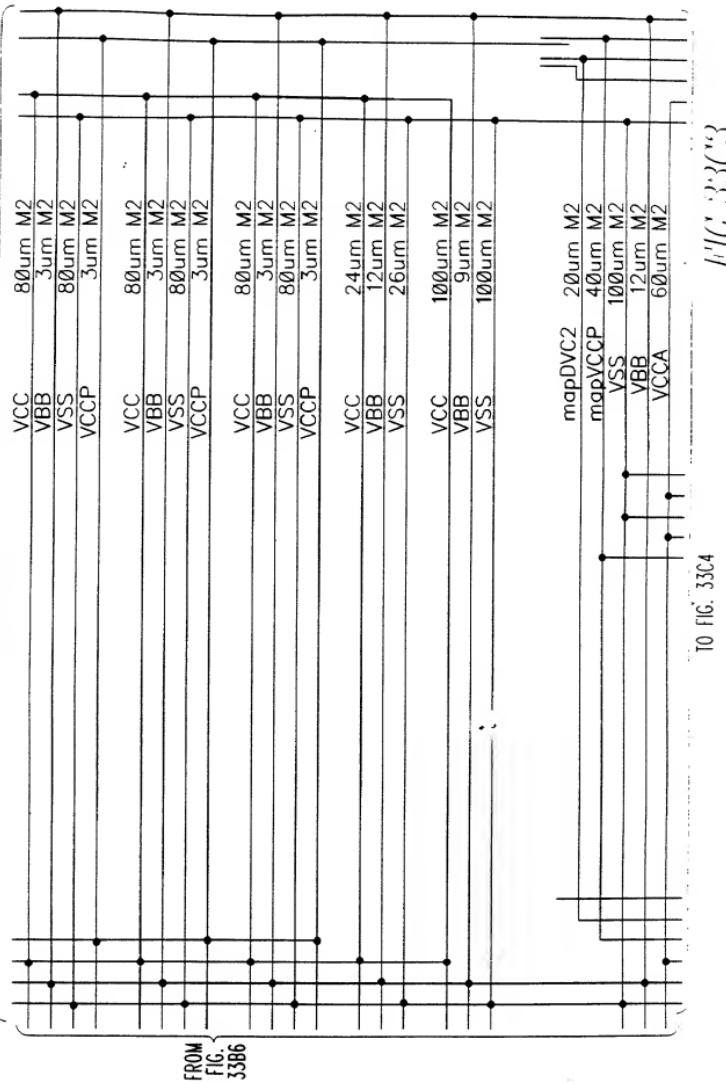
FROM FIG. 33C1

FROM
FIG.
33B3



10 FIG. 33C3

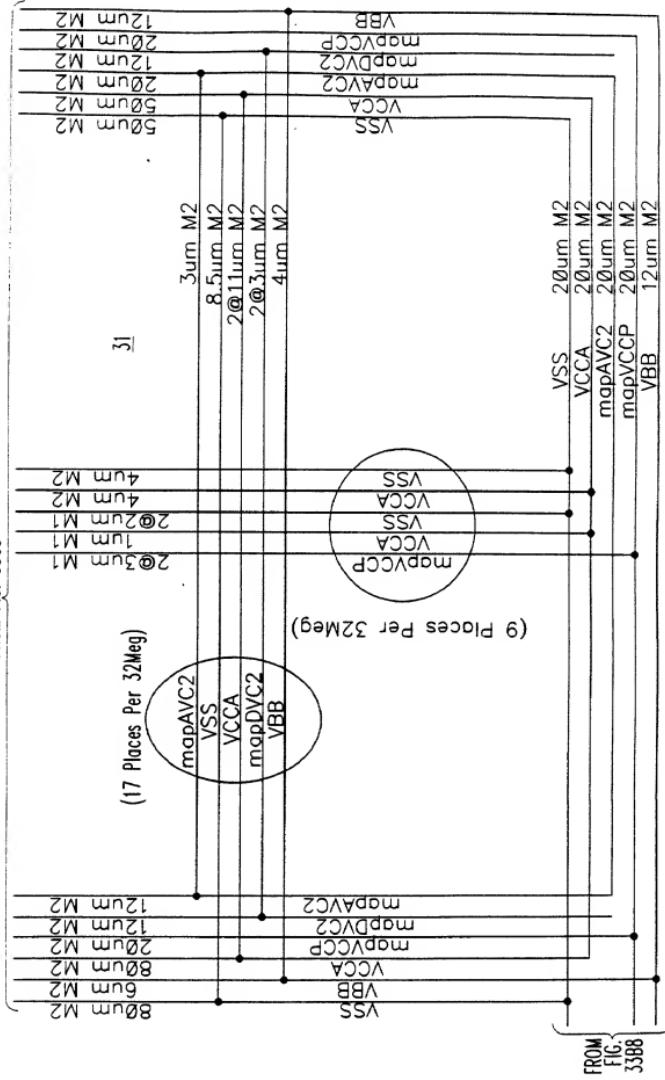
FROM FIG. 33C2



TO FIG. 33C4

卷之三

FROM FIG. 33C3



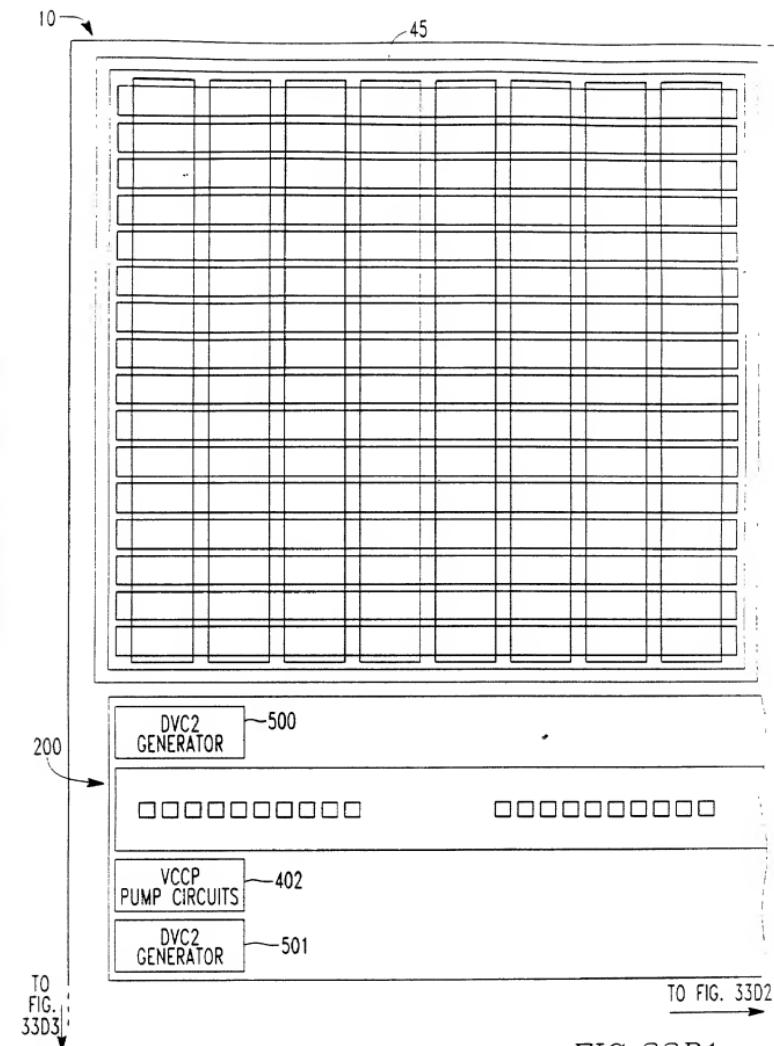


FIG. 33D1

TO FIG. 33D1

47

TDS250-63555863

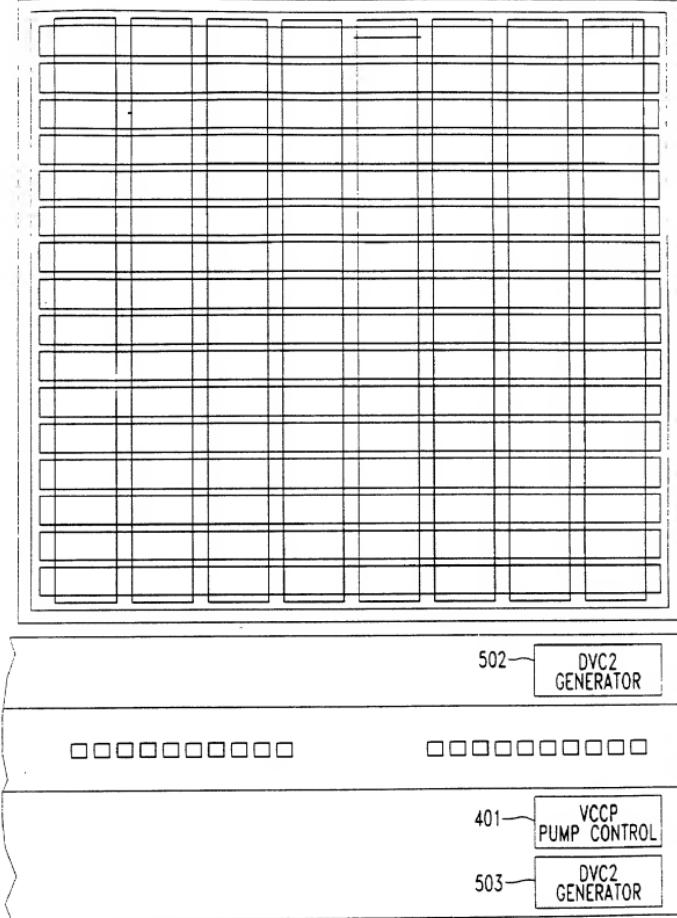


FIG. 33D2

TO
FIG.
▼33D4

TO
FIG.
33D1

(SEE FIG. 33E1)

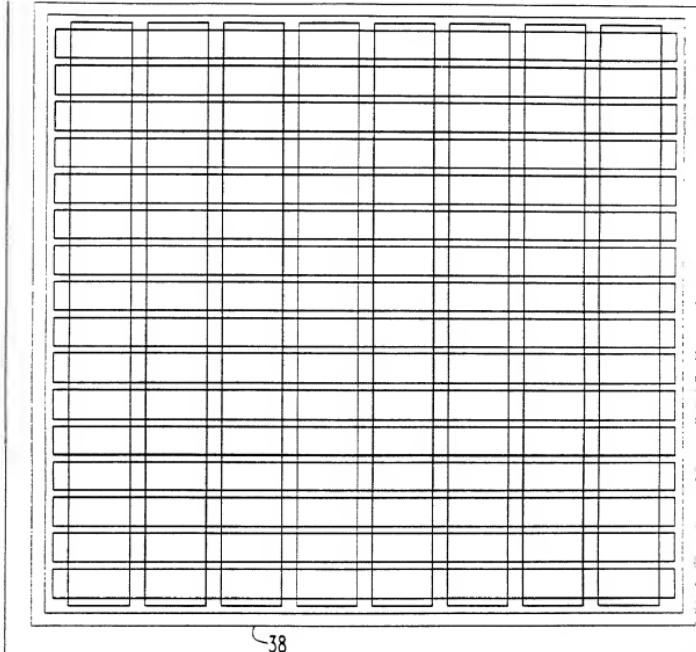
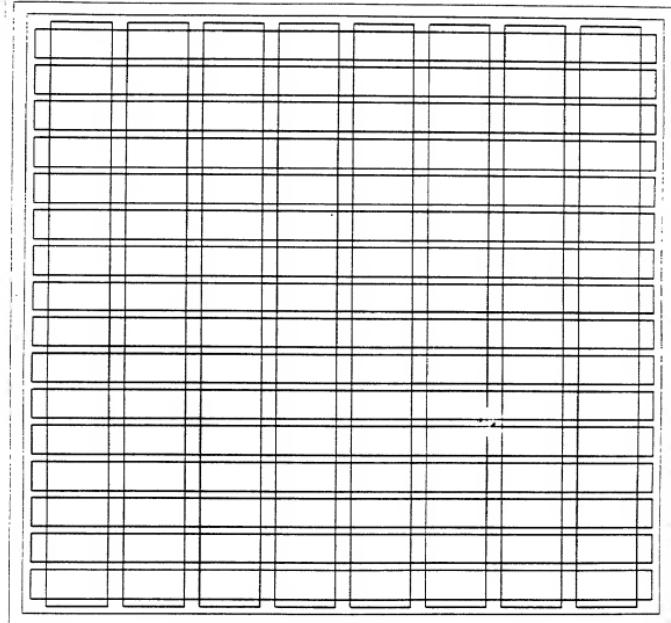


FIG. 33D3

125, 367

TO FIG. 33D2

TO FIG. 33D2



40

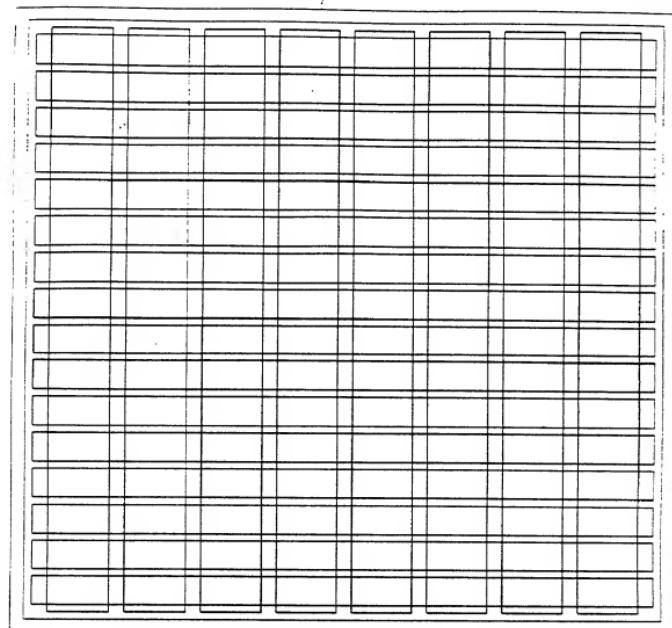
TO FIG. 33D3

FIG. 33D4

(SEE FIG. 33D2)

25

TO FIG. 33E2



DVC2
GENERATOR



VCCP
REGULATOR

DVC2
GENERATOR

TO
FIG.
33E3

FIG. 33E1

TO FIG. 33E1

27

10

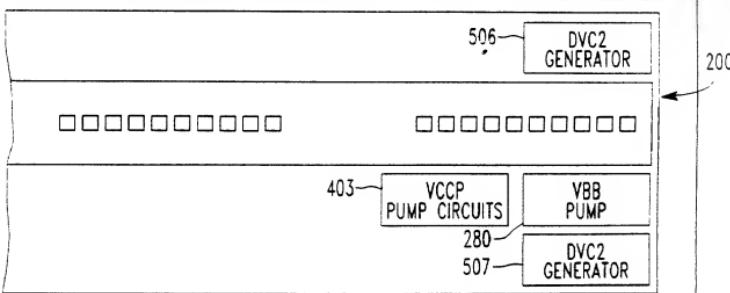
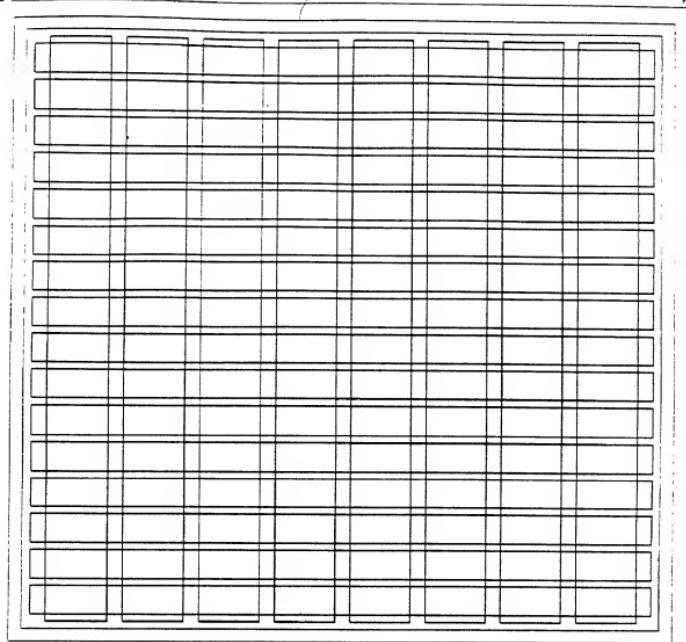
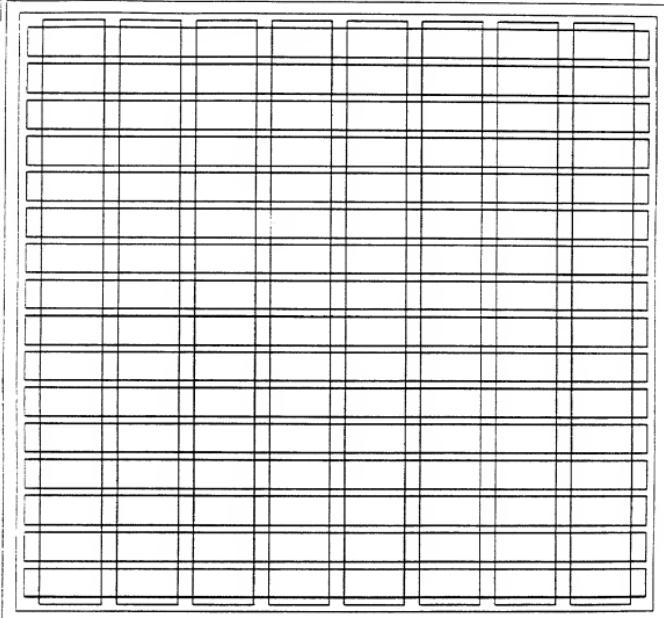


FIG. 33E2

TO
FIG.
33E4

TO
FIG.
33E1

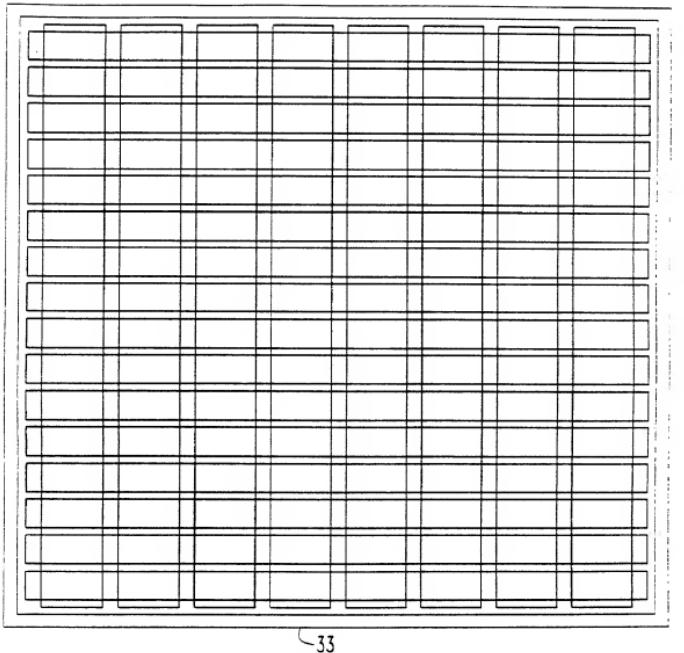


31

TO FIG. 33E4

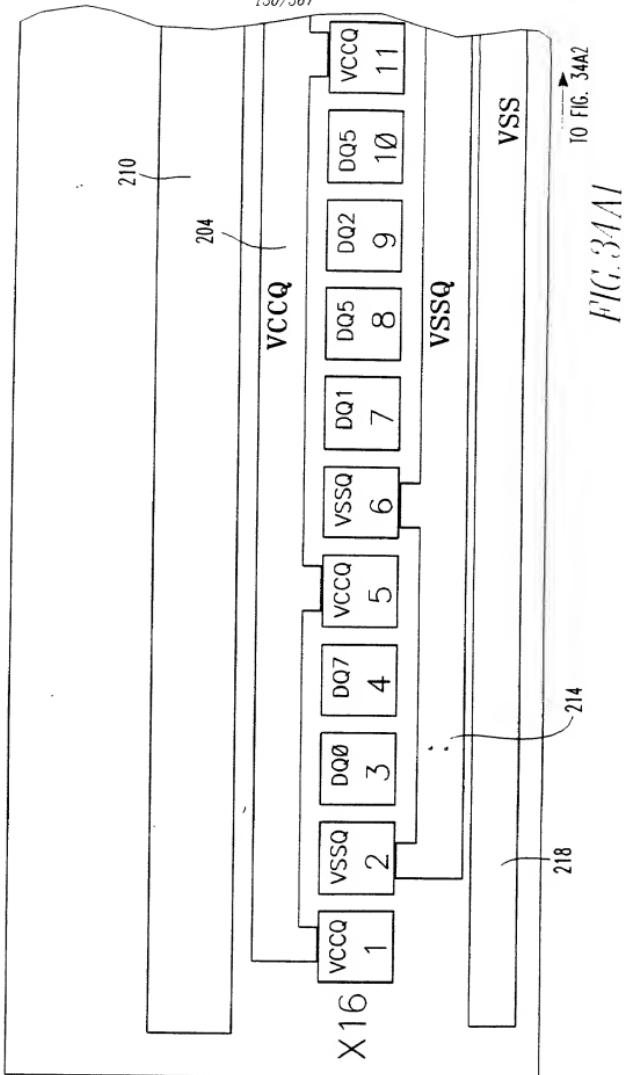
FIG. 33E3

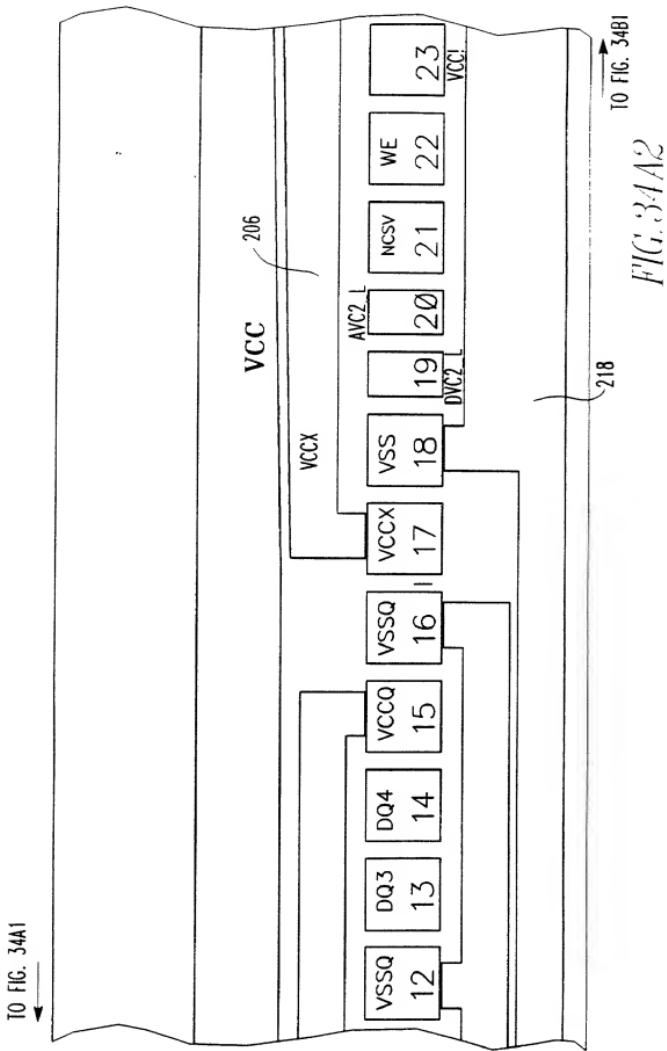
卷之三

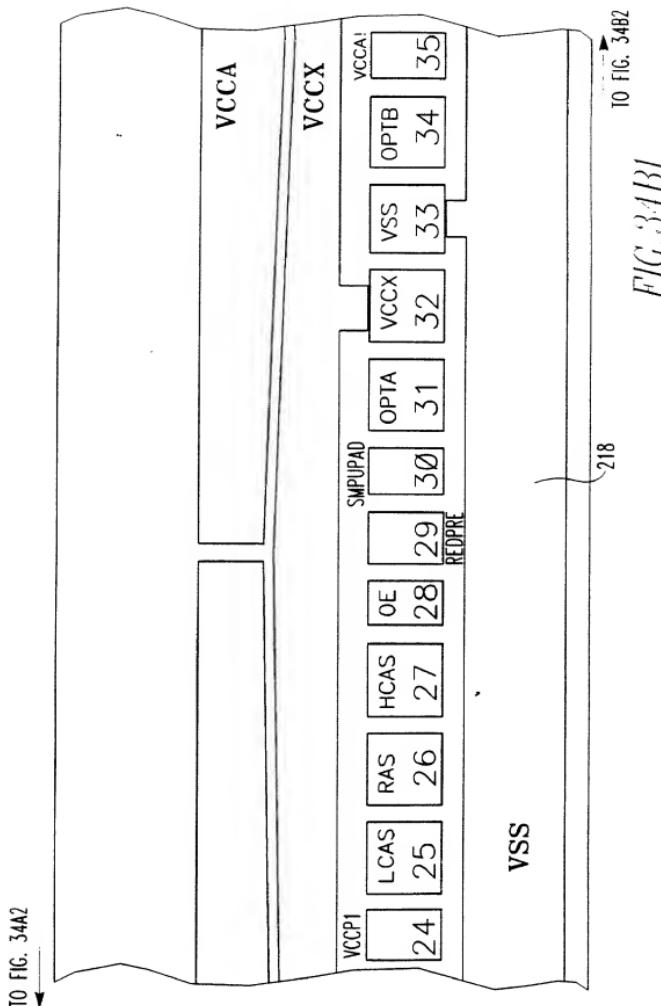


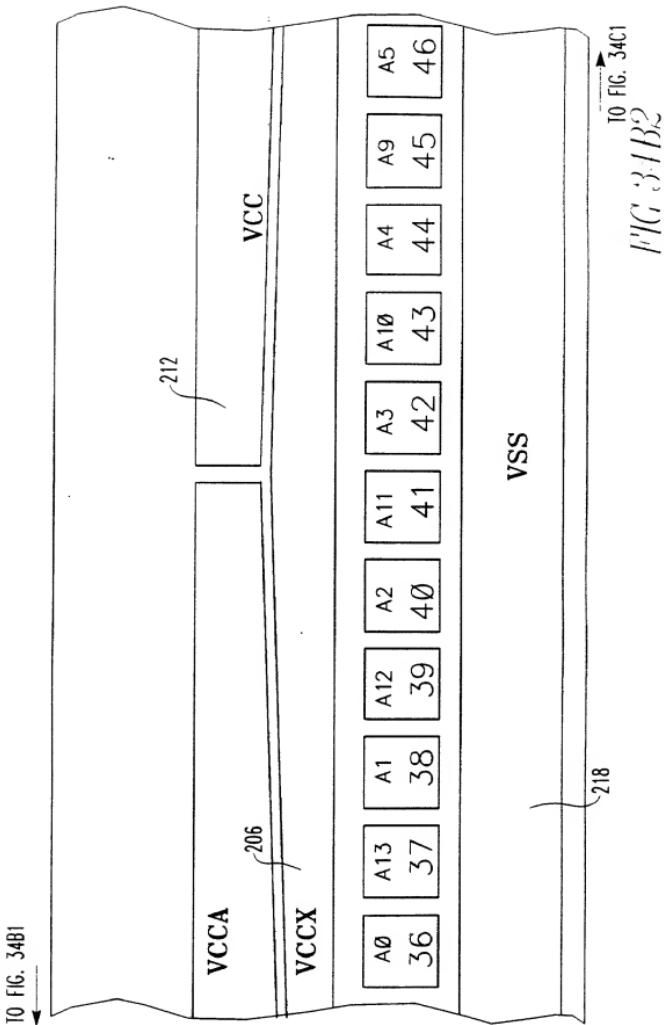
TO FIG. 33E3

FIG. 33E4

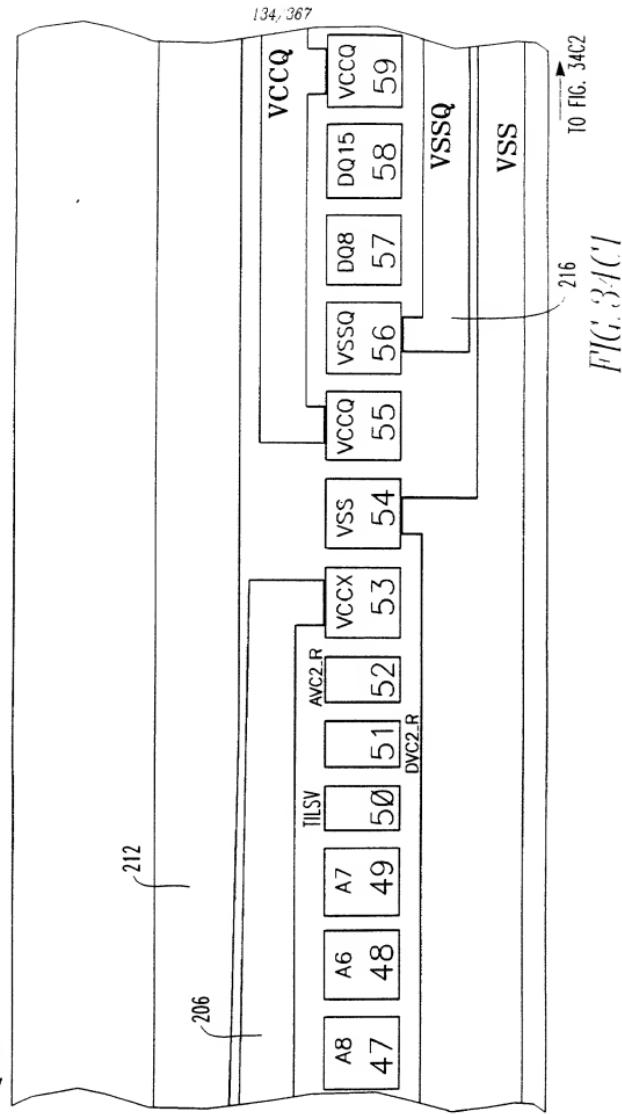




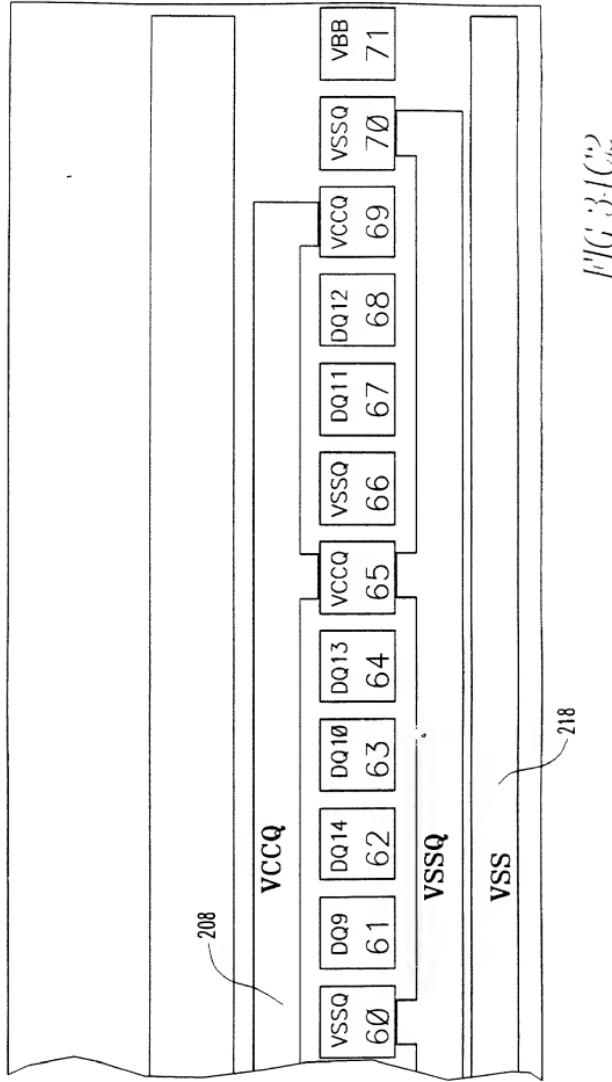


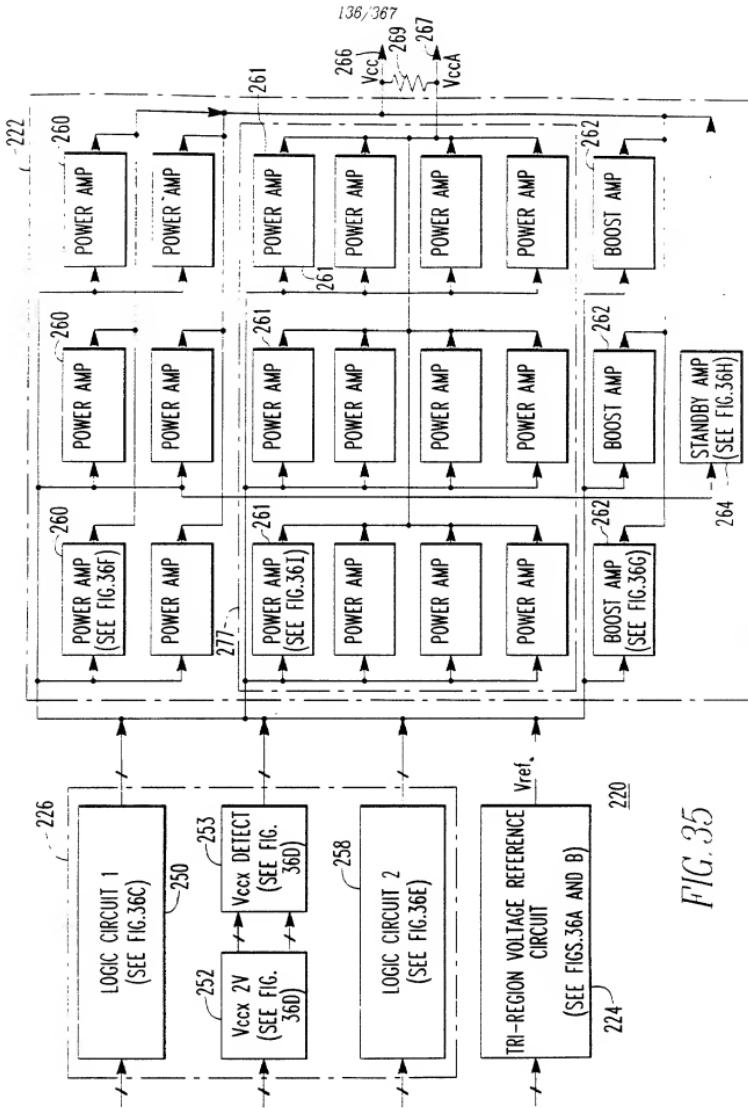


To FIG. 34B2



10 FIG. 34C1





F/G. 35

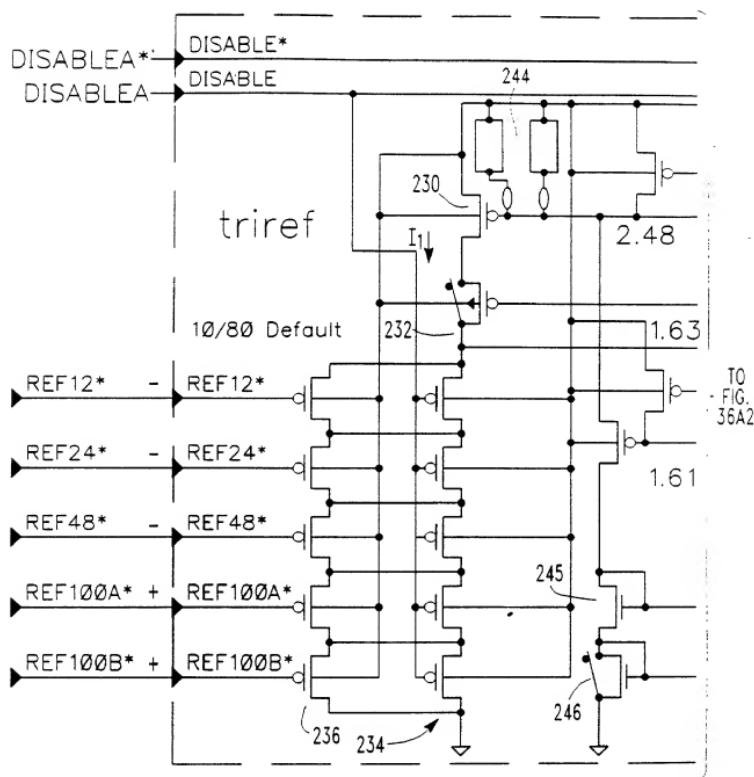


FIG. 36A1

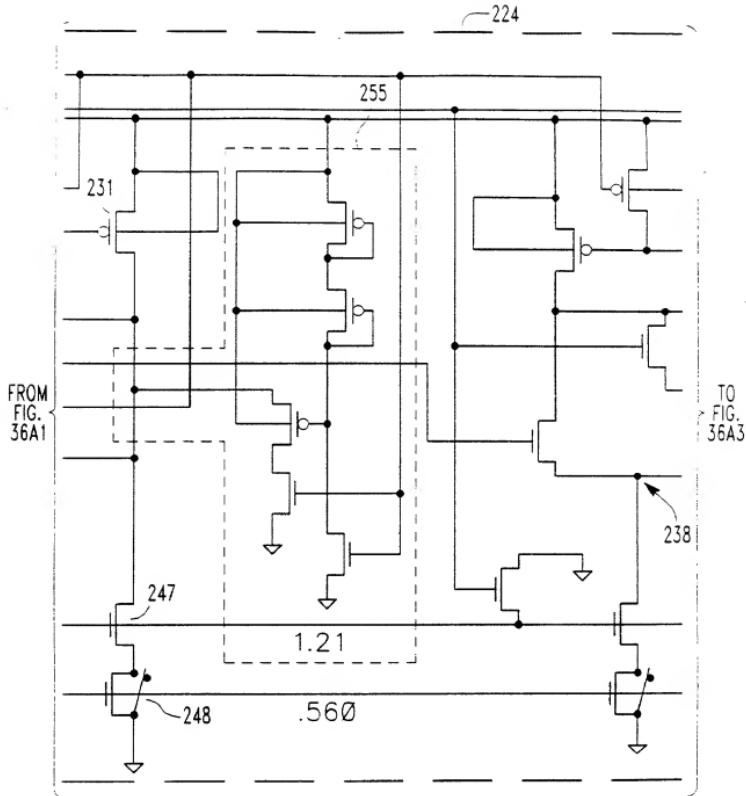


FIG. 36A2

TO FIGS. 36C, D, E, F, G & H

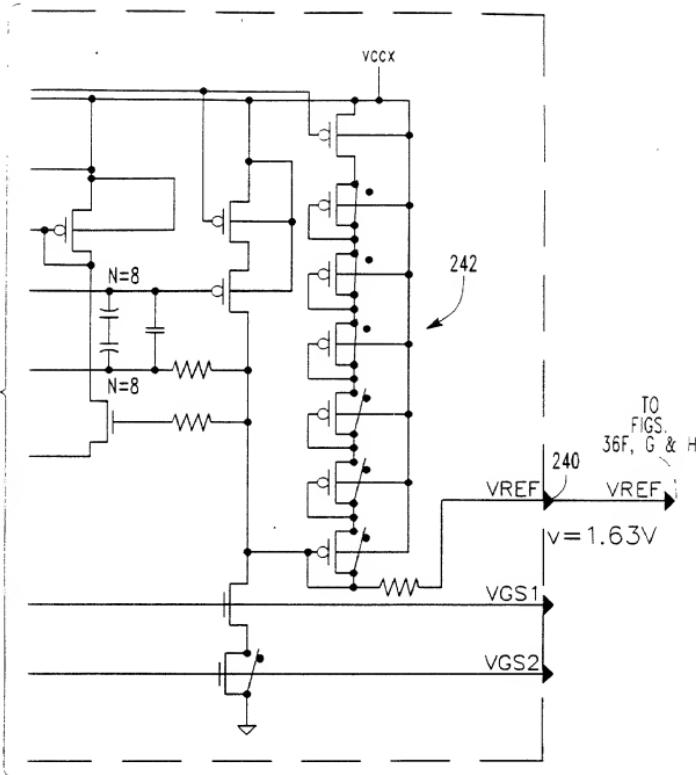
FROM
FIG.
36A2

FIG. 36A3

TELETYPE RECORD

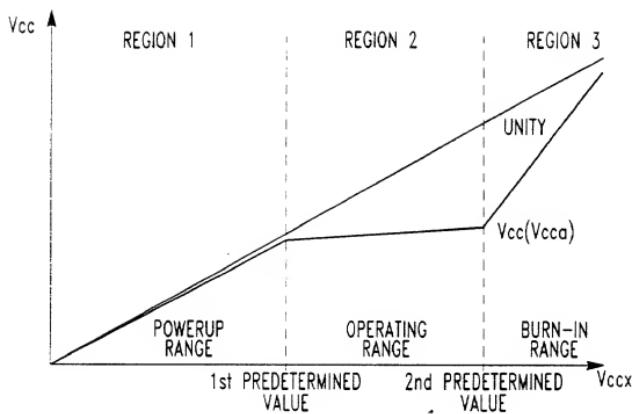


FIG. 36B

SEL32M<0:7>

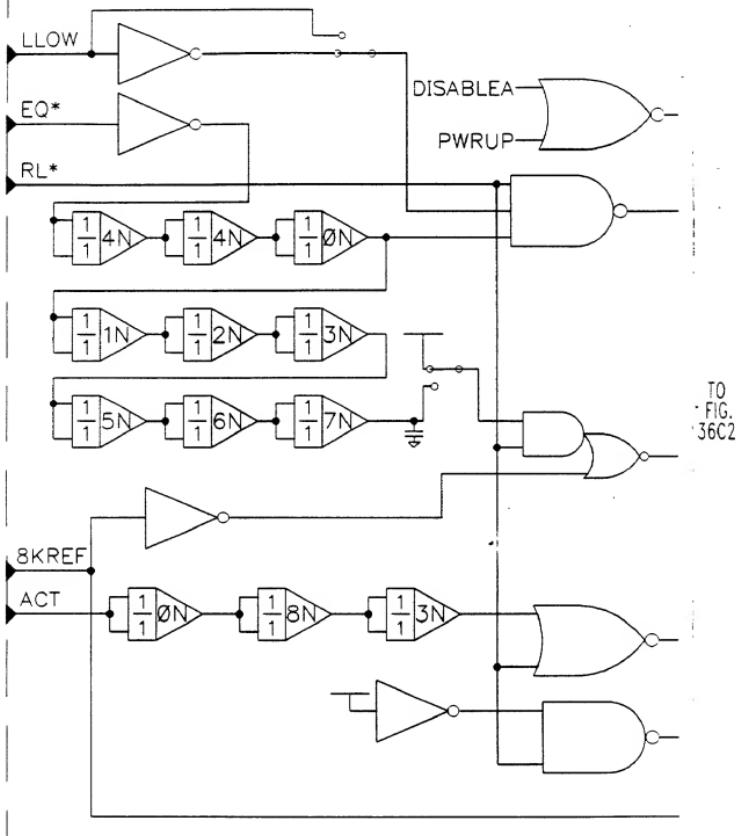


FIG. 36C1

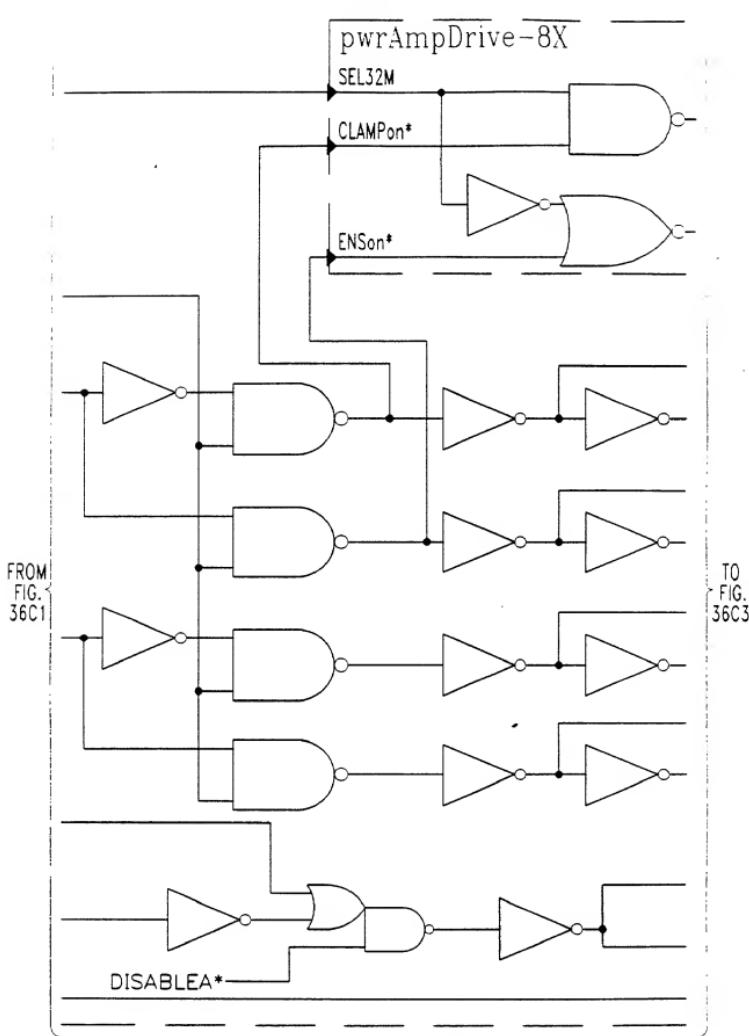


FIG. 36C2

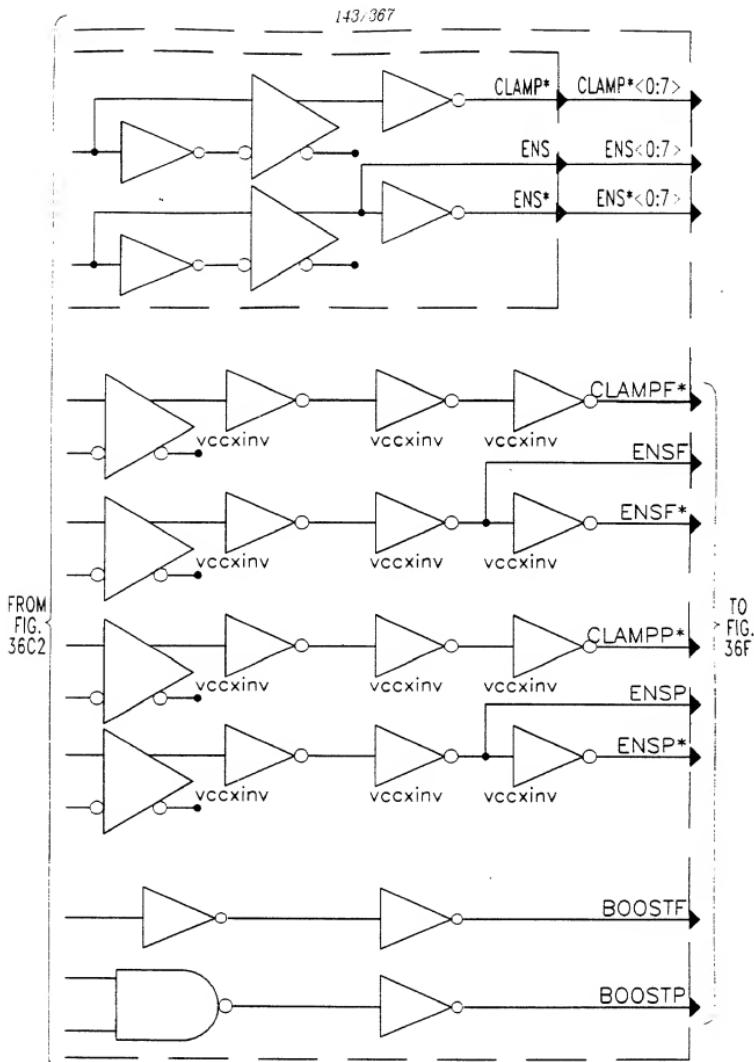
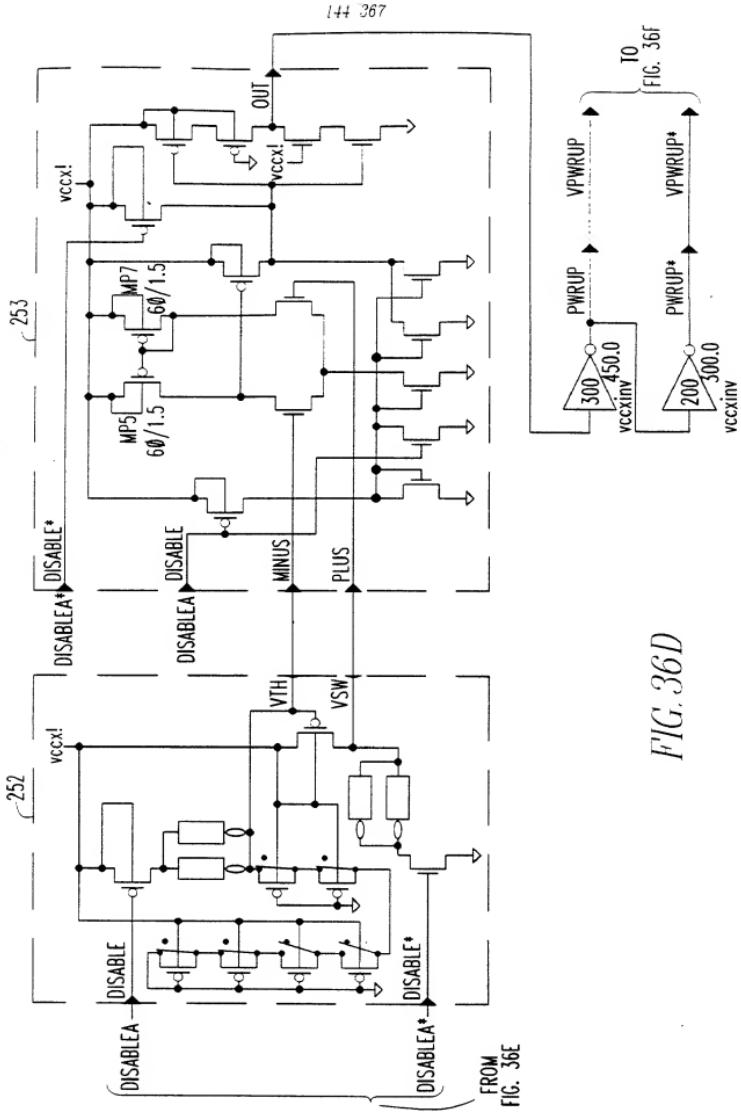
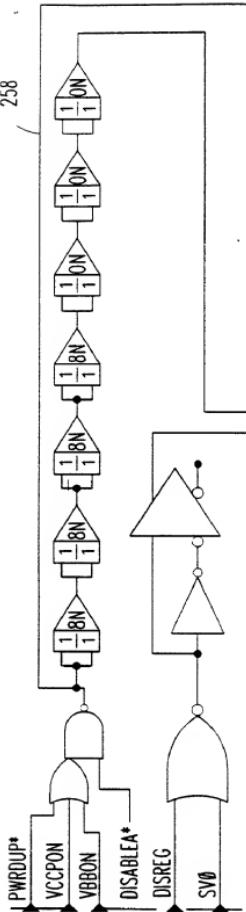


FIG. 36C3



258



10 FIGS.

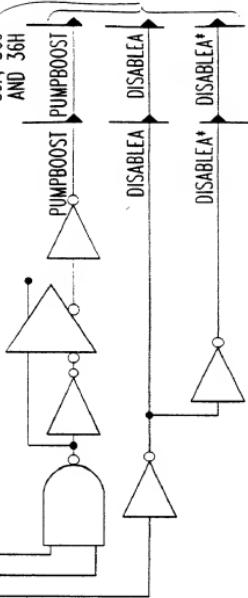
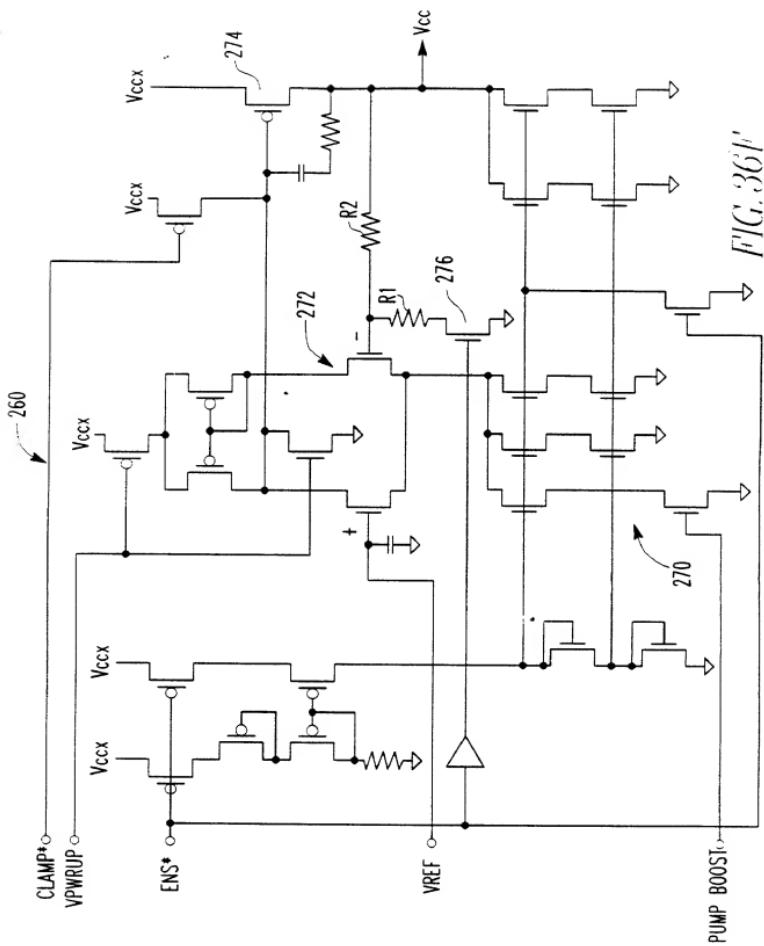
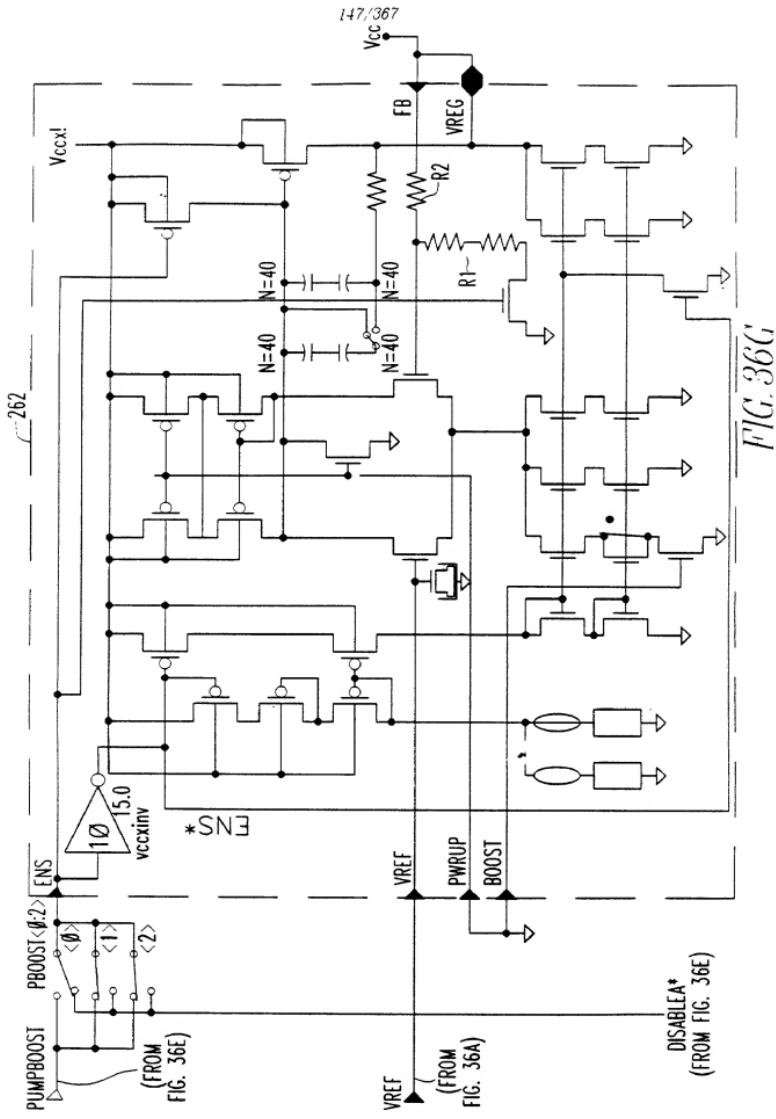
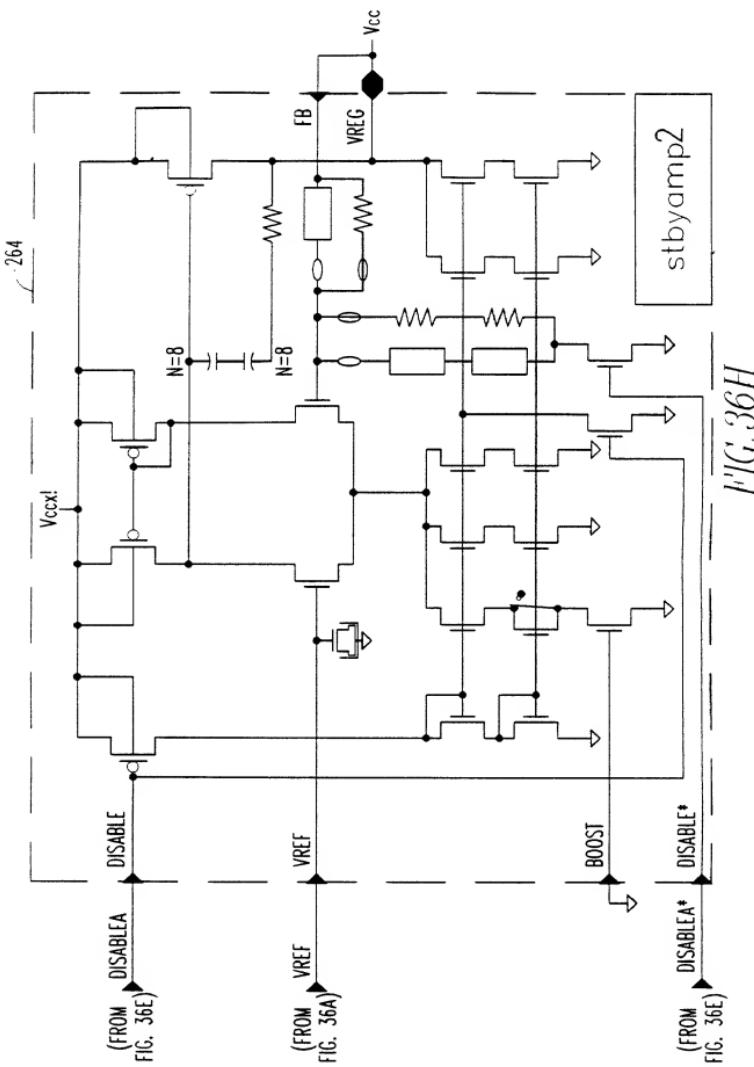
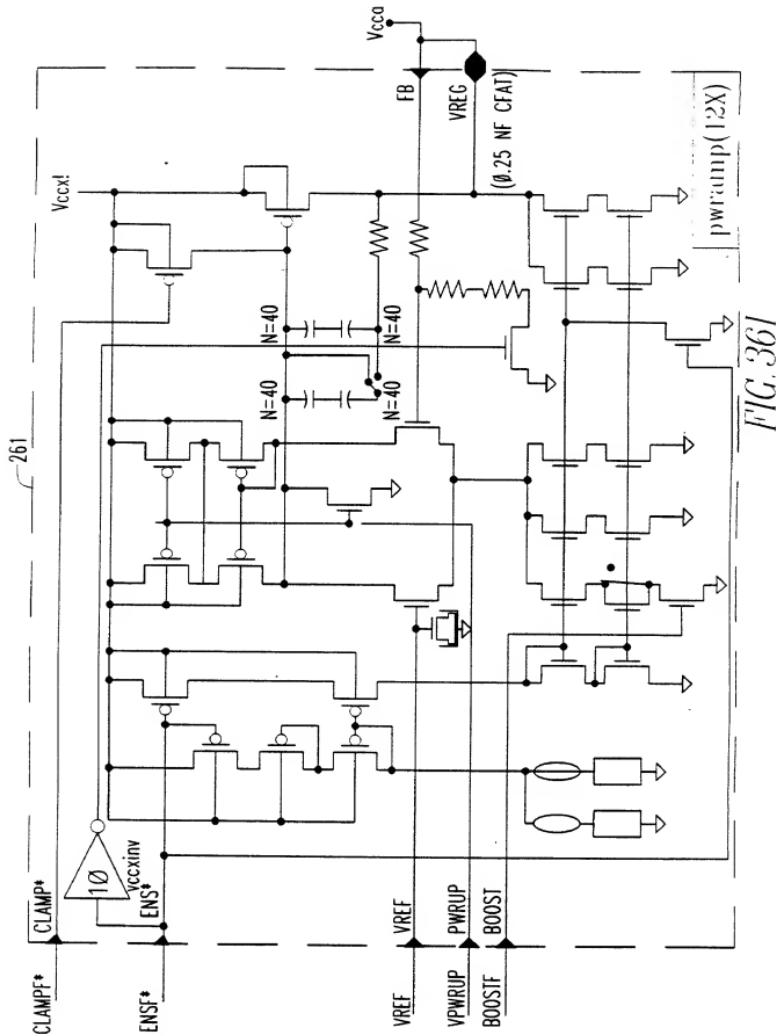


FIG. 36E









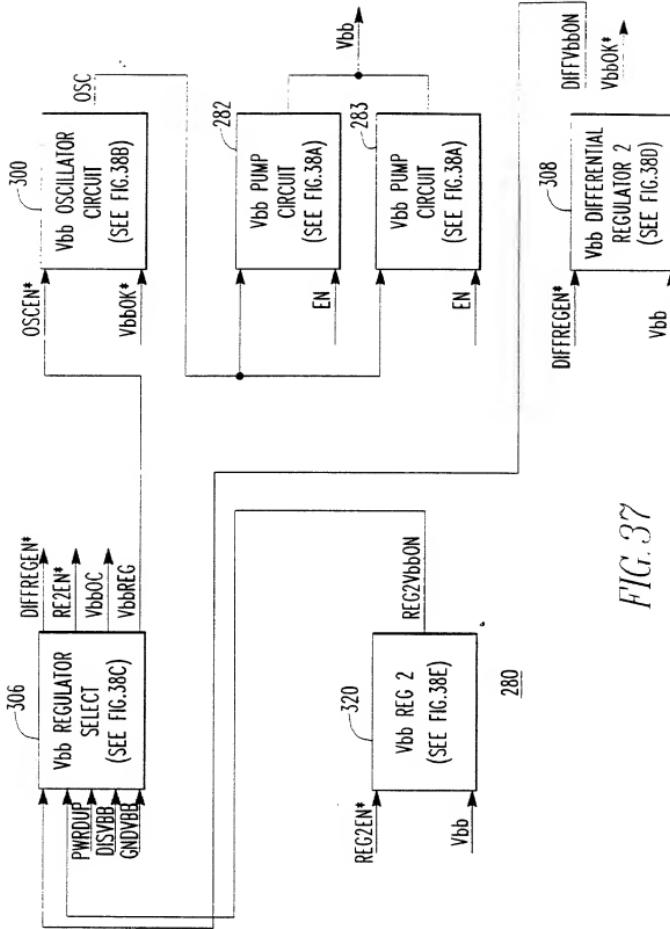


FIG. 37

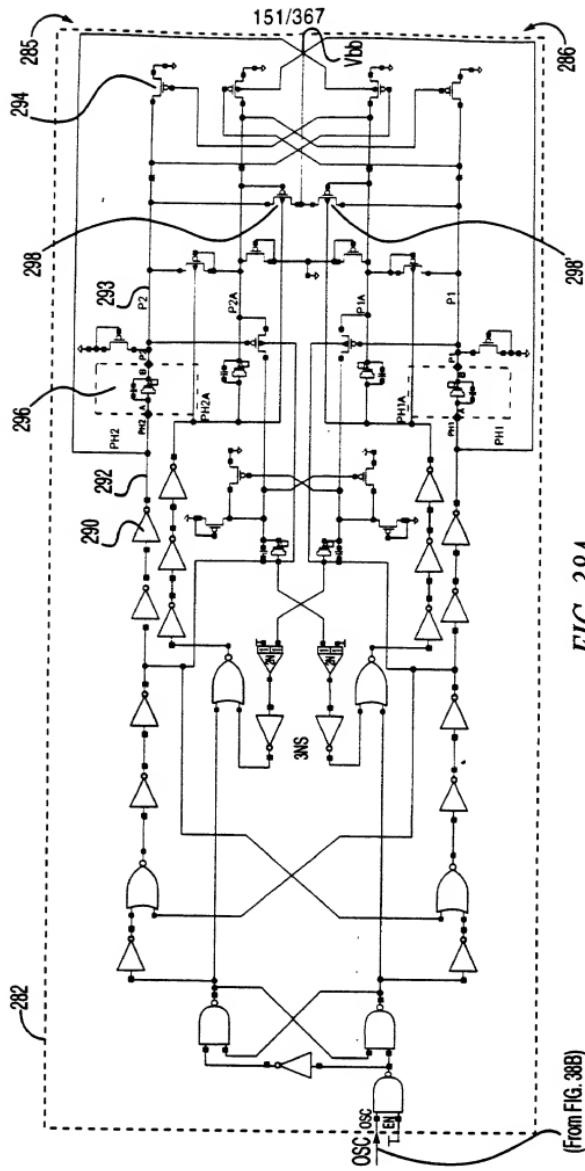
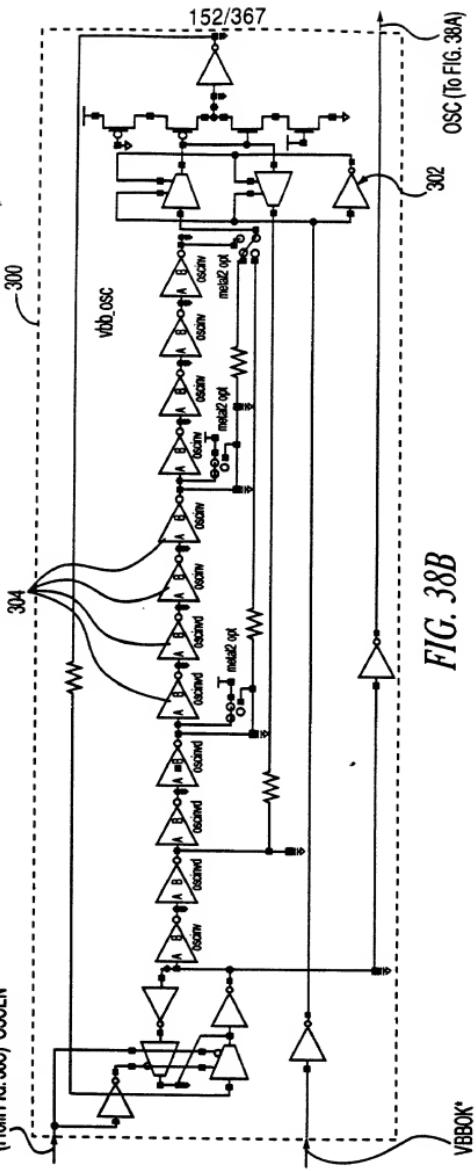


FIG. 38A

(From FIG. 38B)

(From FIG. 38C) OSCEN*



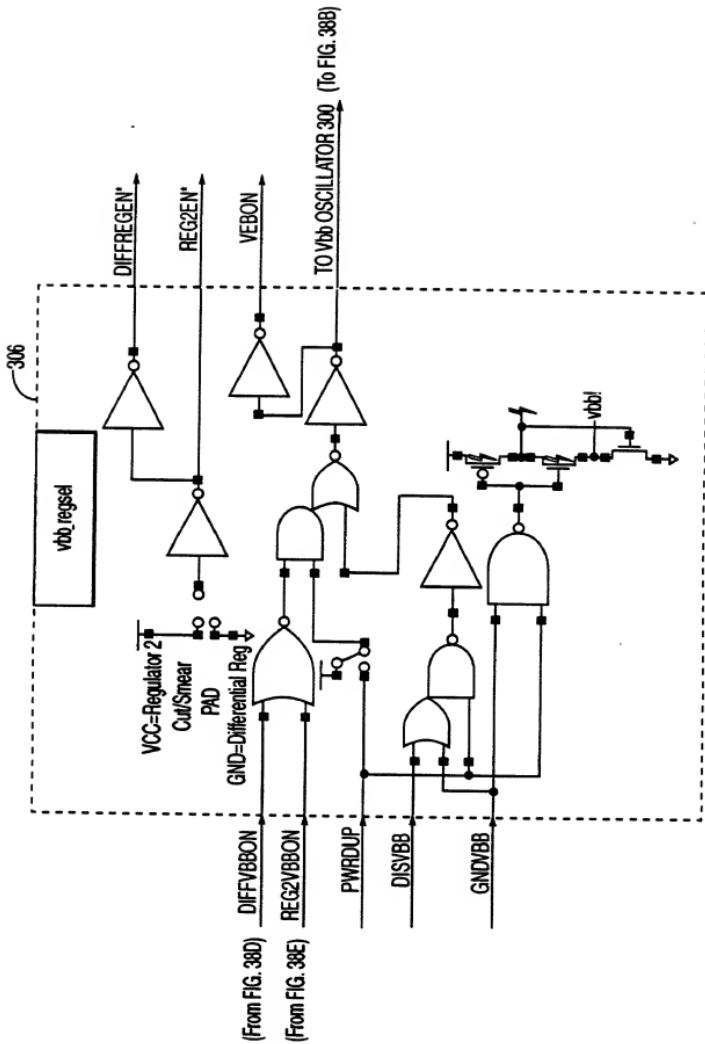


FIG. 38C

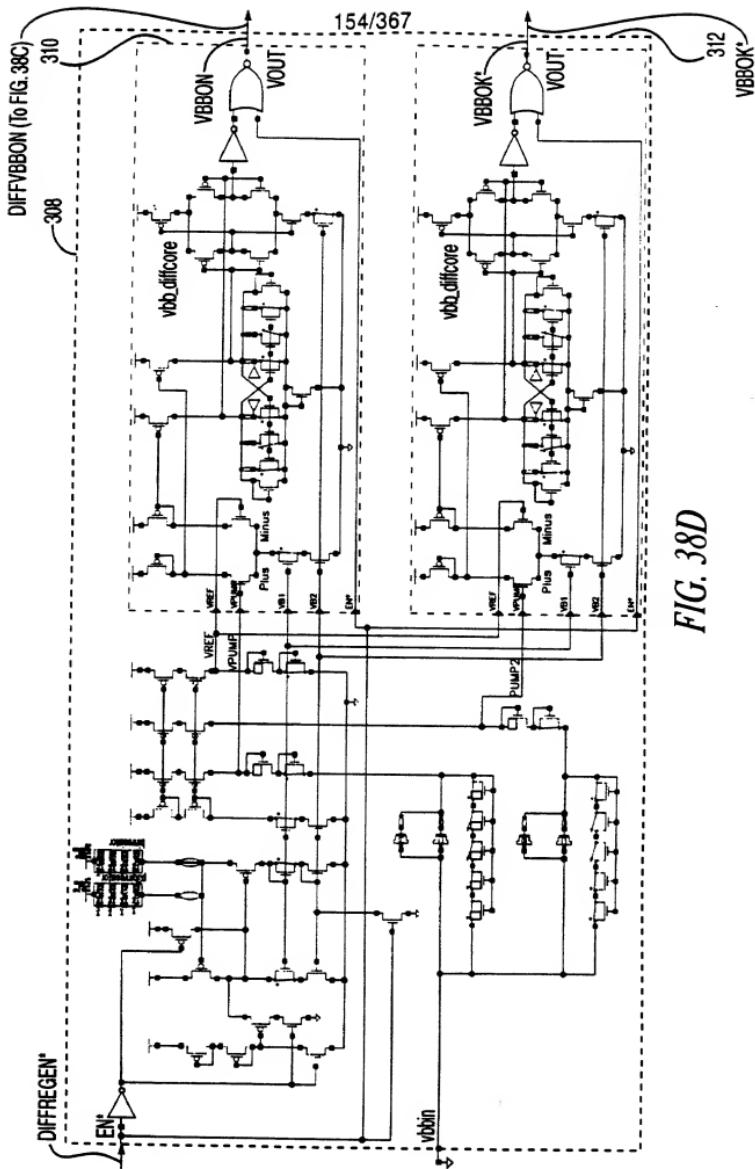


FIG. 38D

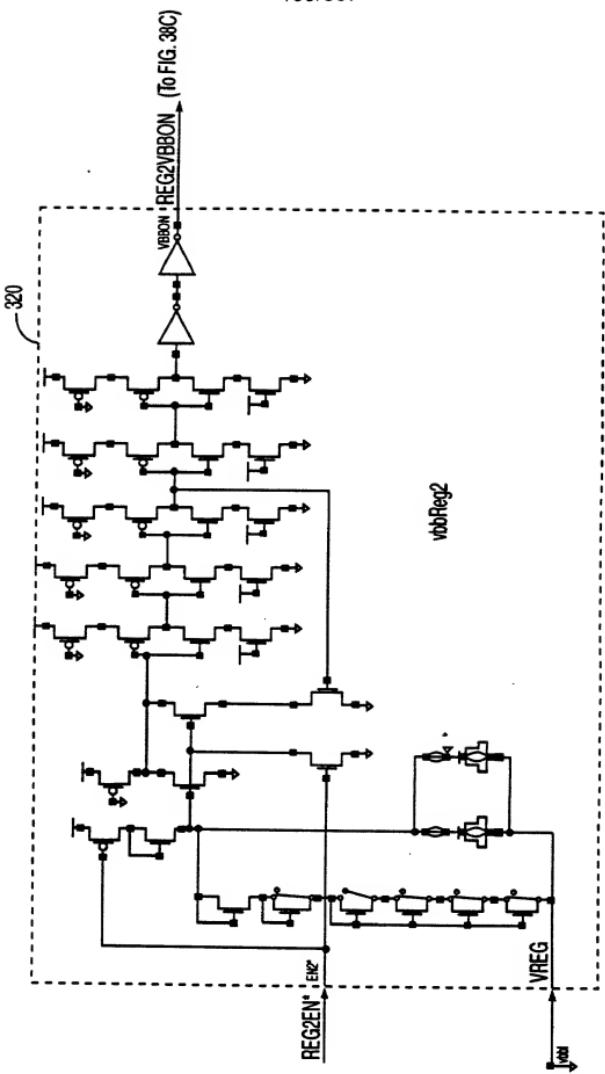
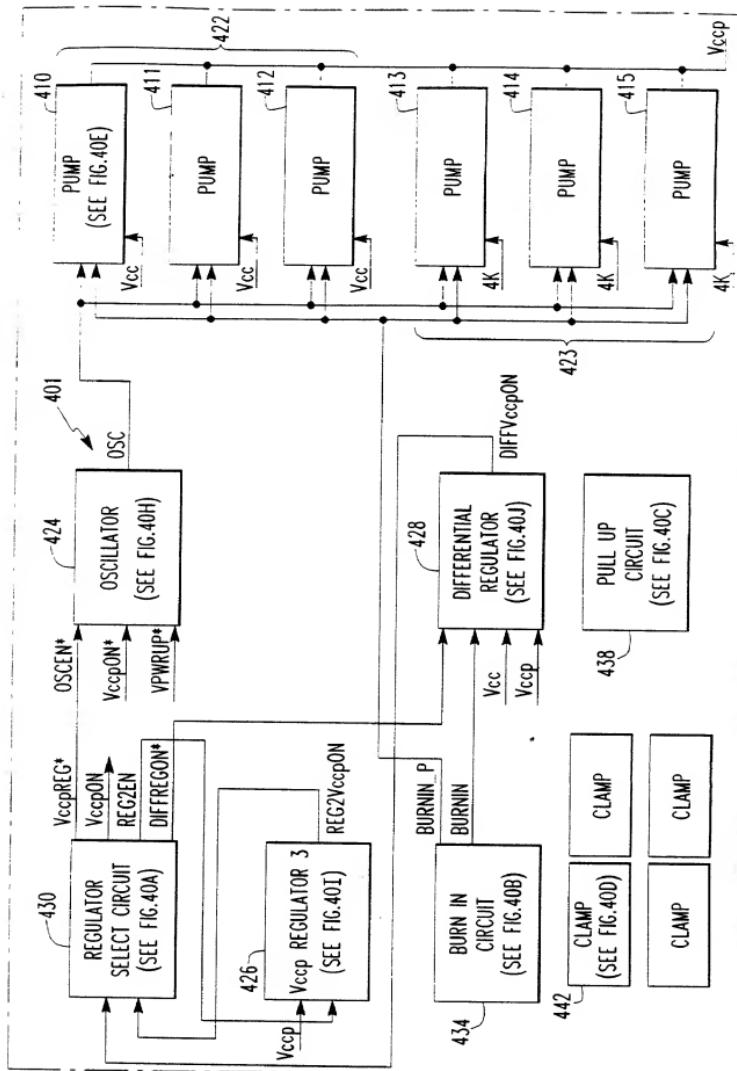


FIG. 38E



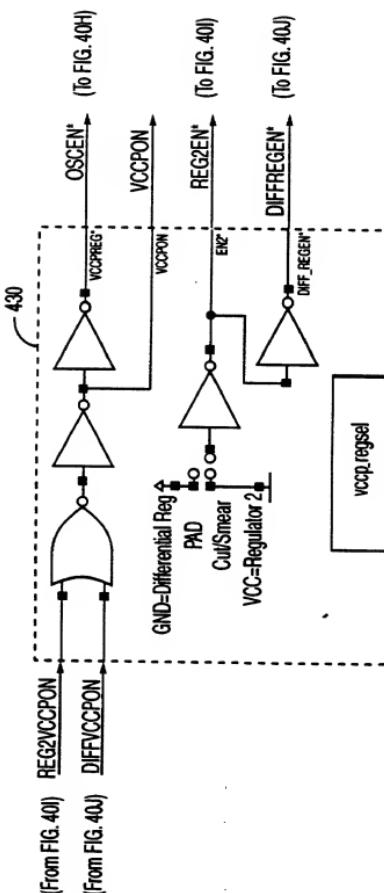


FIG. 40A

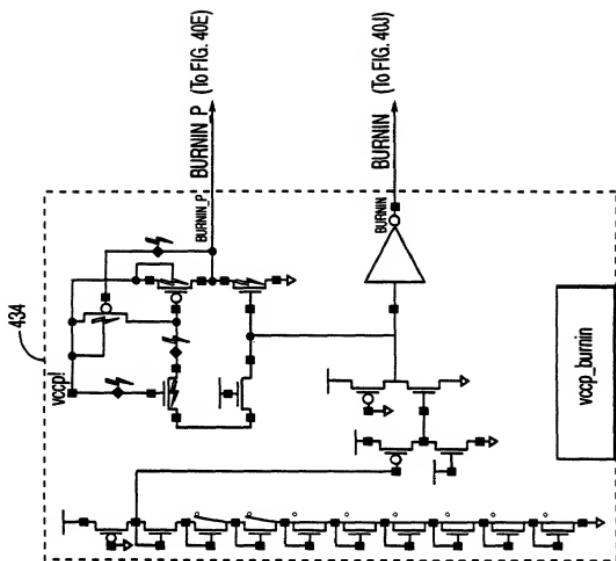


FIG. 40B

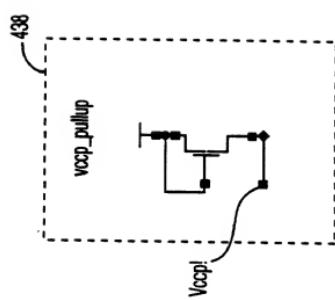


FIG. 40C

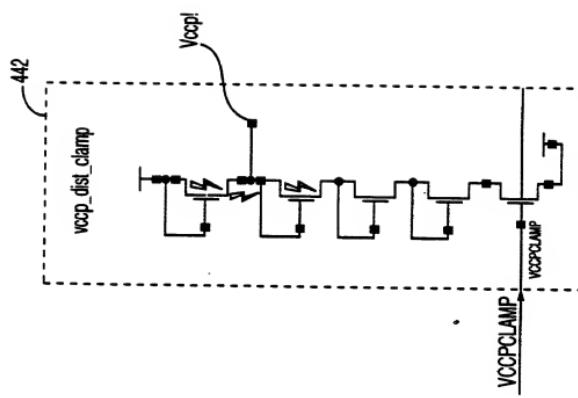


FIG. 40D

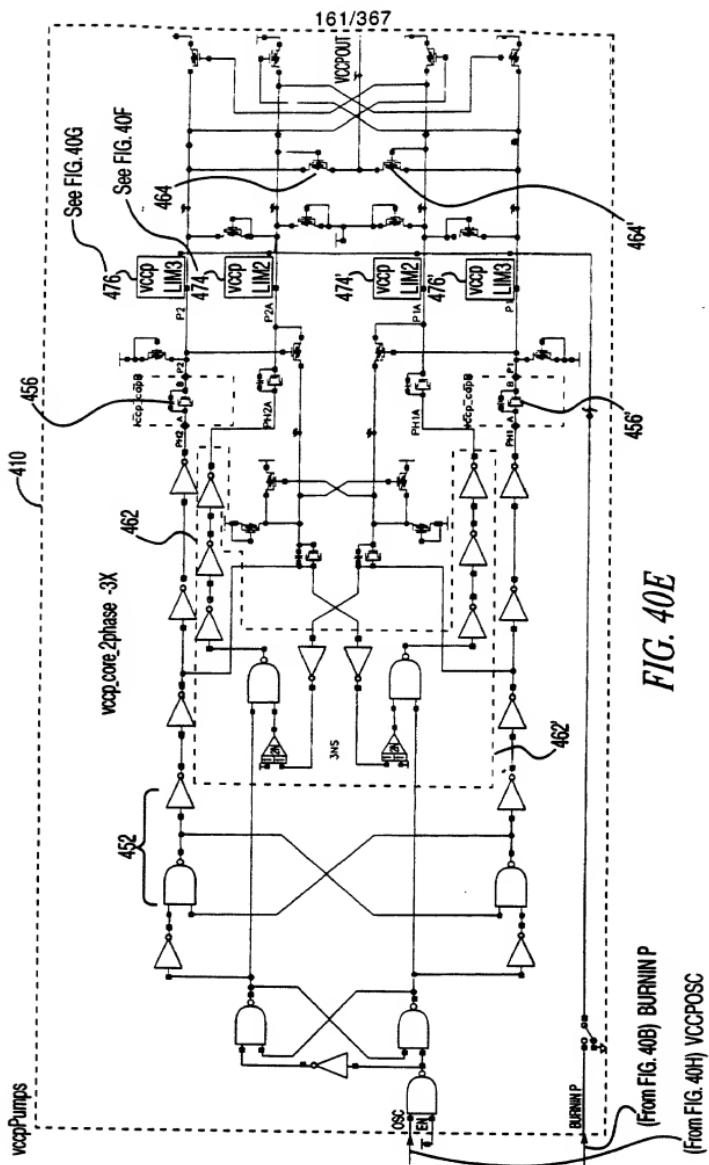
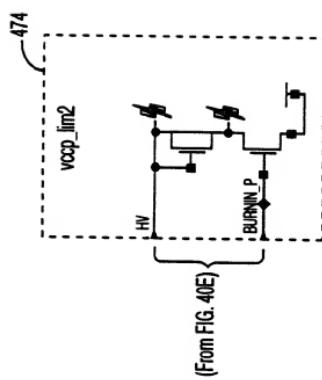


FIGURE 40F

FIG. 40F



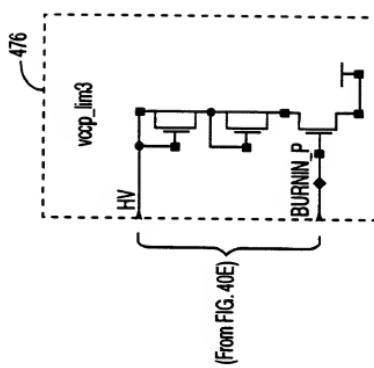
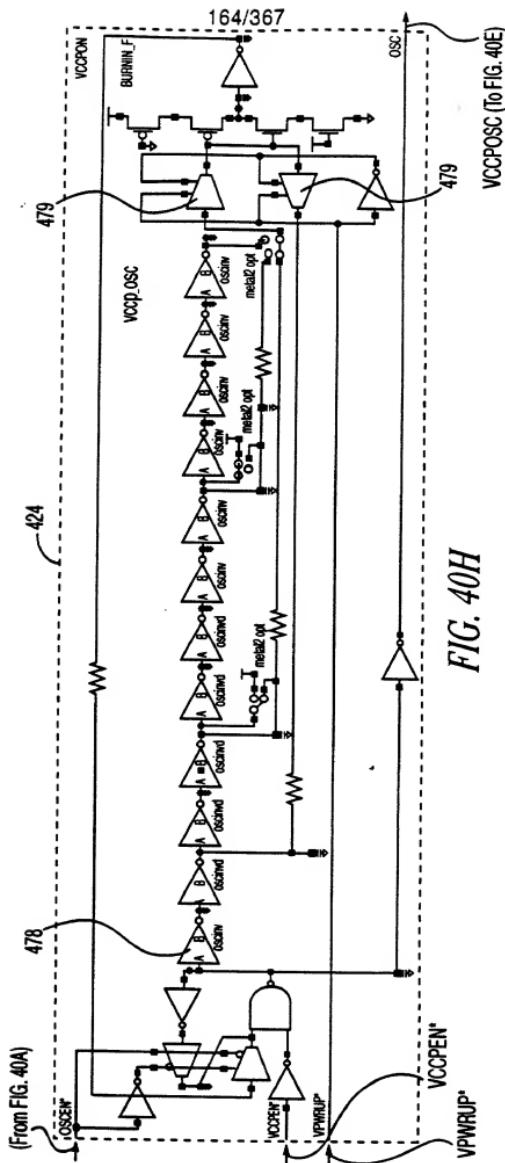


FIG. 40G

FIG. 40H



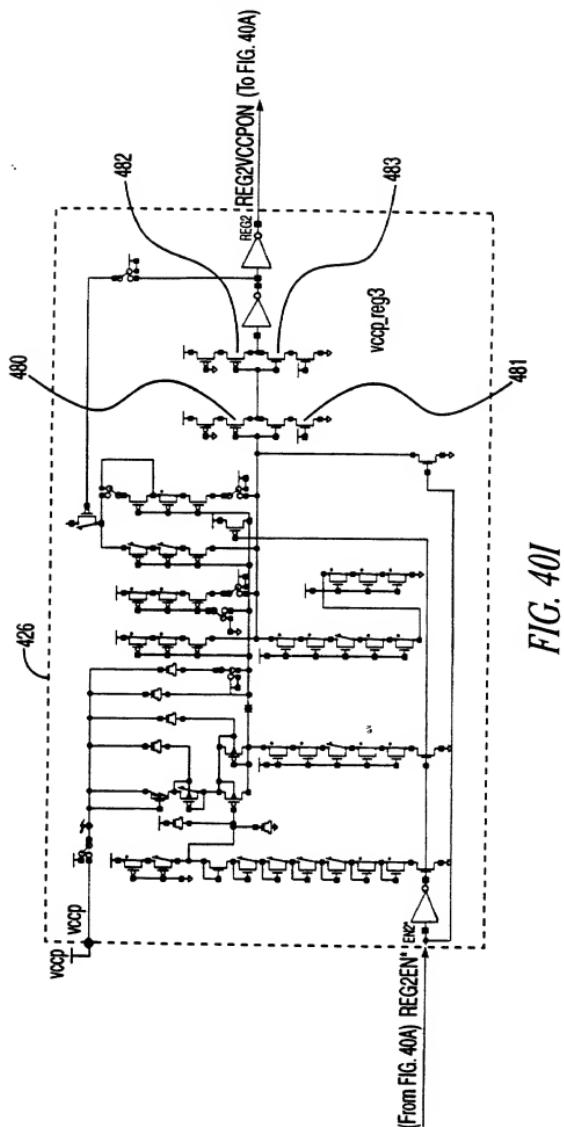
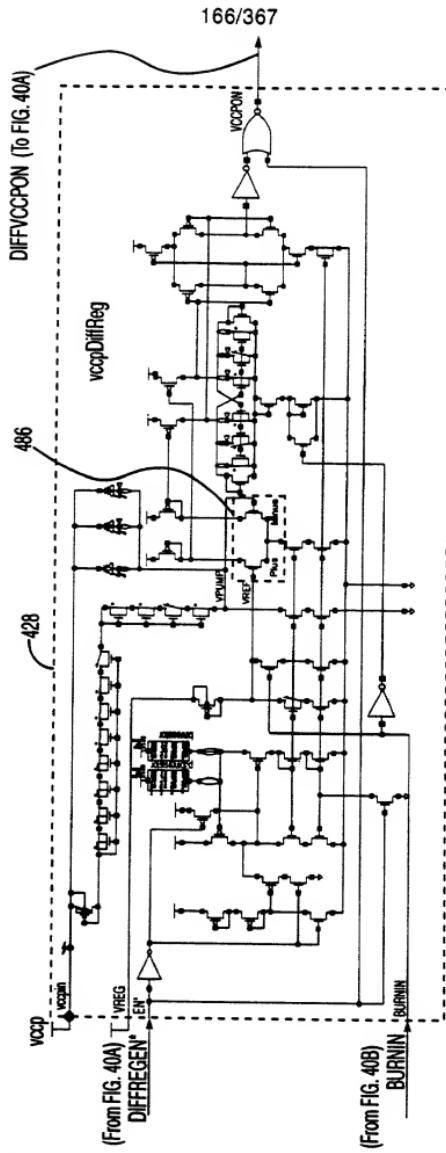
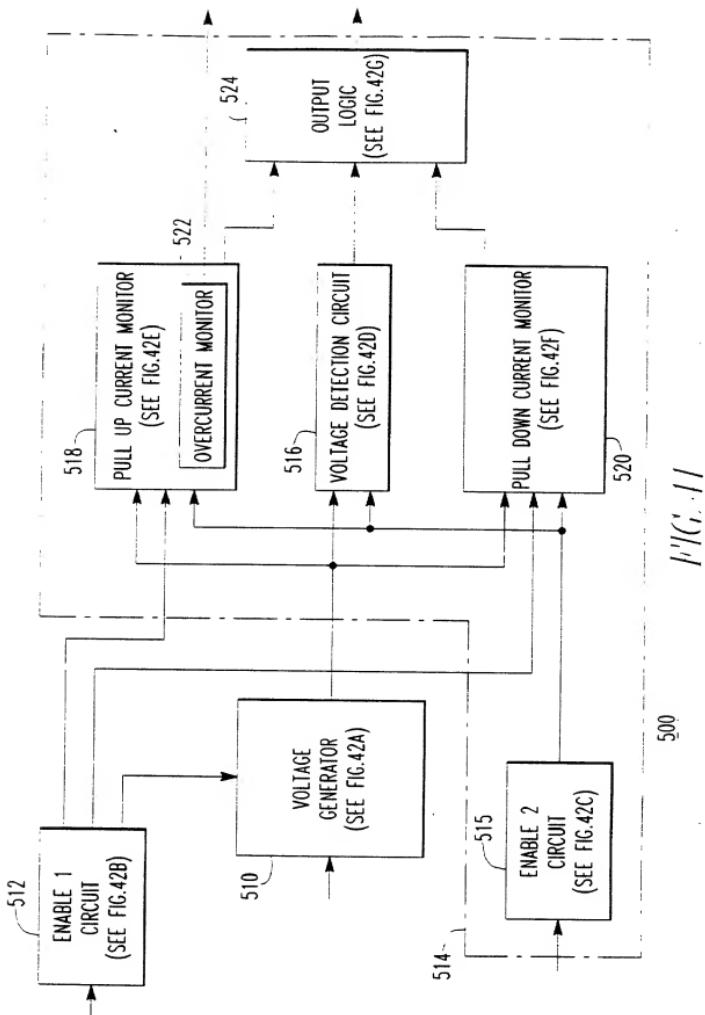
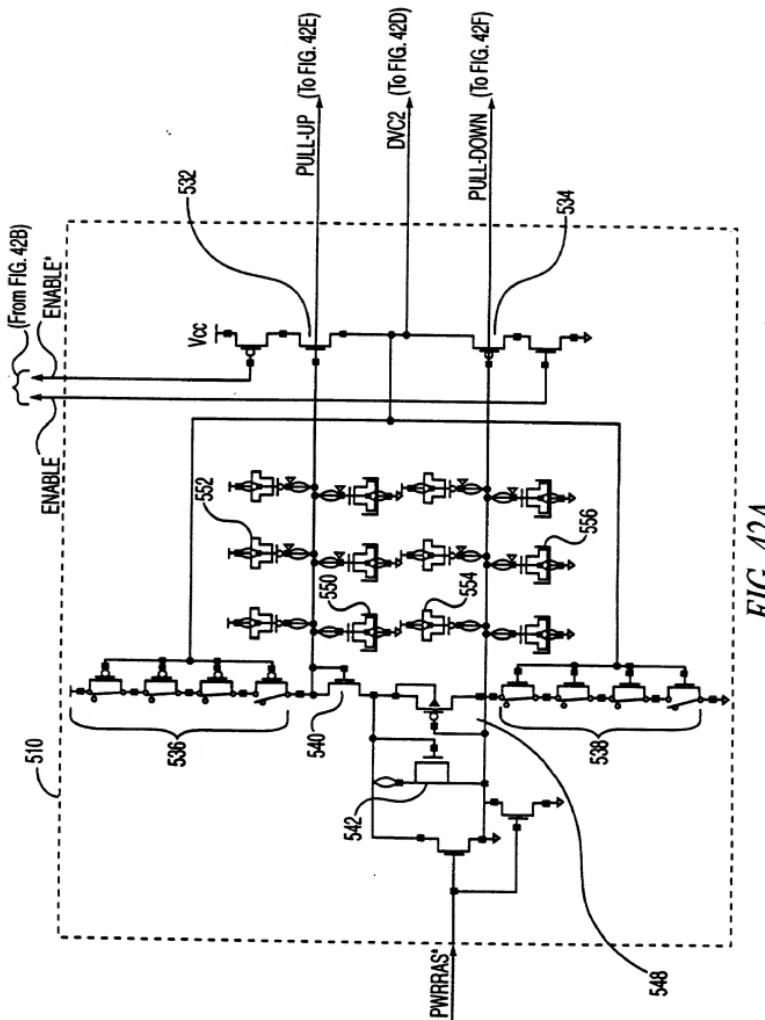


FIG. 40I

FIG. 40J







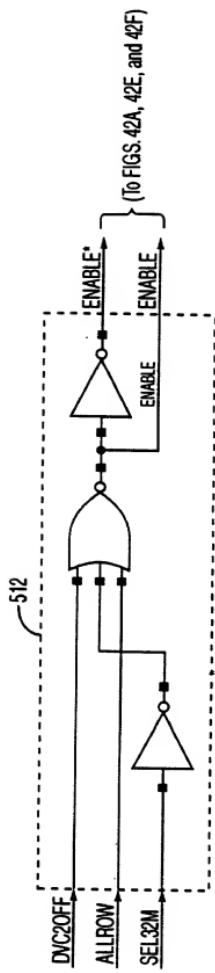


FIG. 42B

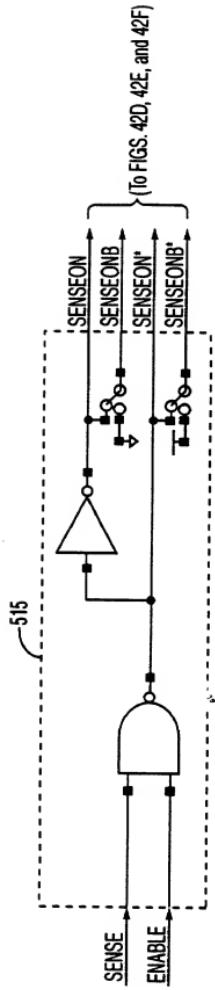
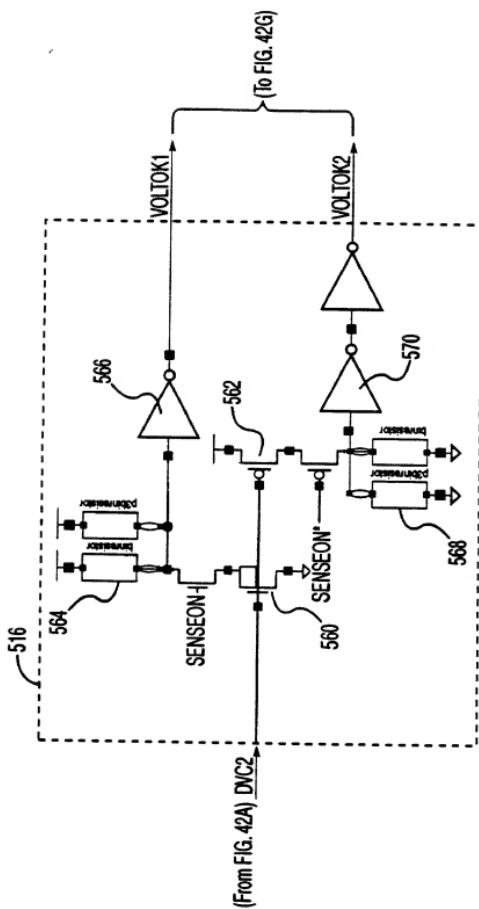


FIG. 42C

FIG. 42D



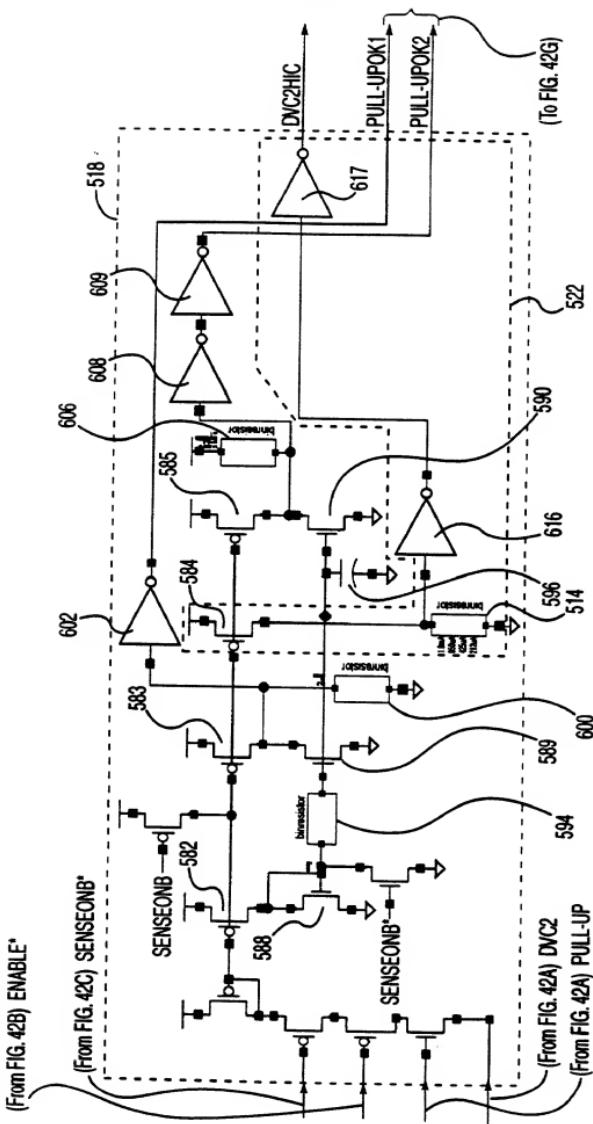


FIG. 42E

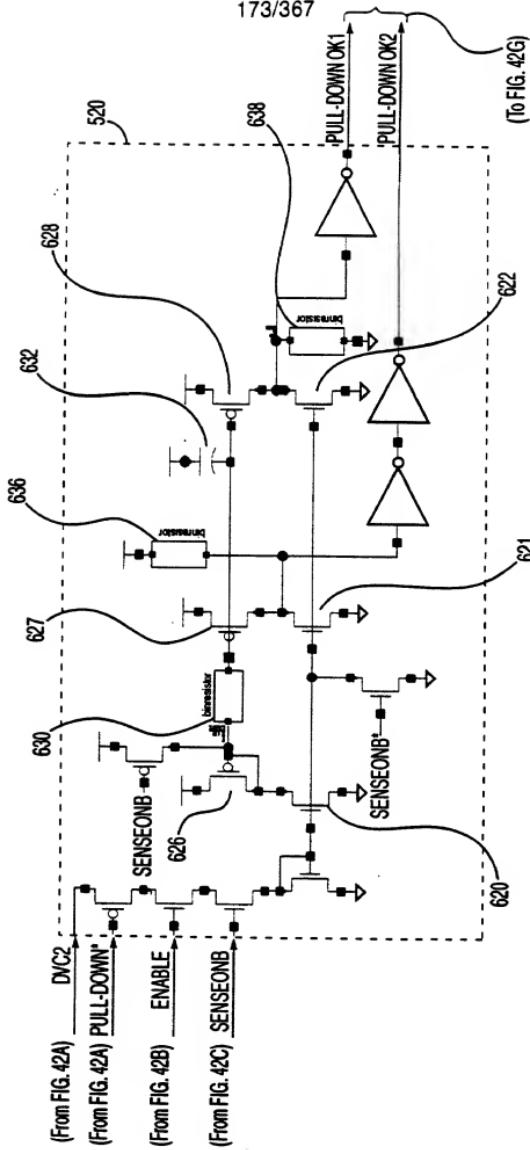


FIG. 42F

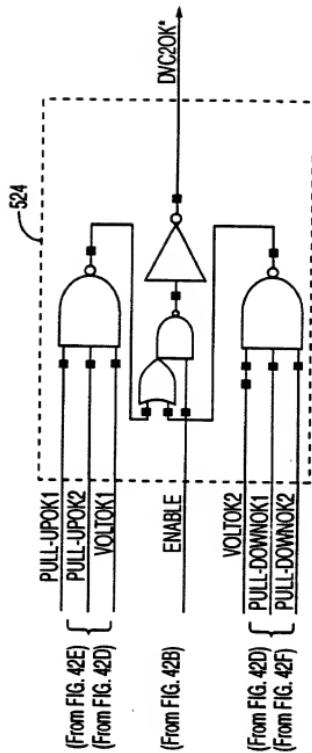
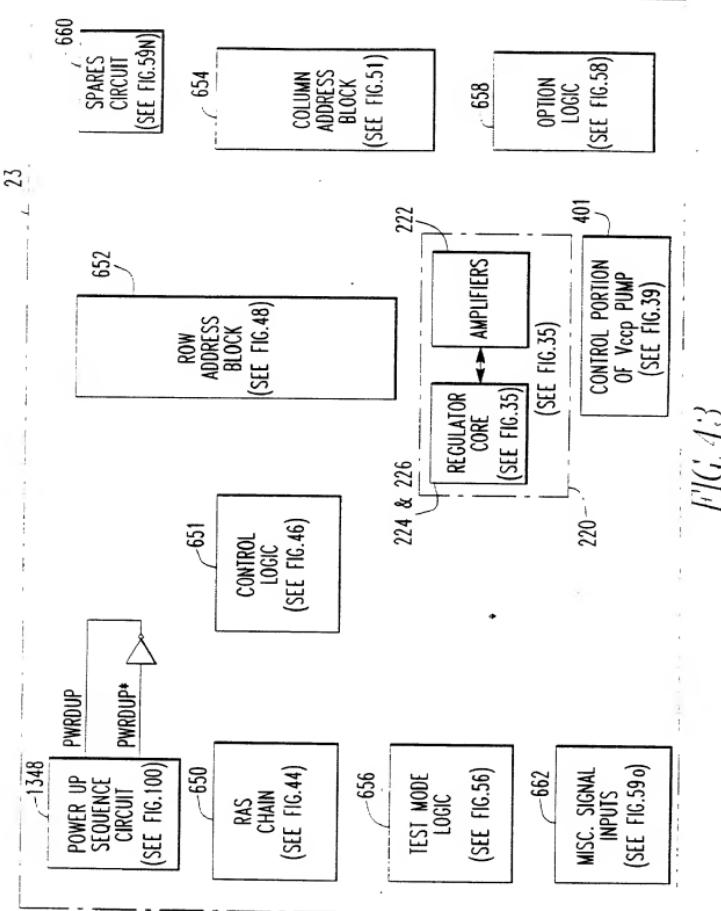
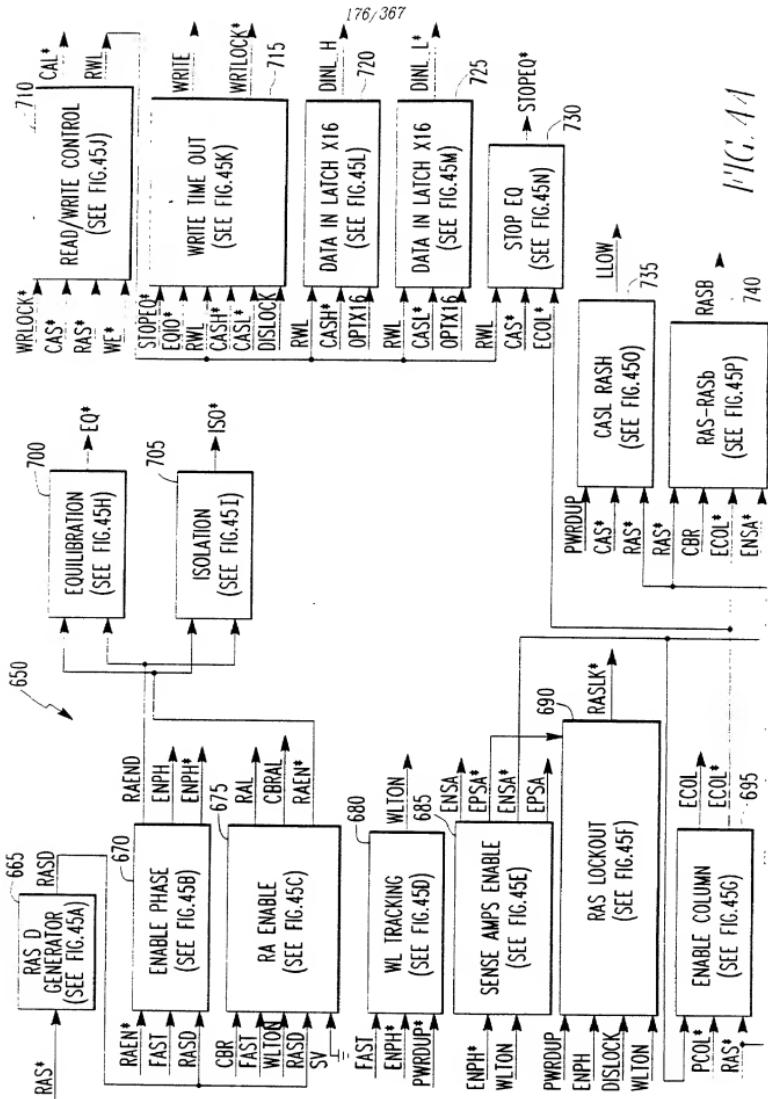


FIG. 42G

TOP SECRET SOURCECODE





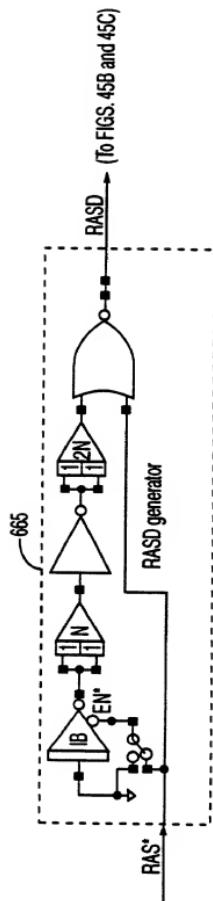


FIG. 45A

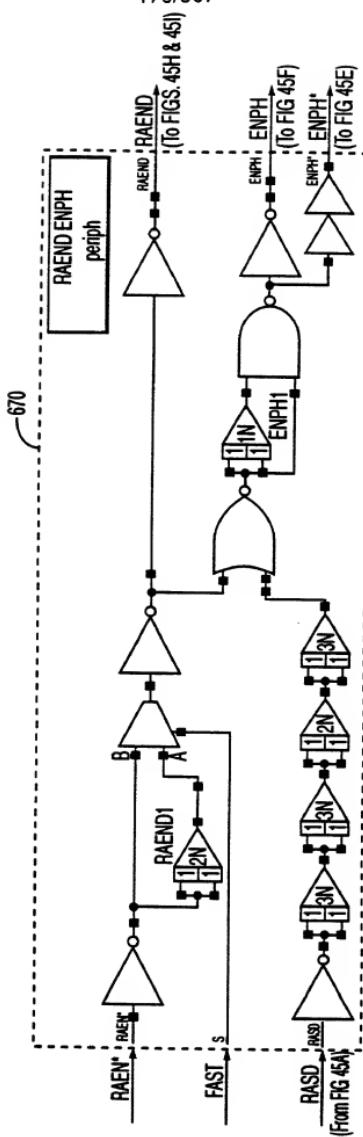


FIG. 45B

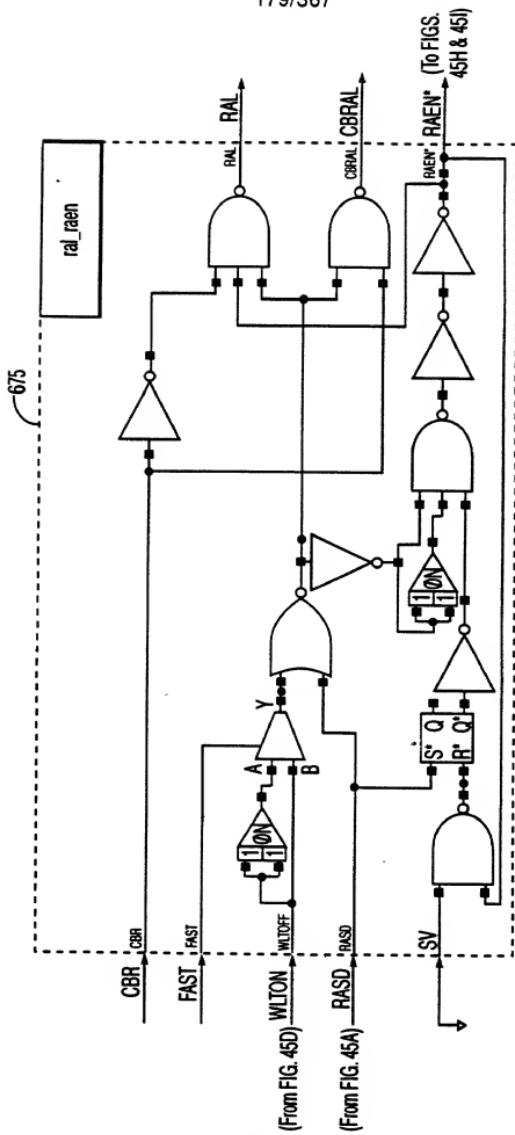
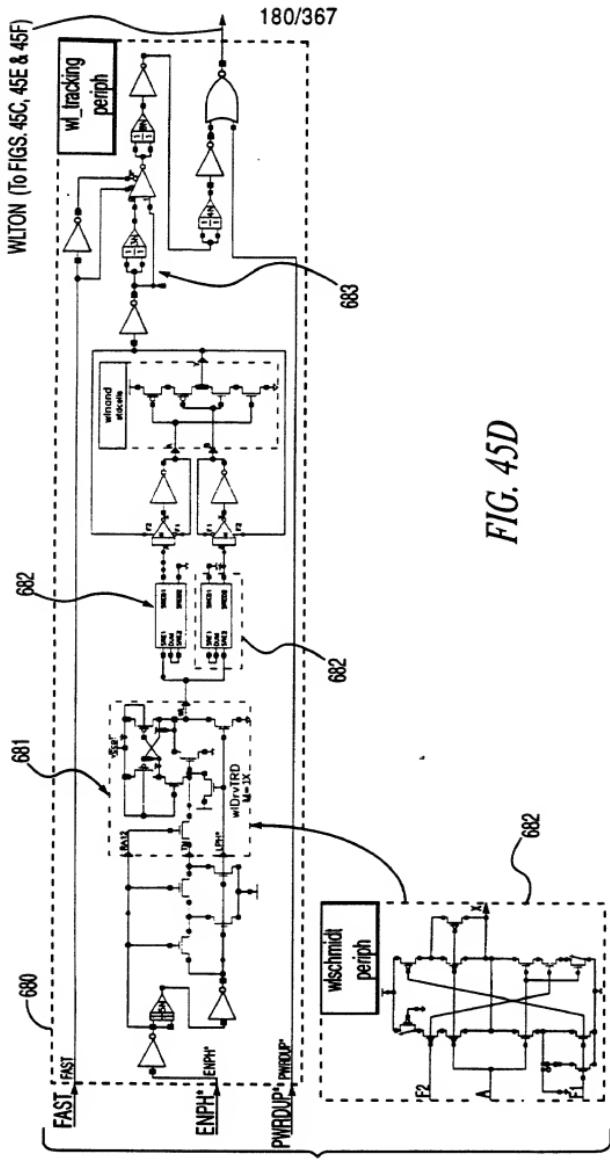


FIG. 45C

FIG. 45D

(From FIG. 45B)



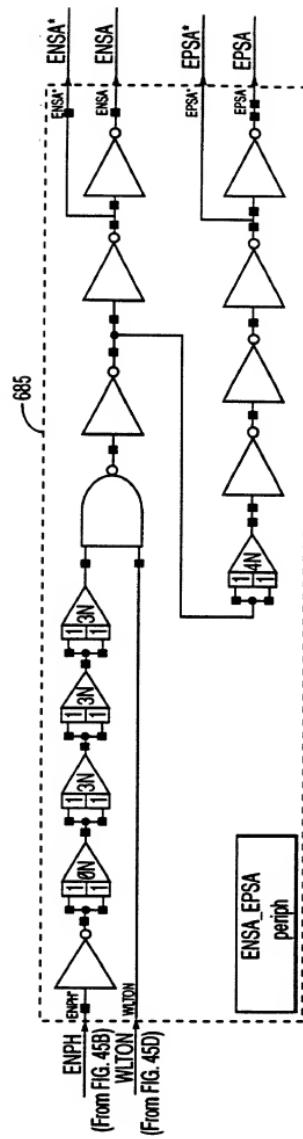


FIG. 45E

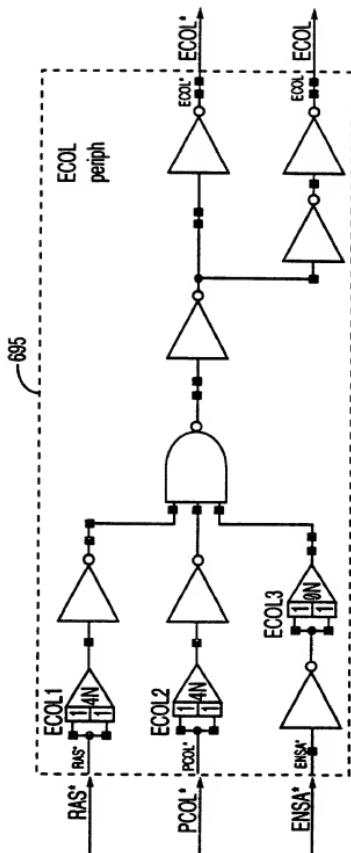


FIG. 45G

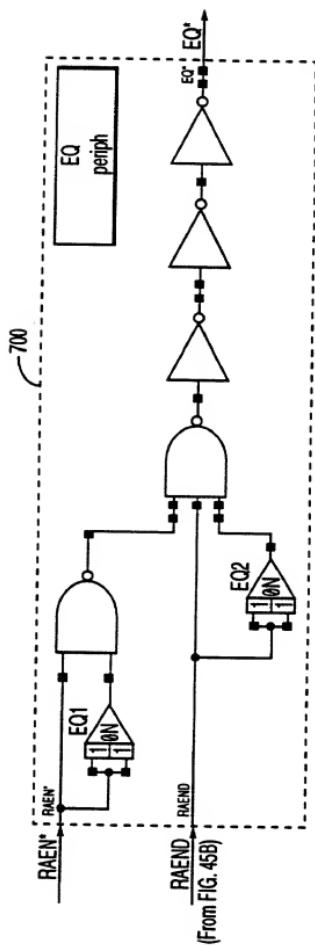


FIG. 45H

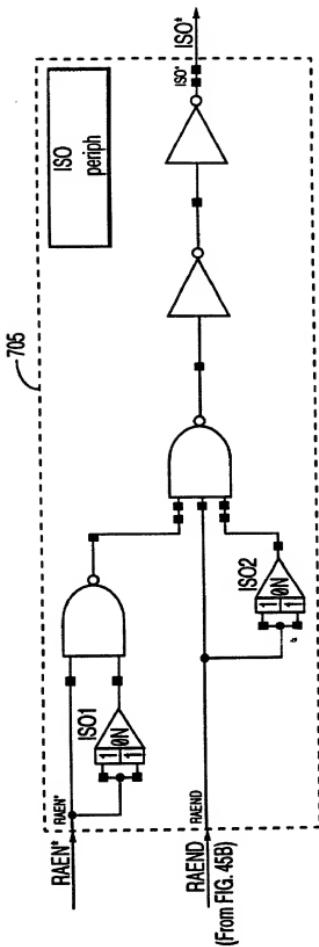


FIG. 451

(From FIG. 450)

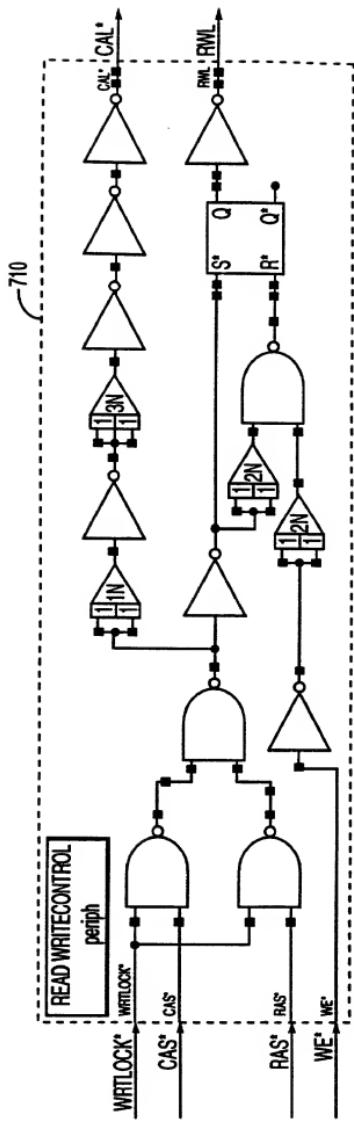


FIG. 45J

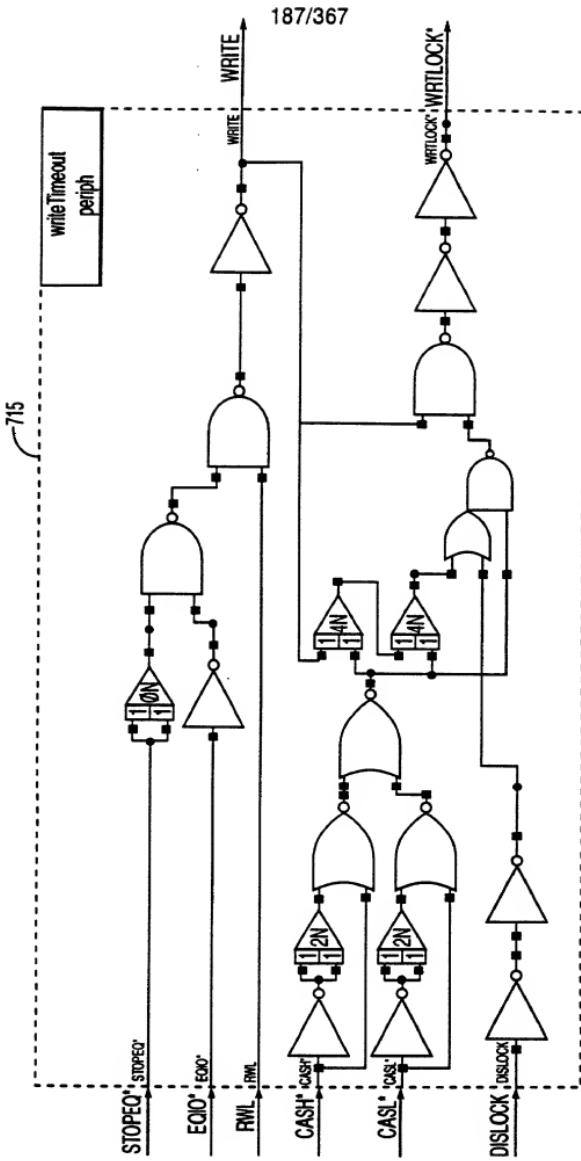


FIG. 45K

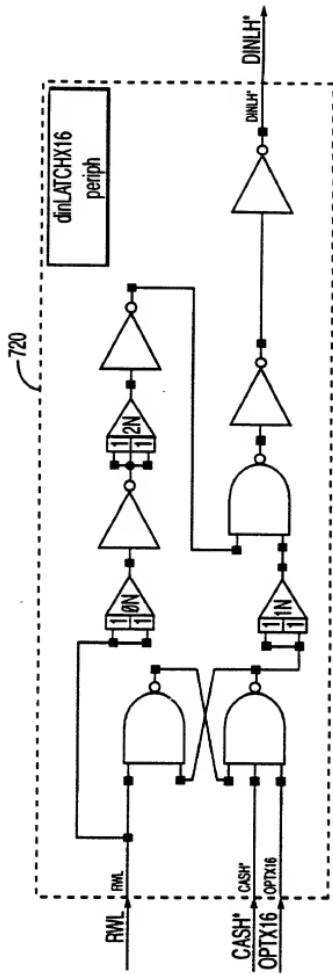


FIG. 45L

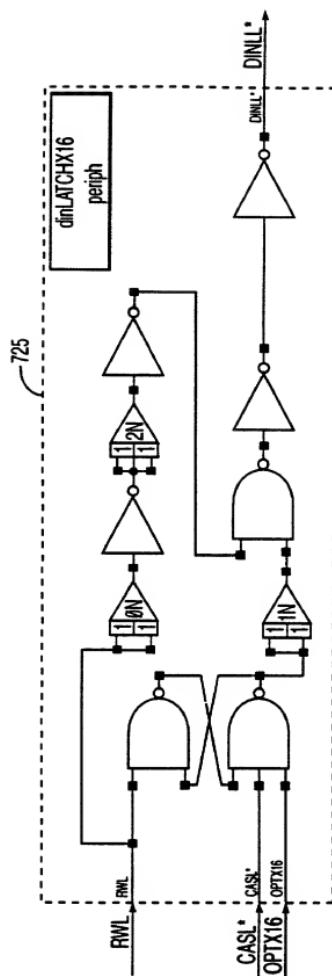


FIG. 45M

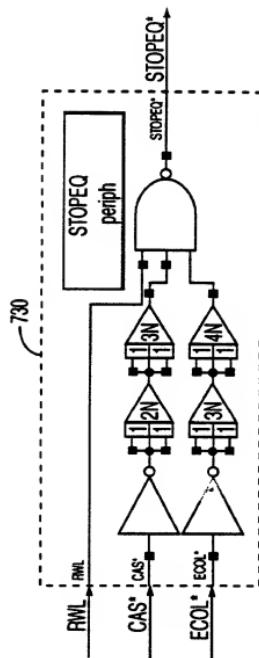


FIG. 45N

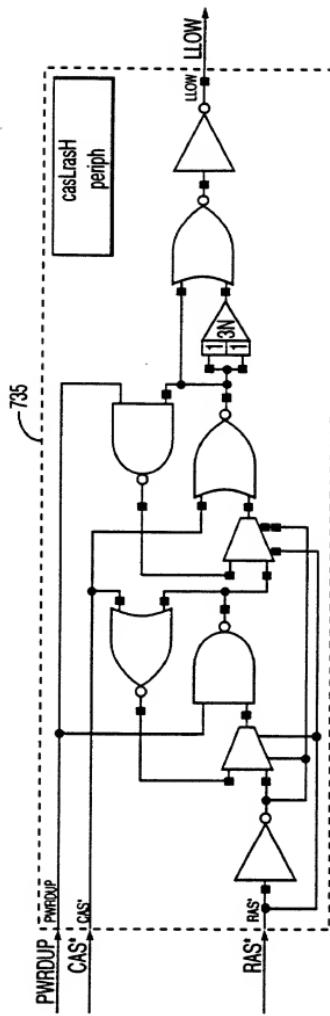


FIG. 450

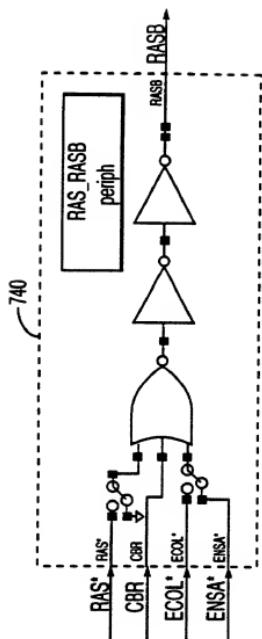
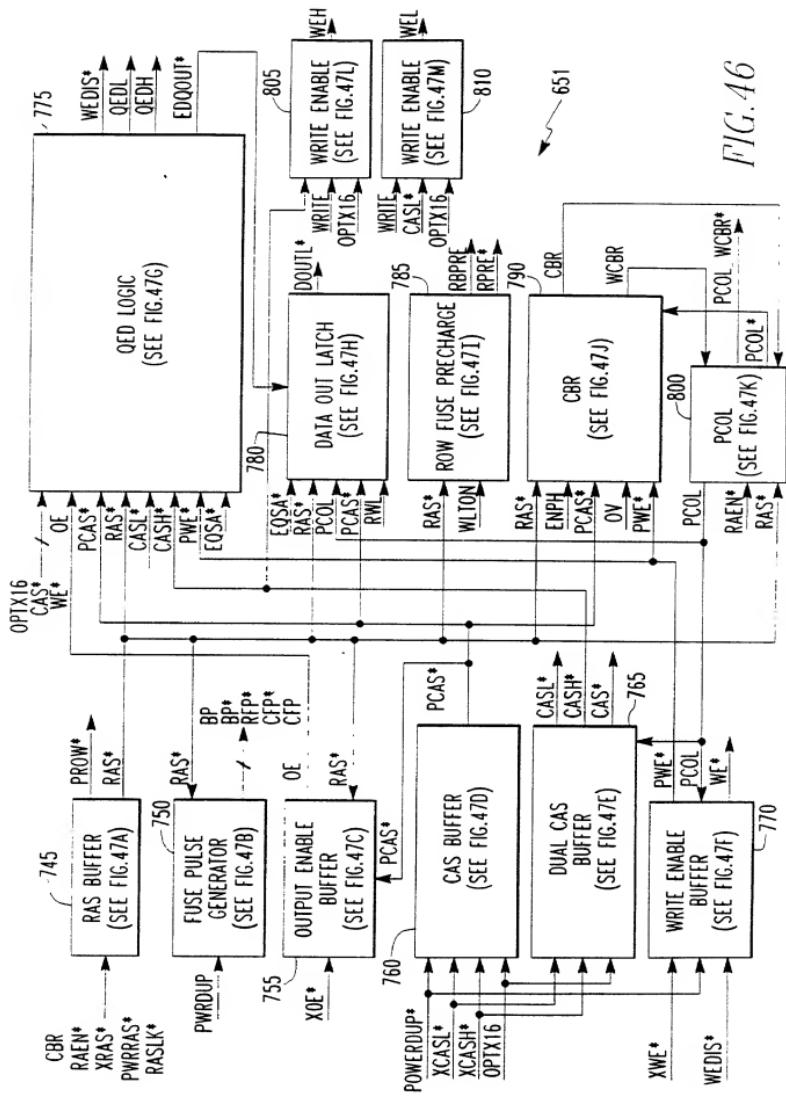


FIG. 45P



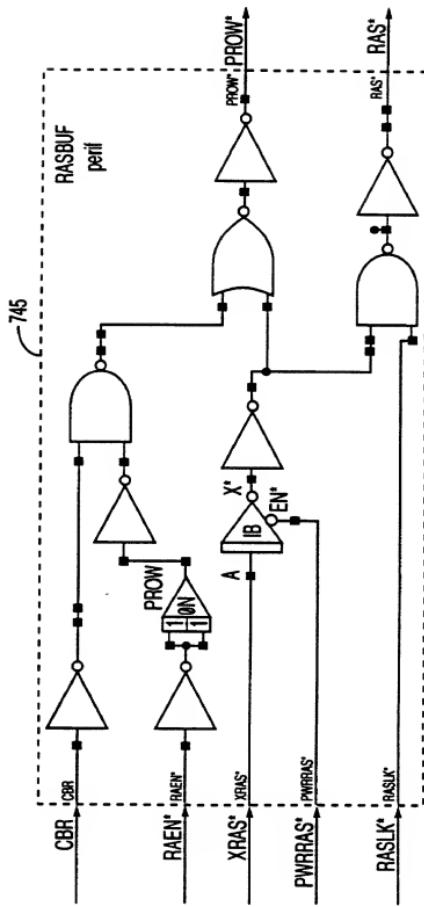


FIG. 47A

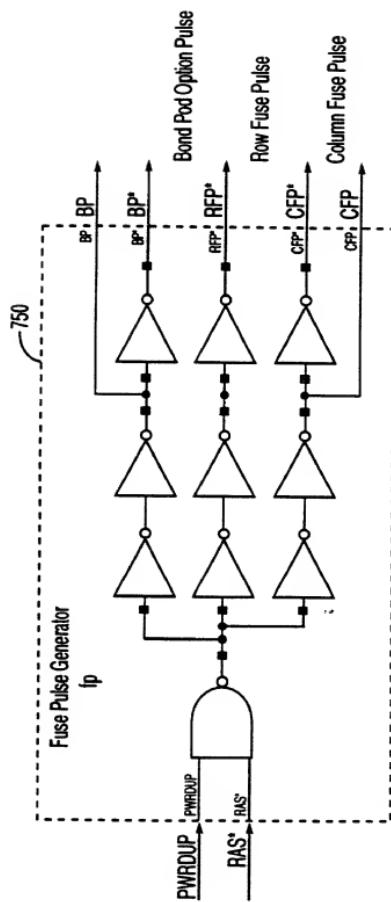


FIG. 47B

卷之三

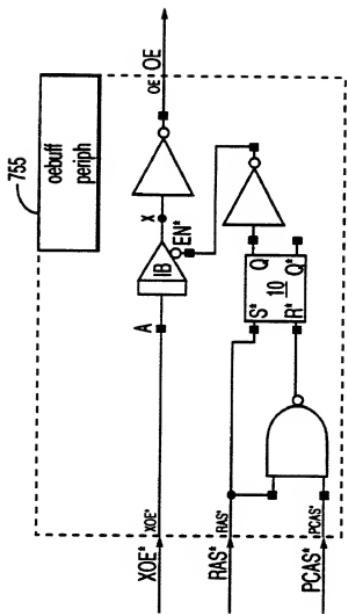


FIG. 47C

TOKYO ELECTRON

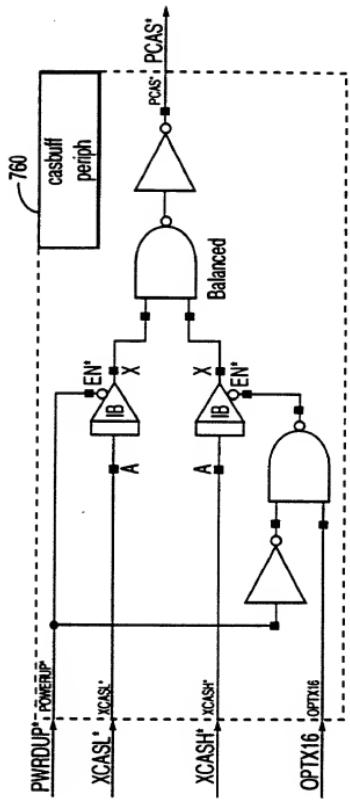


FIG. 47D

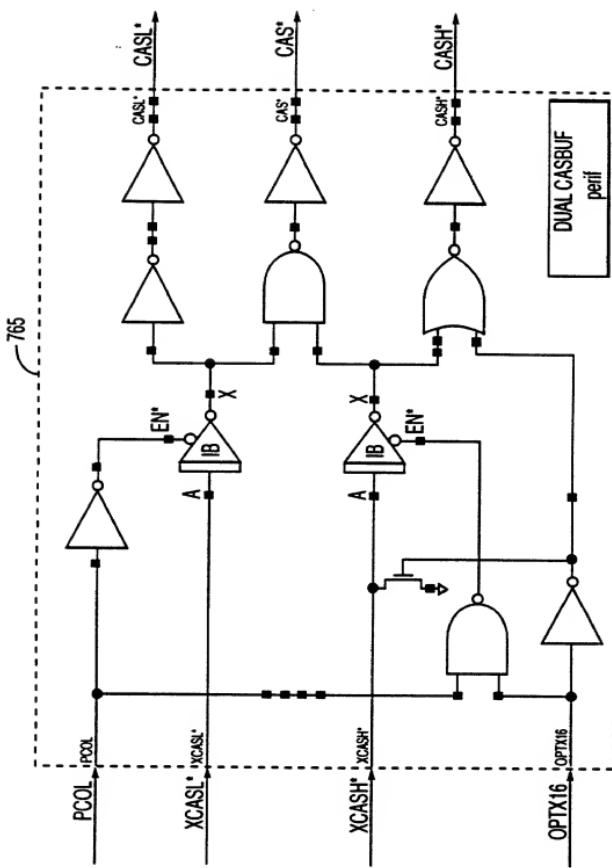


FIG. 47E

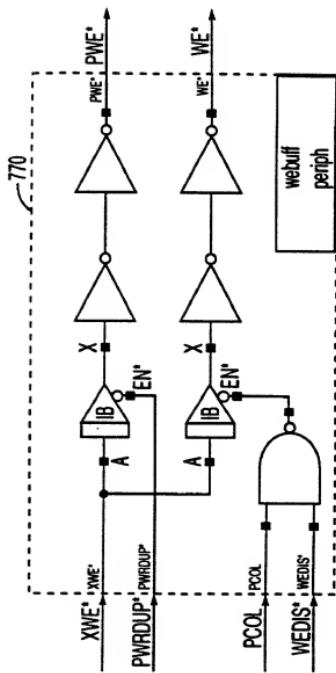


FIG. 47F

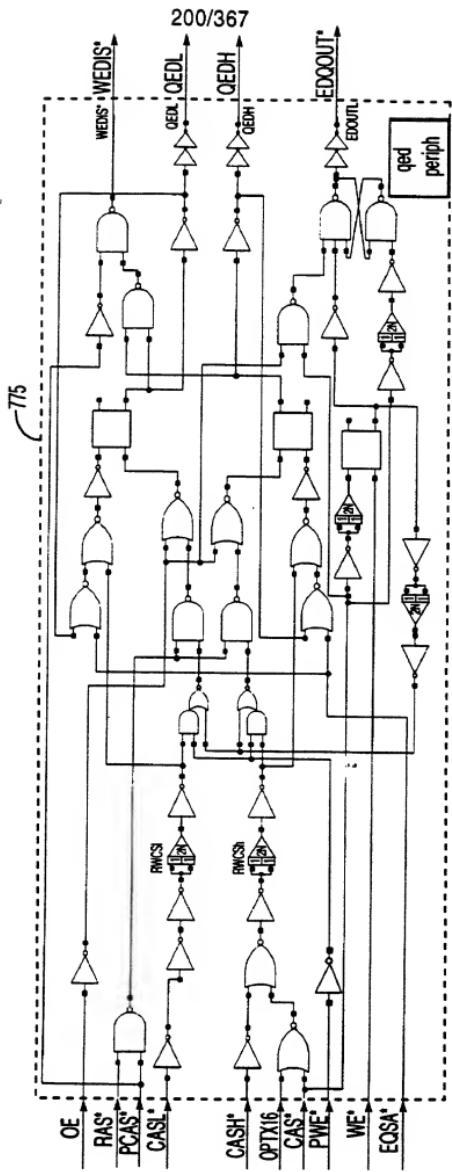


FIG. 47C

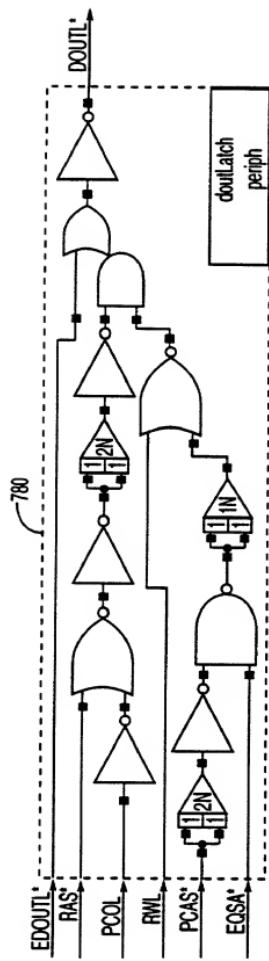


FIG. 47H

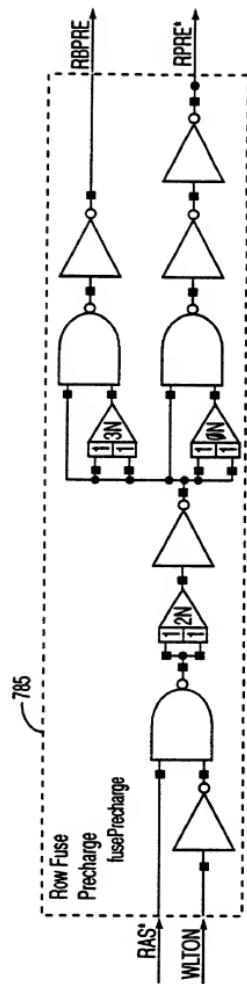


FIG. 471

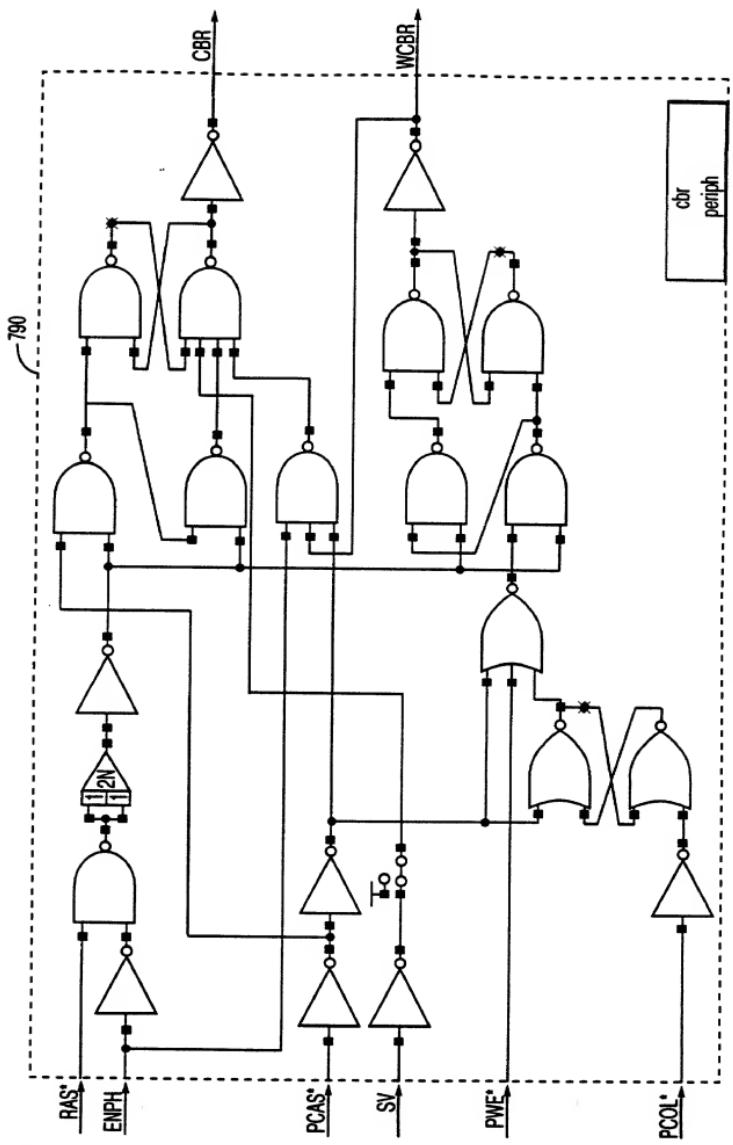


FIG. 47J

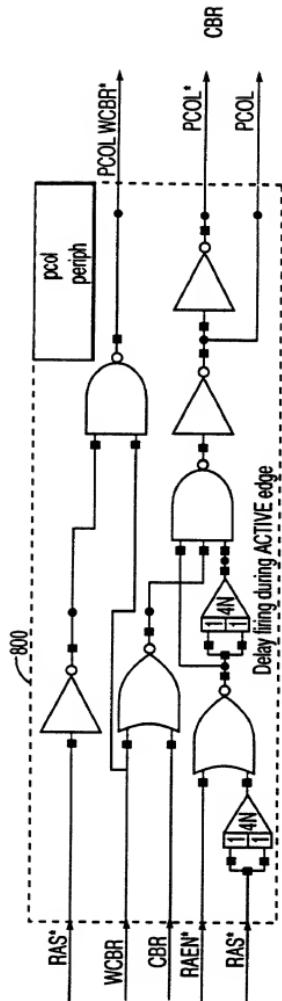


FIG. 47K

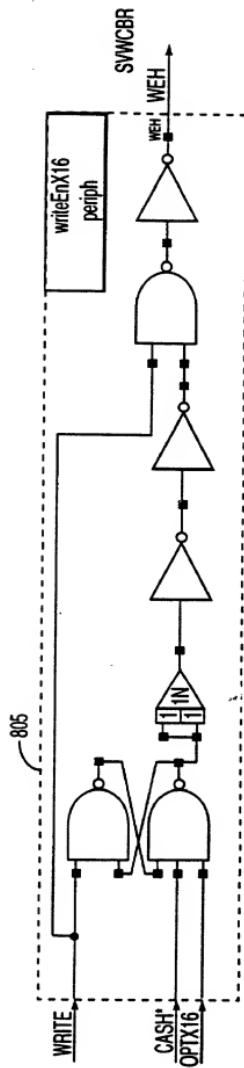


FIG. 47L

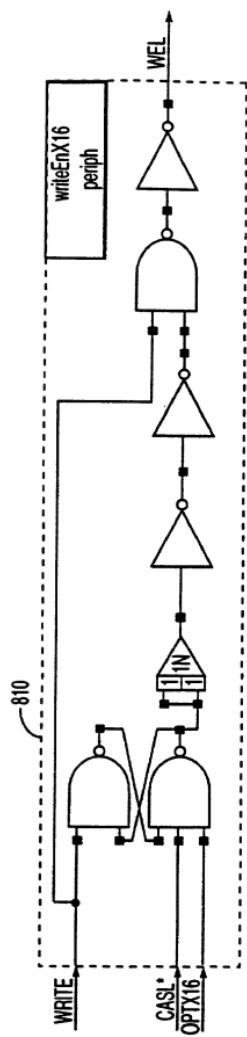


FIG. 47M

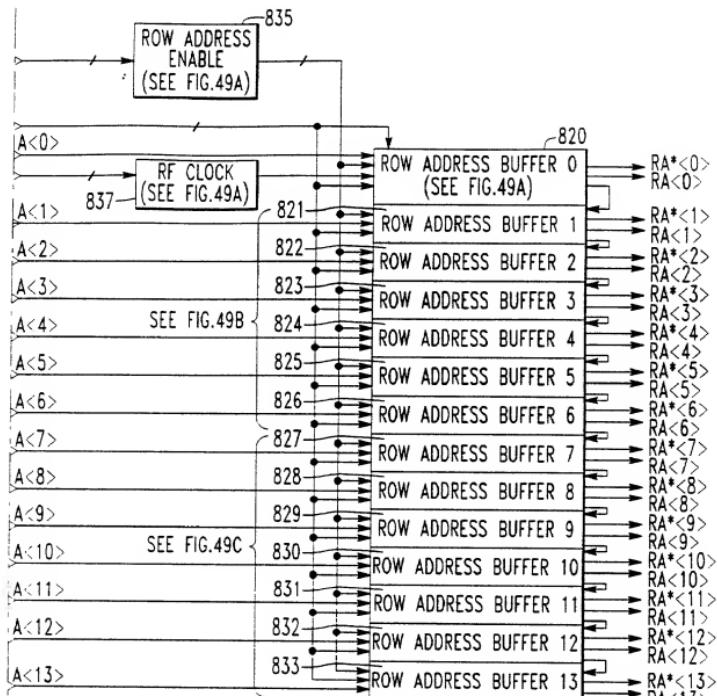


FIG. 48A

652-

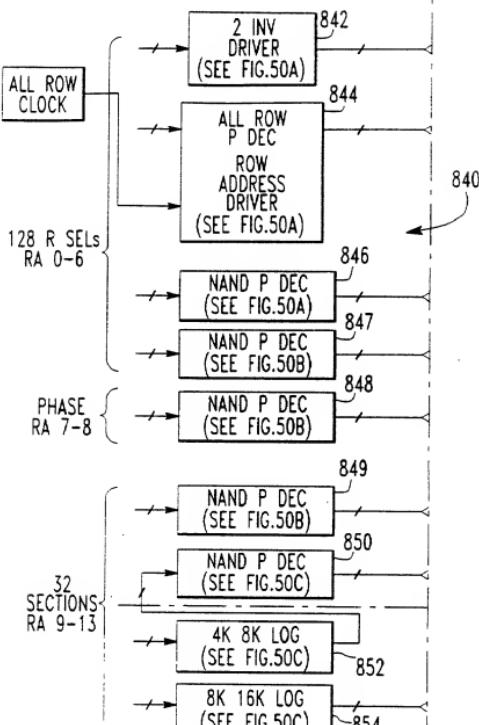


FIG. 48B

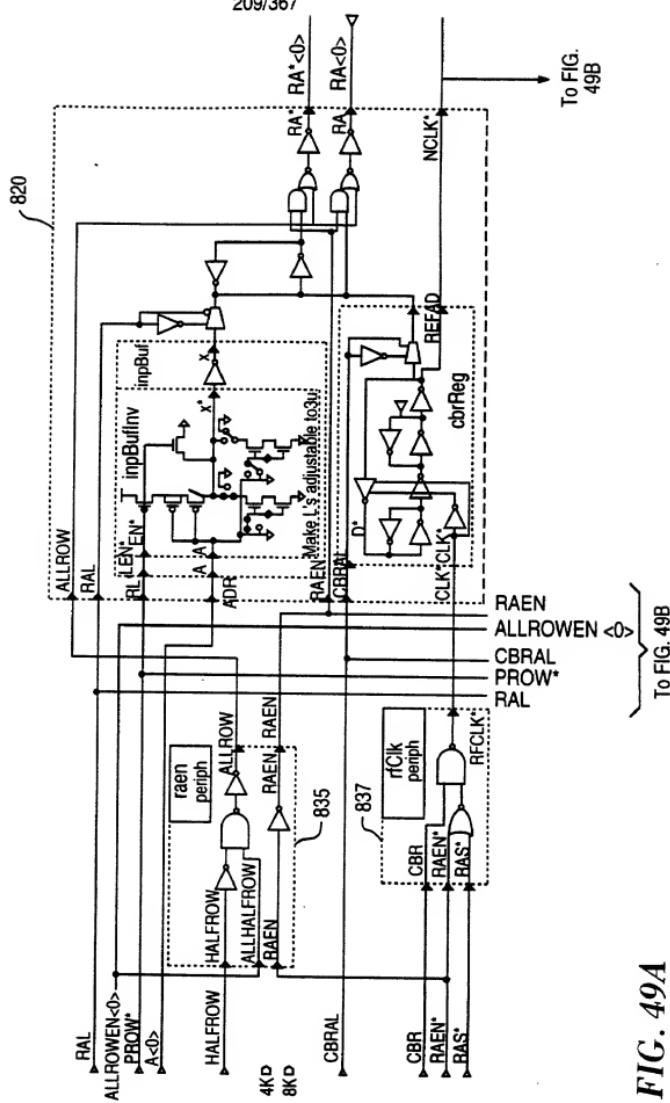
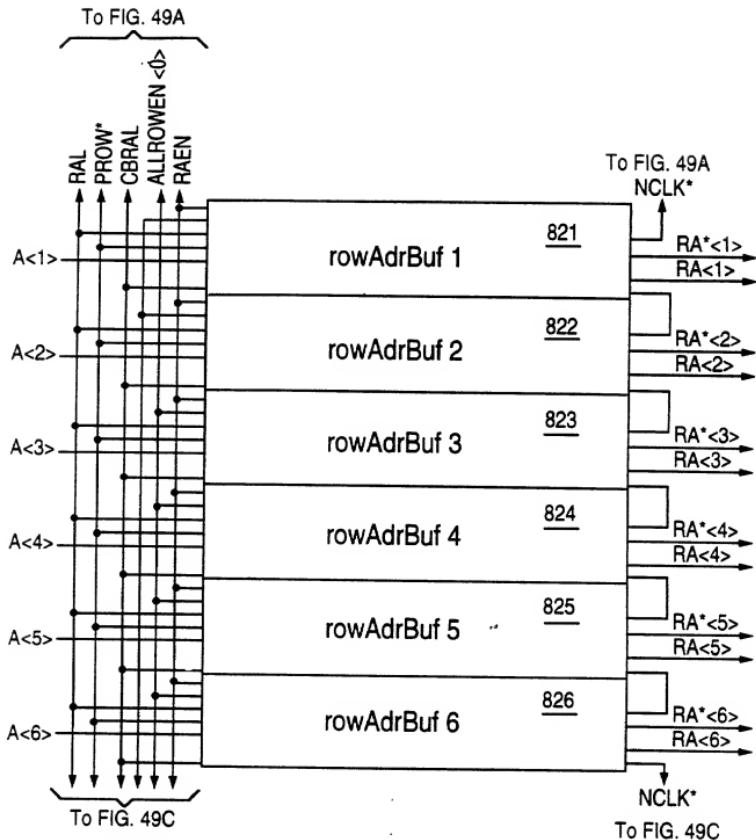
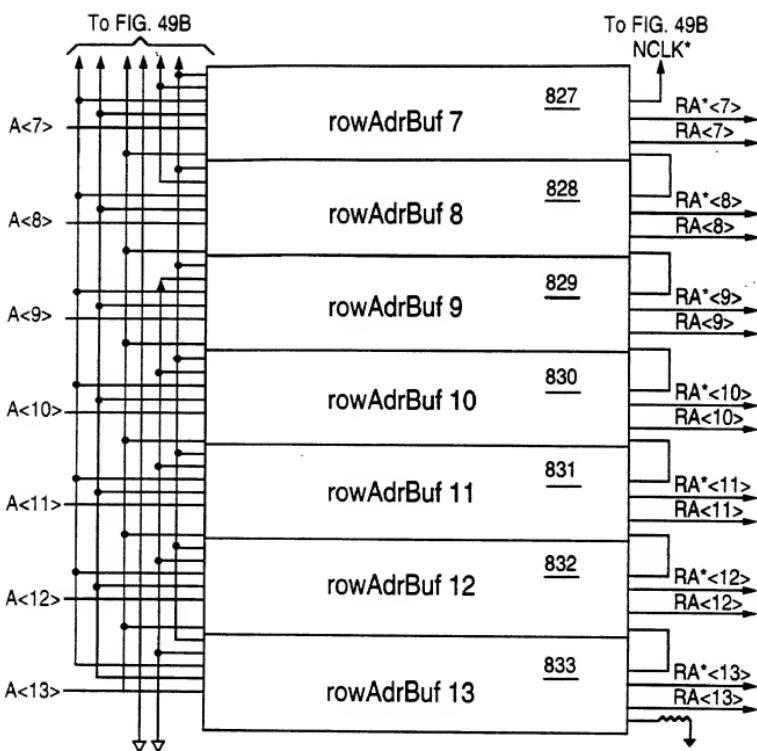
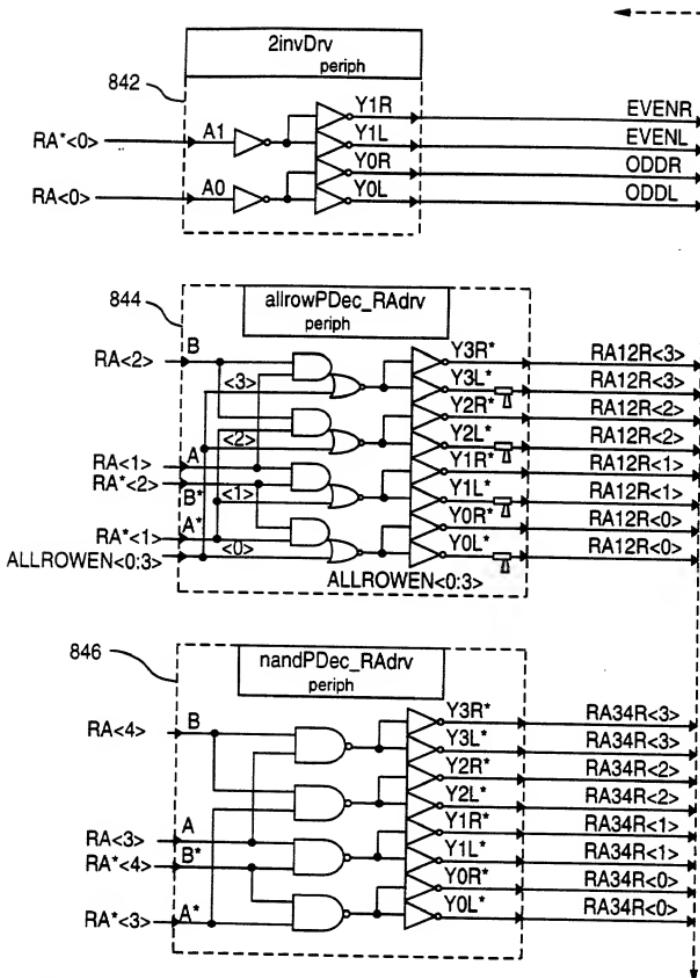
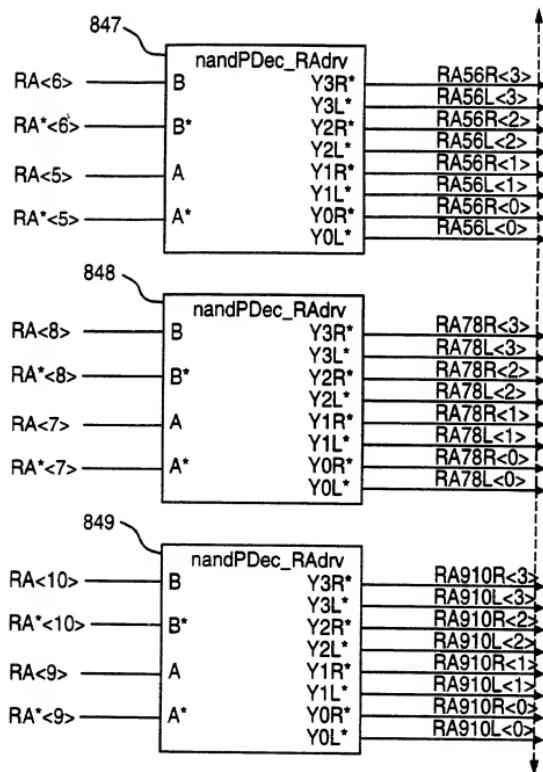


FIG. 49A

**FIG. 49B**

**FIG. 49C**

**FIG. 50A**

**FIG. 50B**

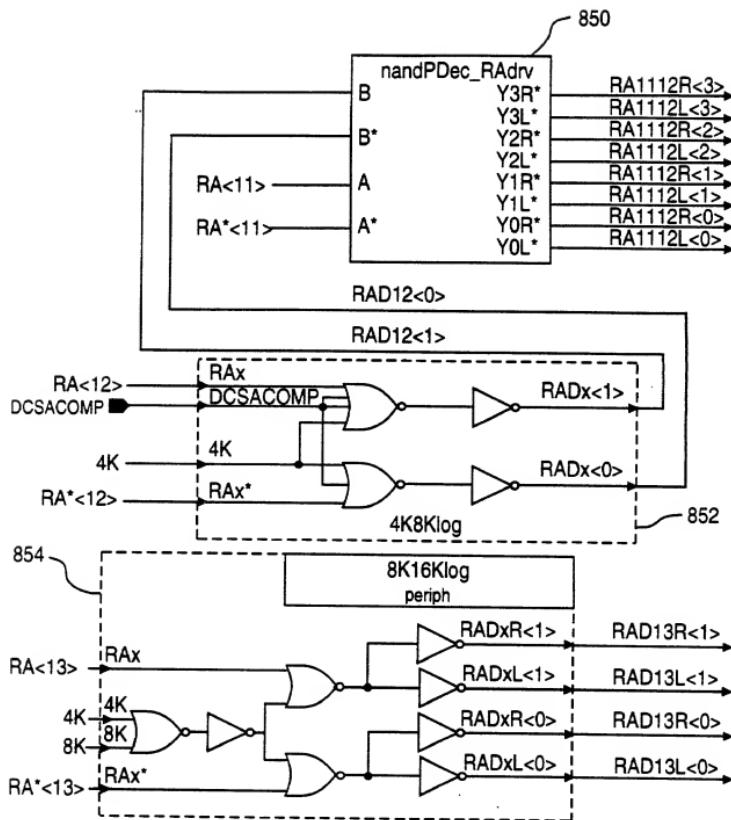


FIG. 50C

654-

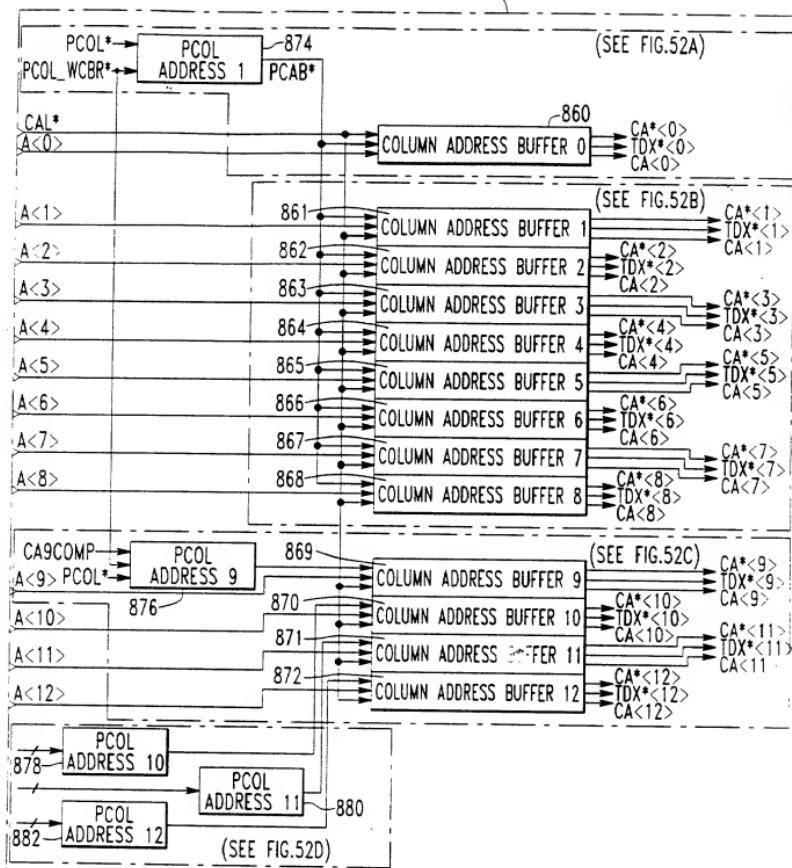


FIG. 51A

-654

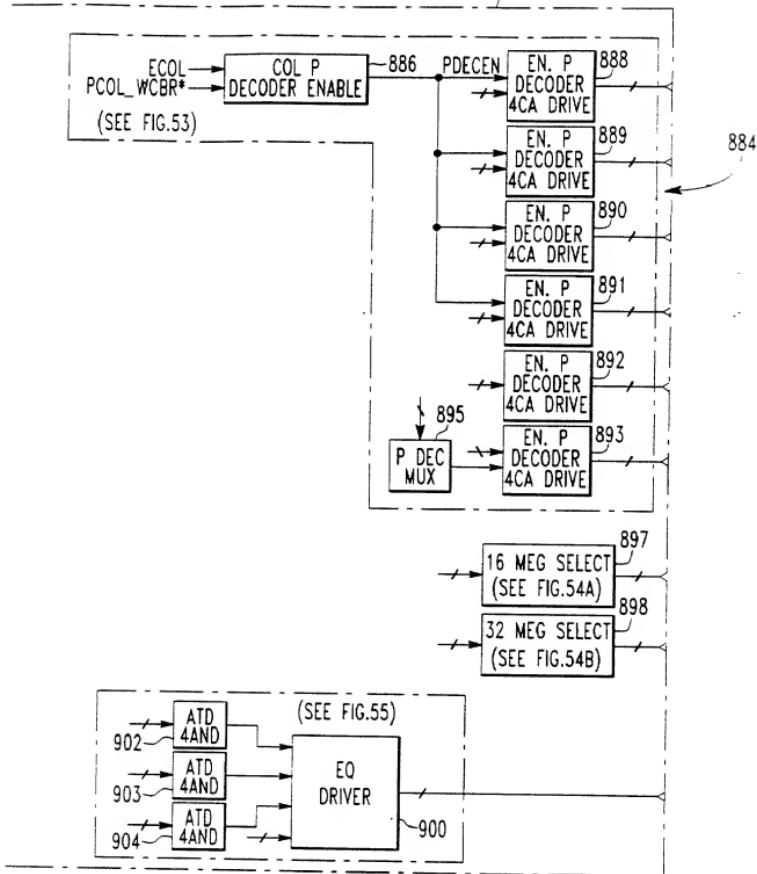
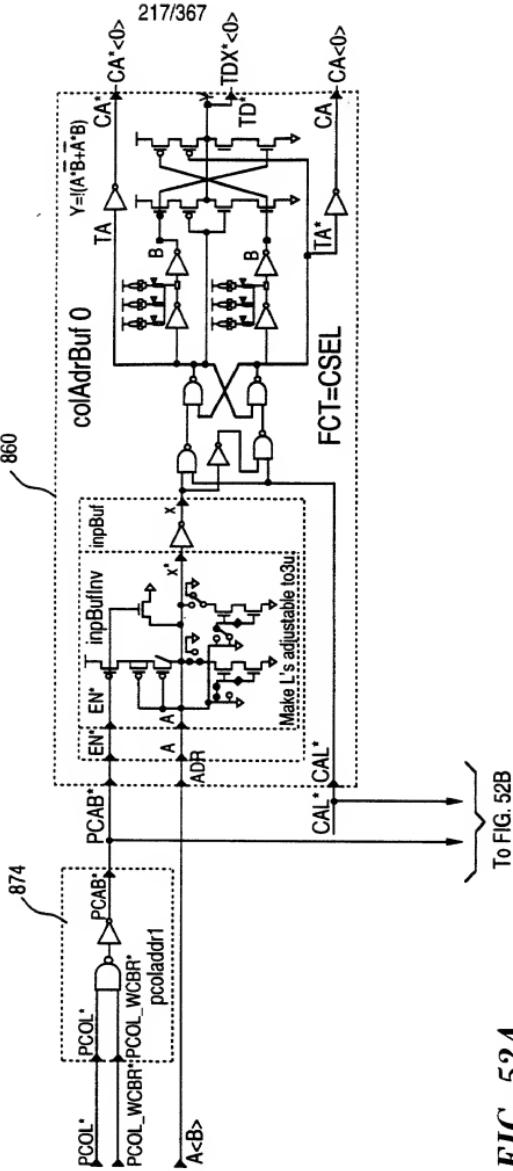


FIG. 51B

**FIG. 52A**

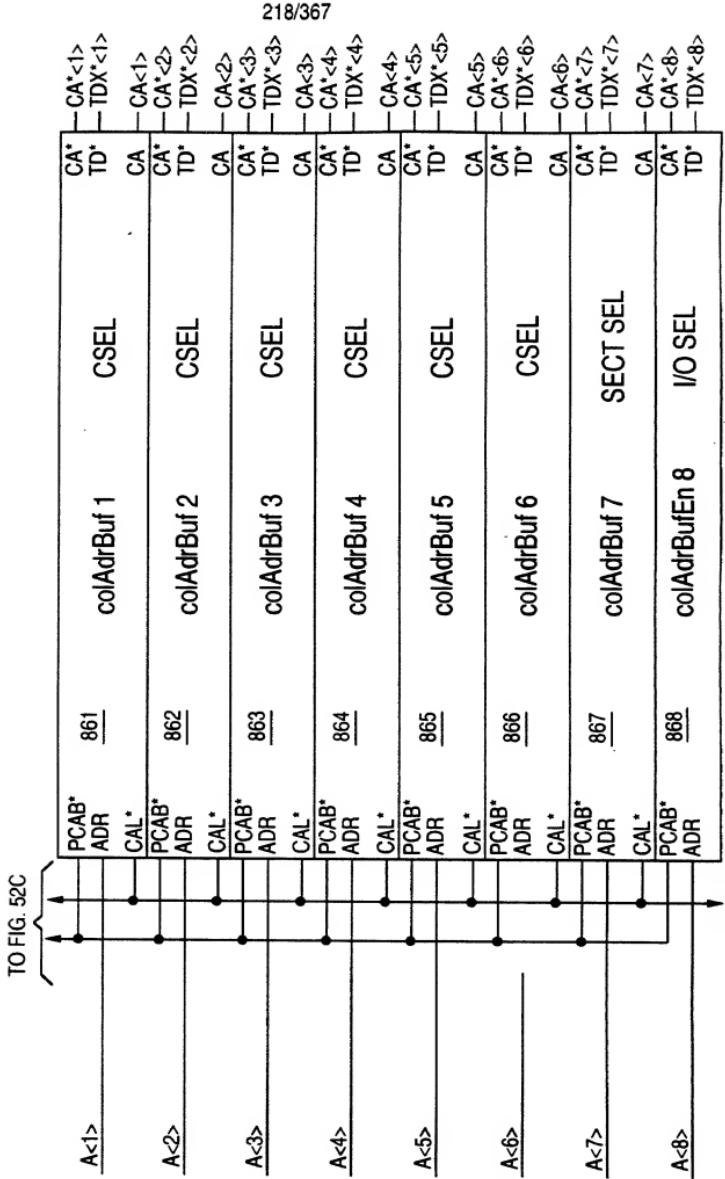


FIG. 52B

TO FIG. 52C

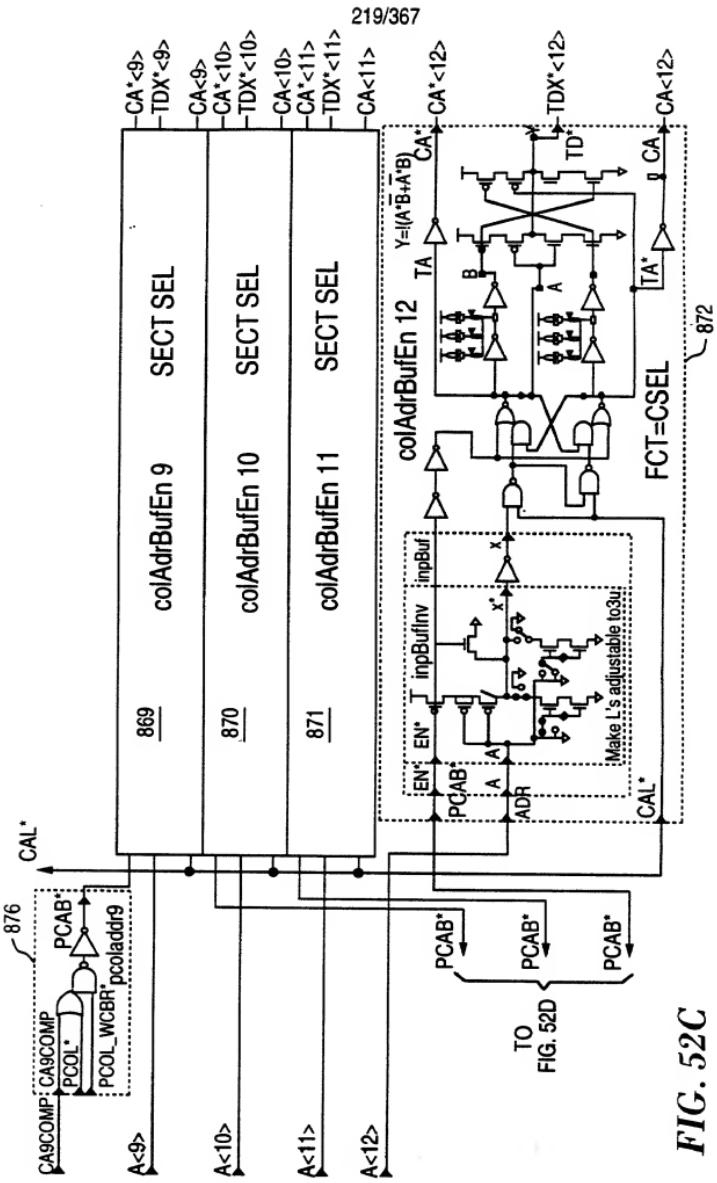
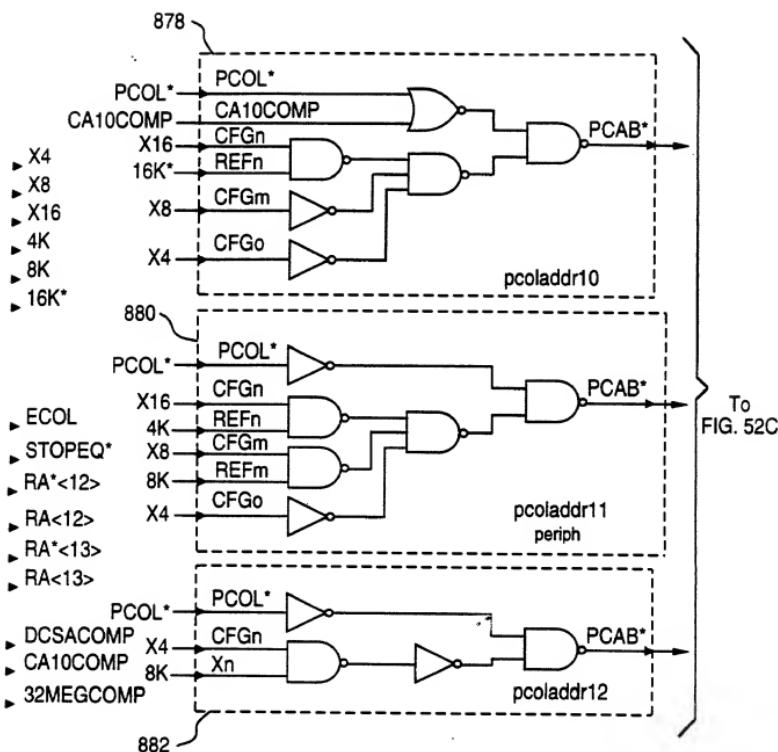


FIG. 52C

**FIG. 52D**

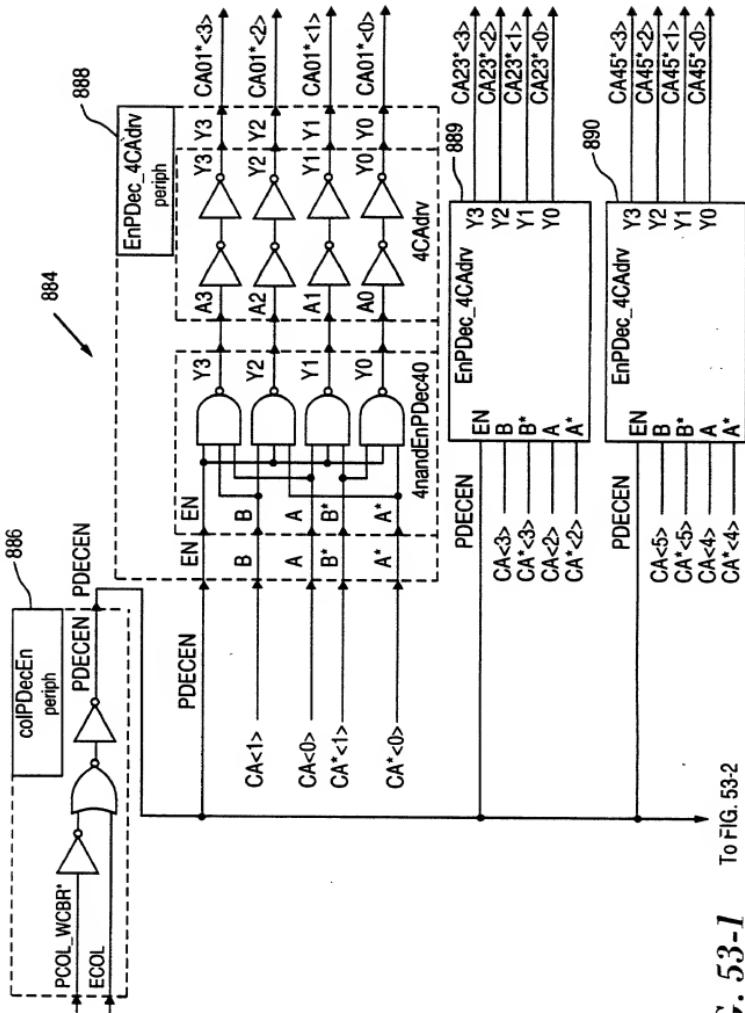
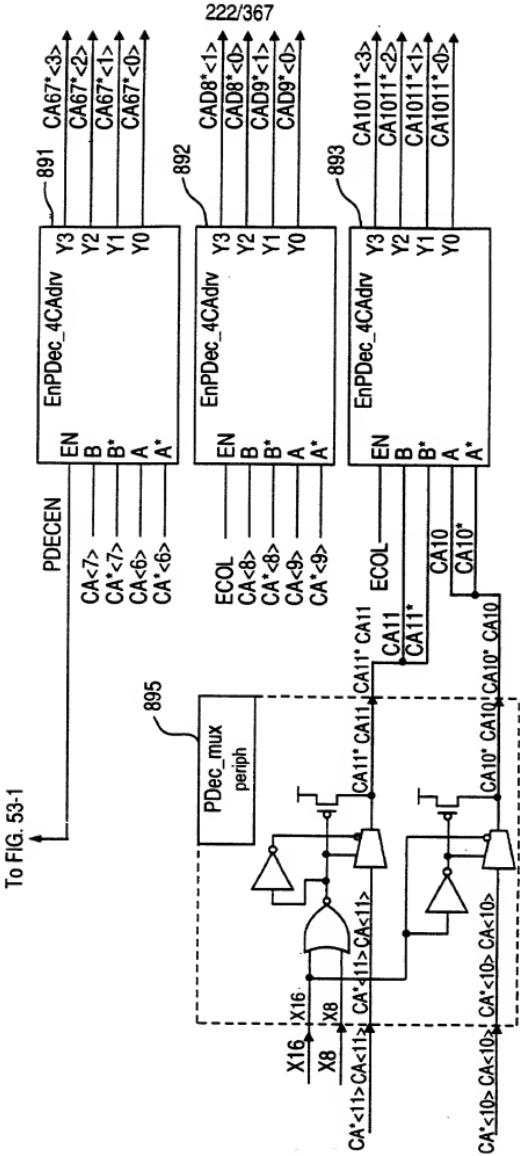


FIG. 53-1 To FIG. 53-2

**FIG. 53-2**

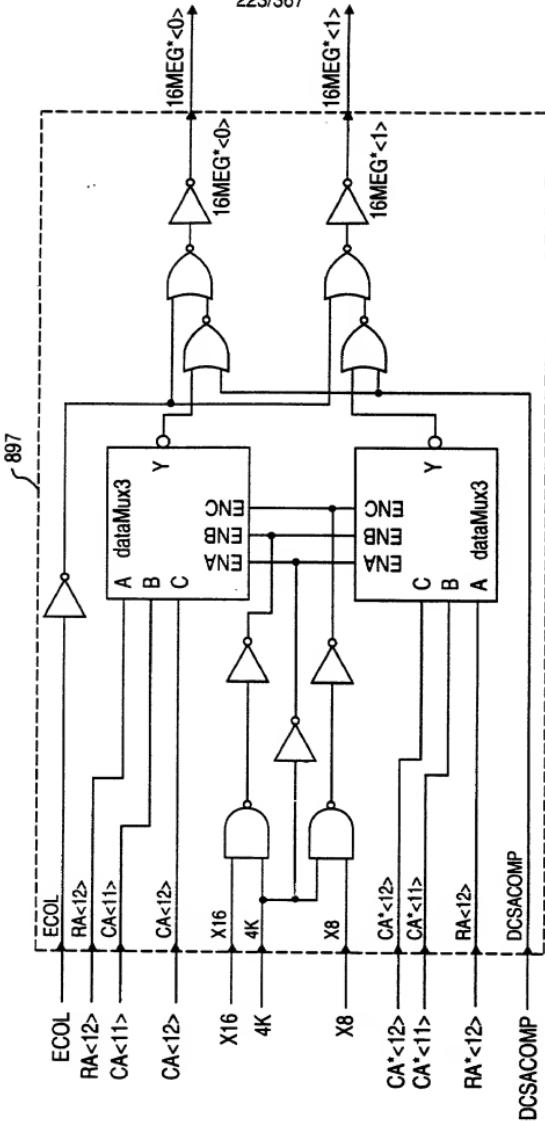
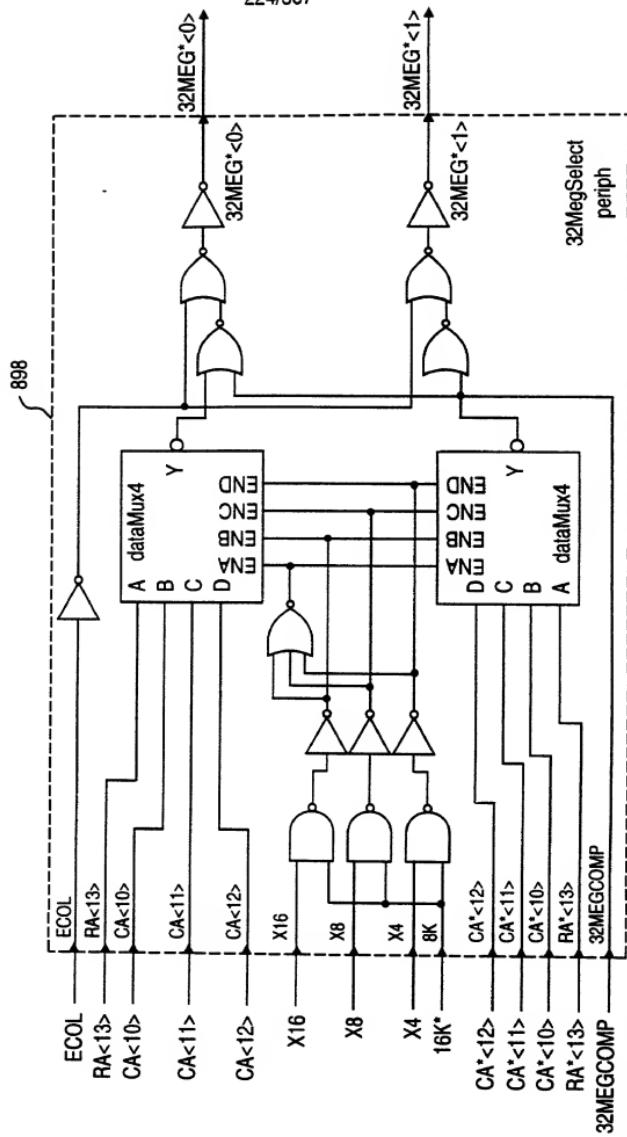


FIG. 54A

**FIG. 54B**

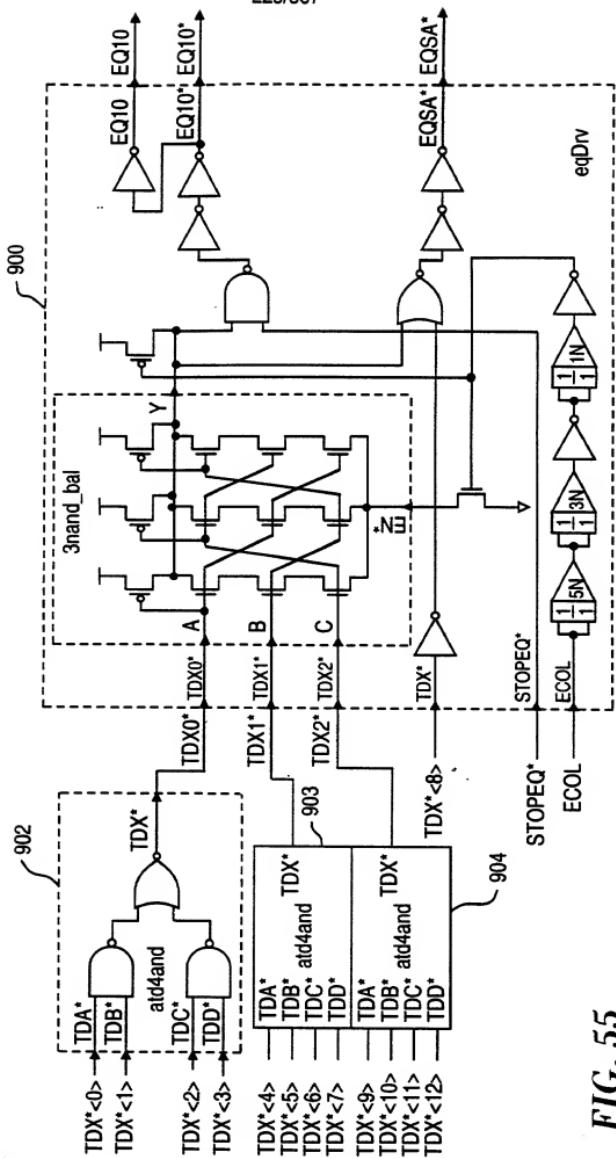


FIG. 55

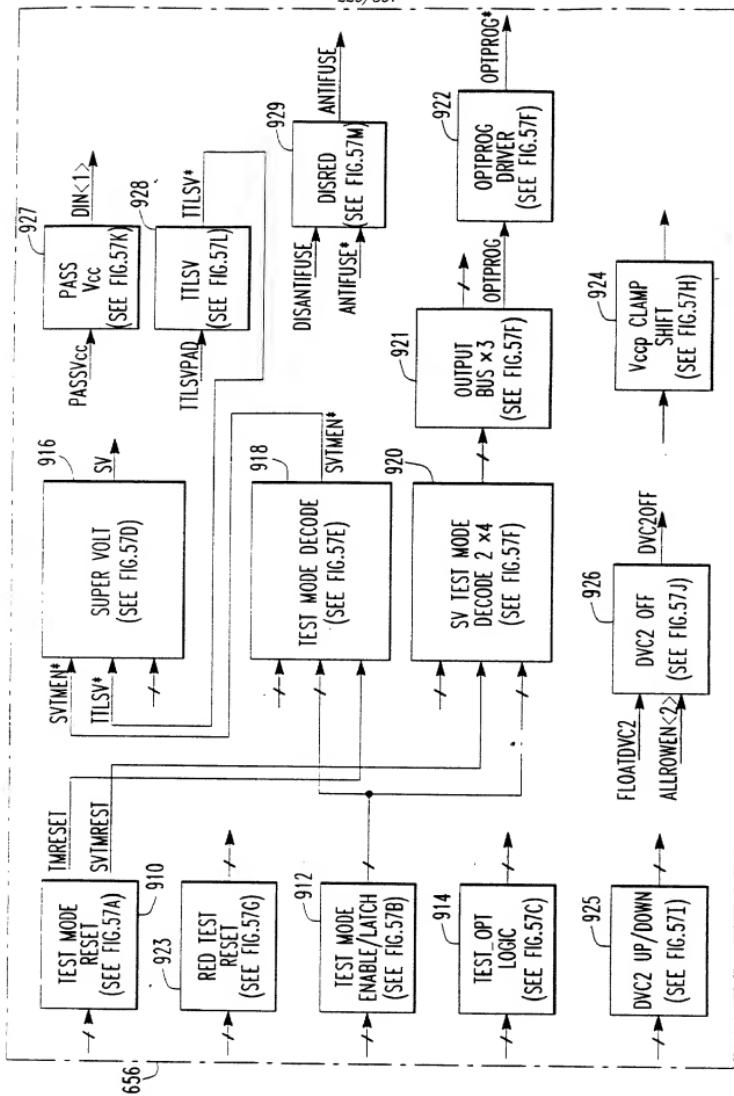


FIG. 56

TOSSED Schematic

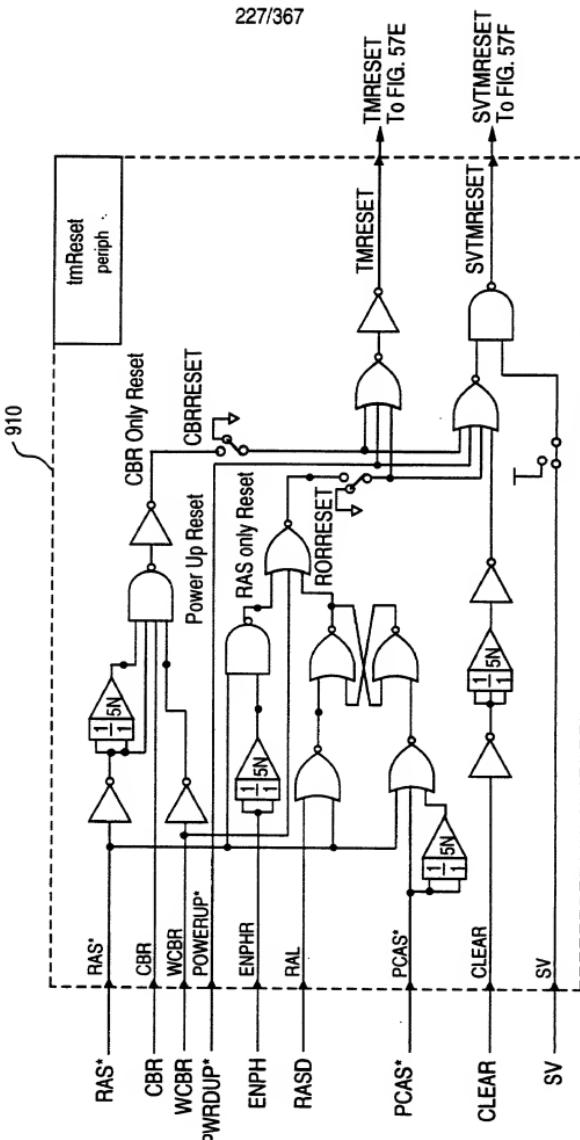


FIG. 57A

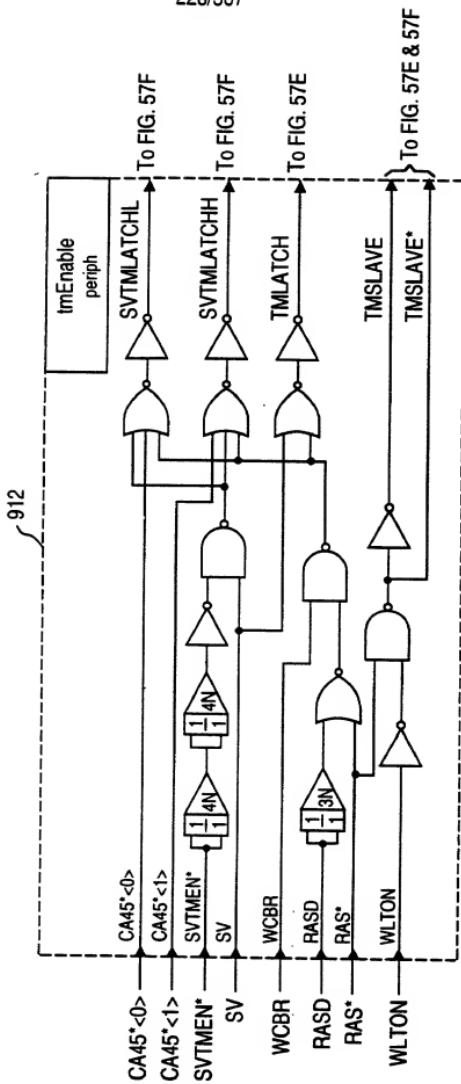


FIG. 57B

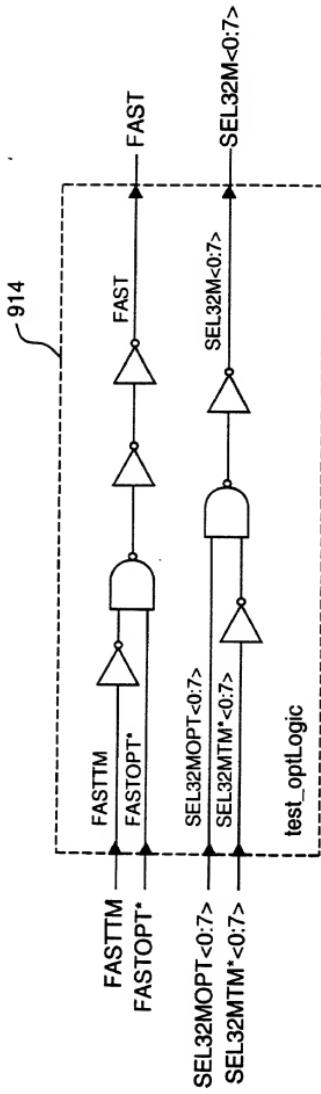


FIG. 57C

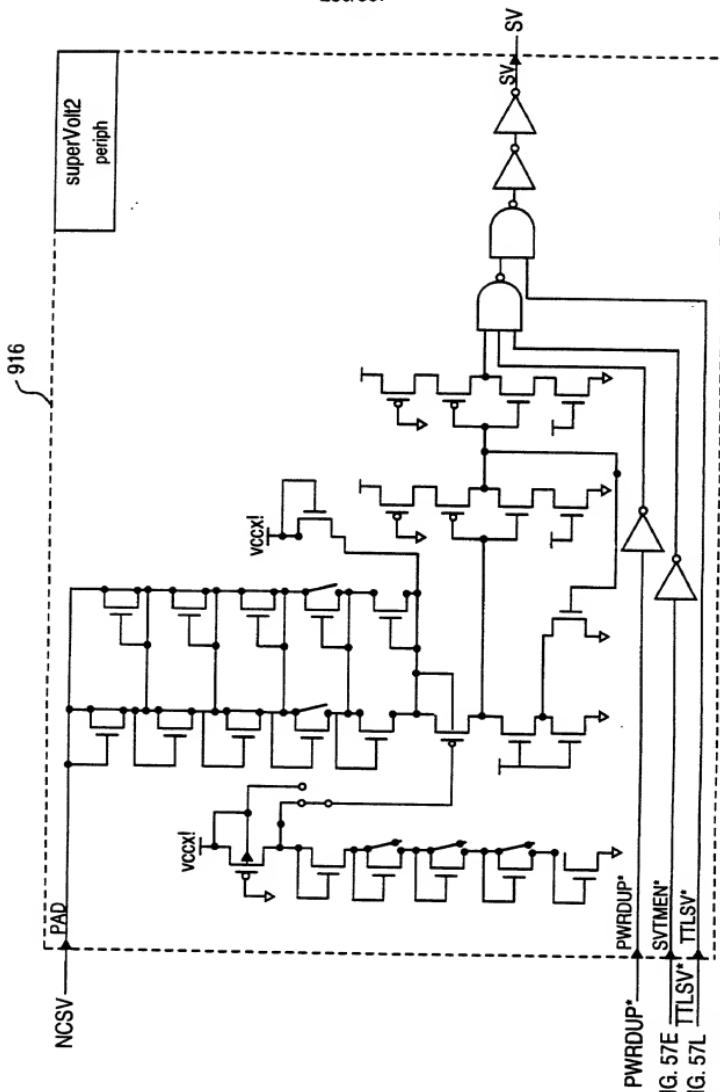


FIG. 57D

From FIG. 57E
From FIG. 57L

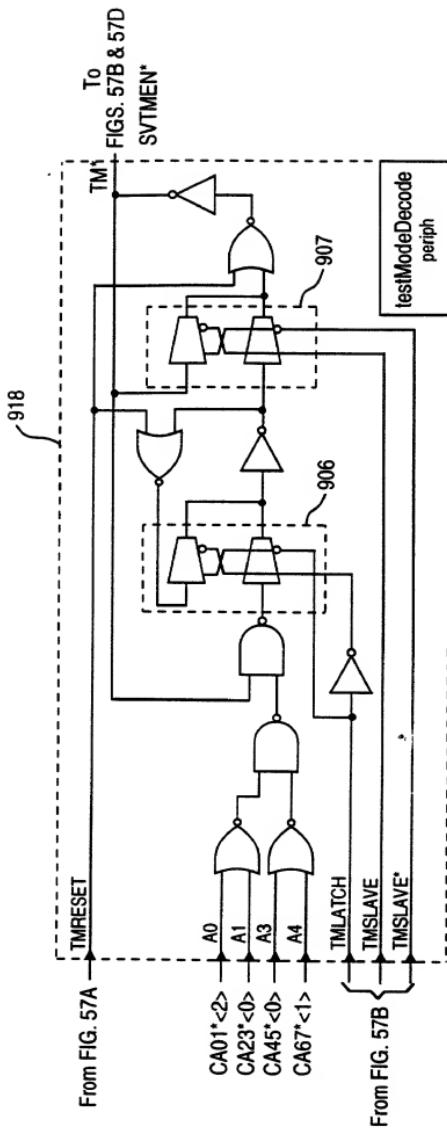
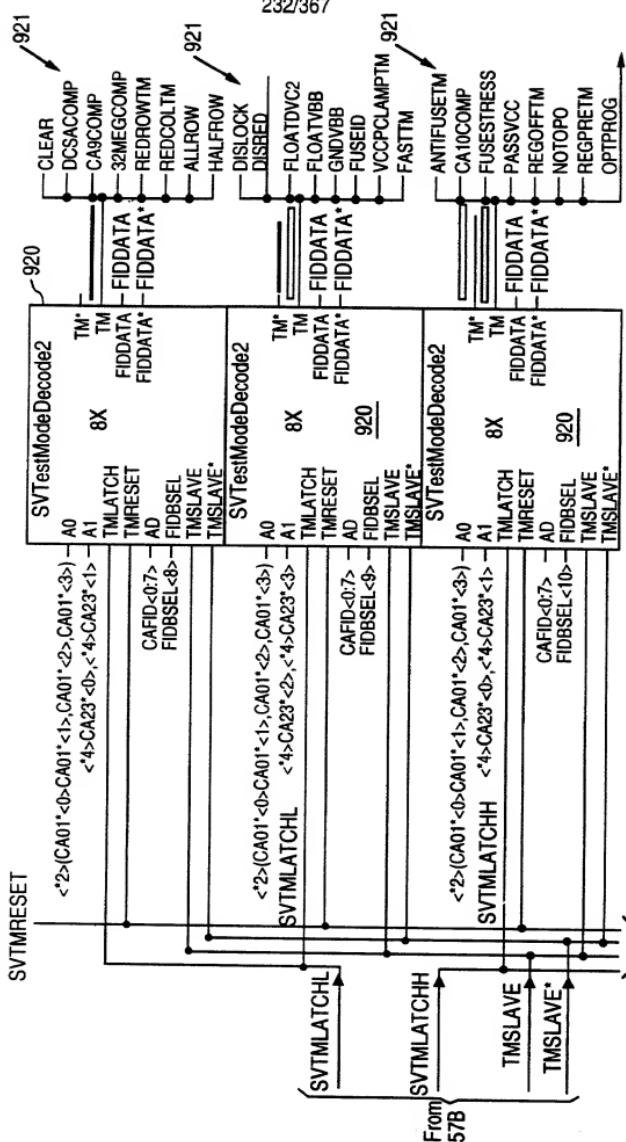
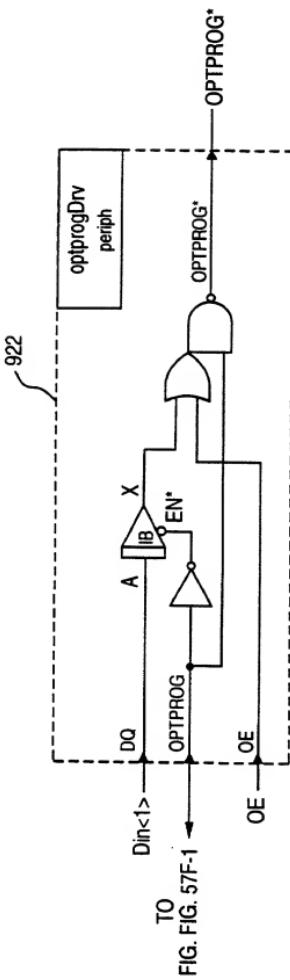


FIG. 57E

**FIG. 57F-1****FIG. 57F-3****FIG. 57F-2**

**FIG. 57F-2**

To
FIG. 57F-1

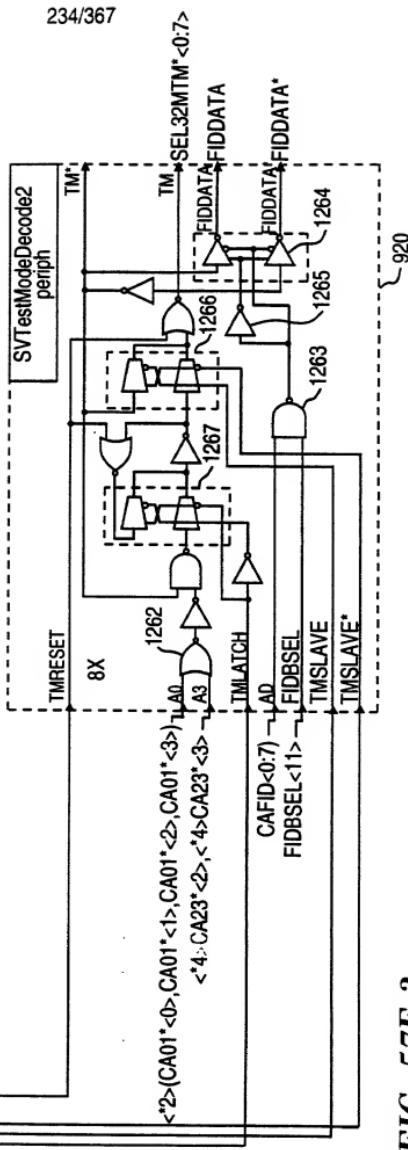


FIG. 57F-3

920

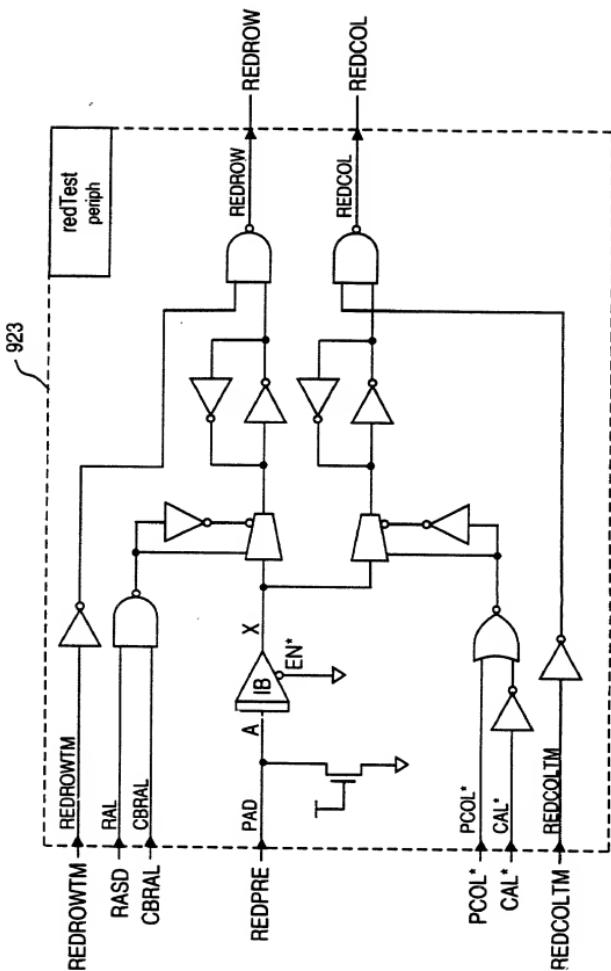


FIG. 57G

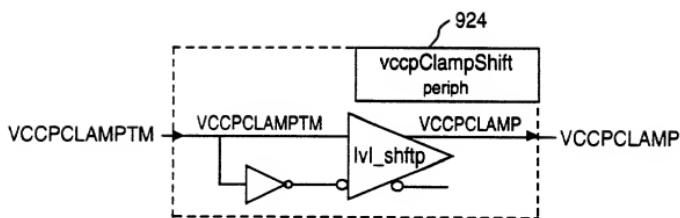


FIG. 57H

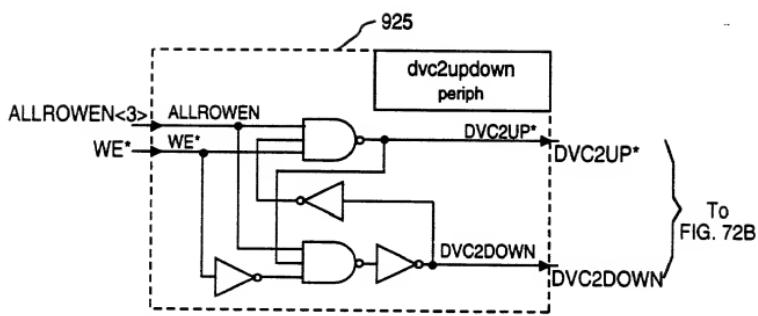


FIG. 57I

To
FIG. 72B

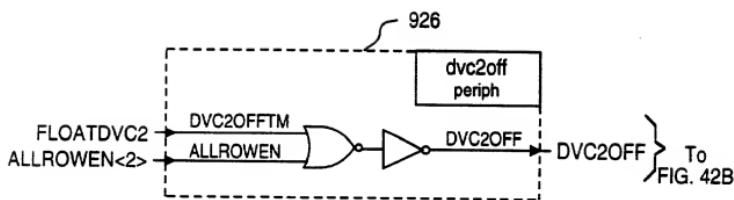


FIG. 57J

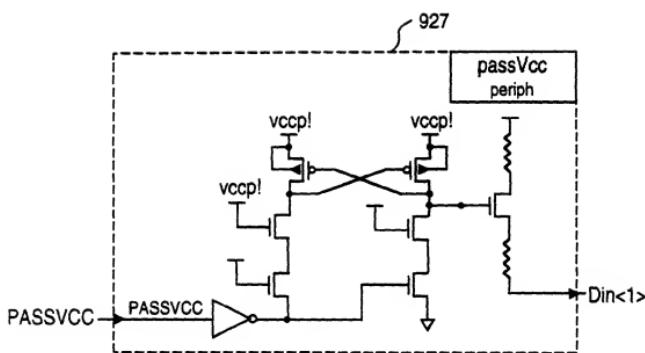


FIG. 57K

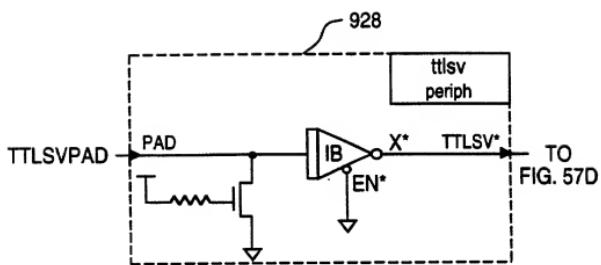


FIG. 57L

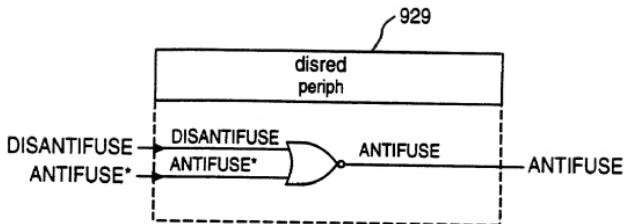


FIG. 57M

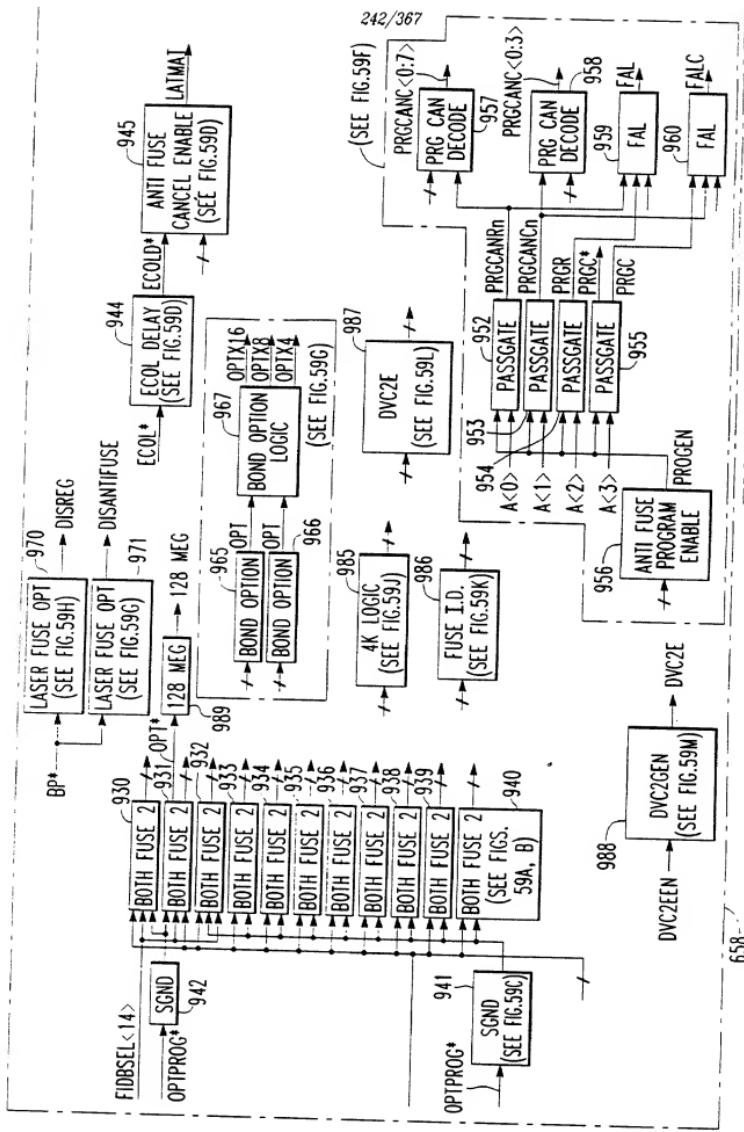


FIG. 58A

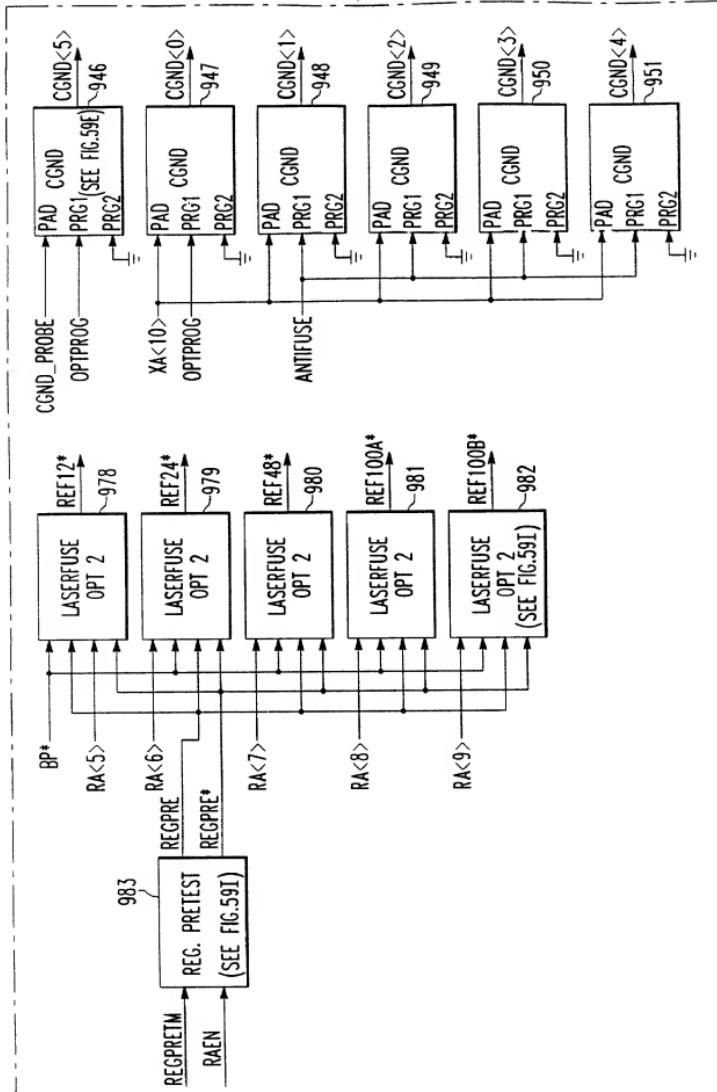


FIG. 58B

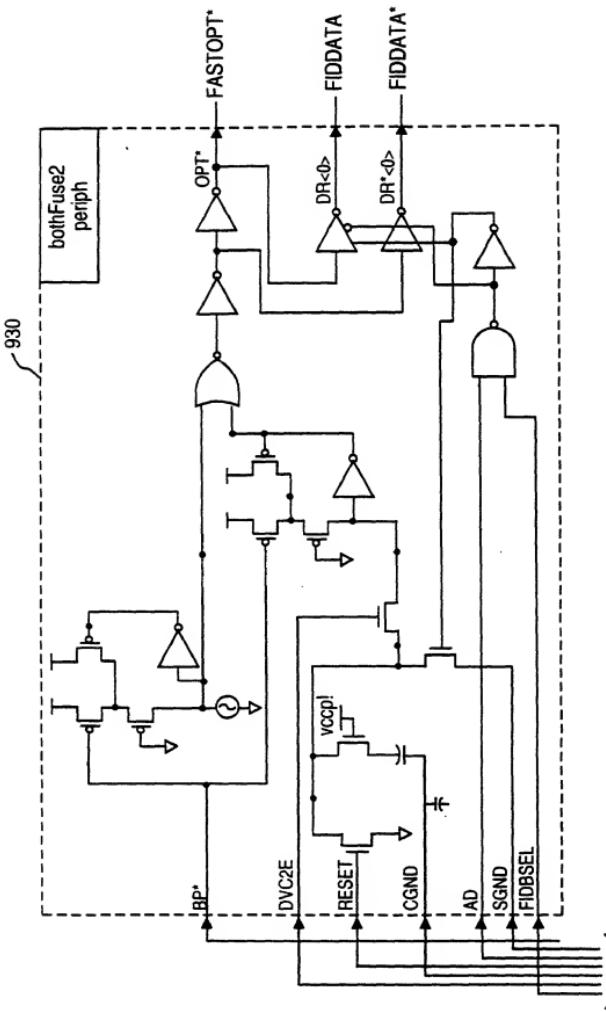
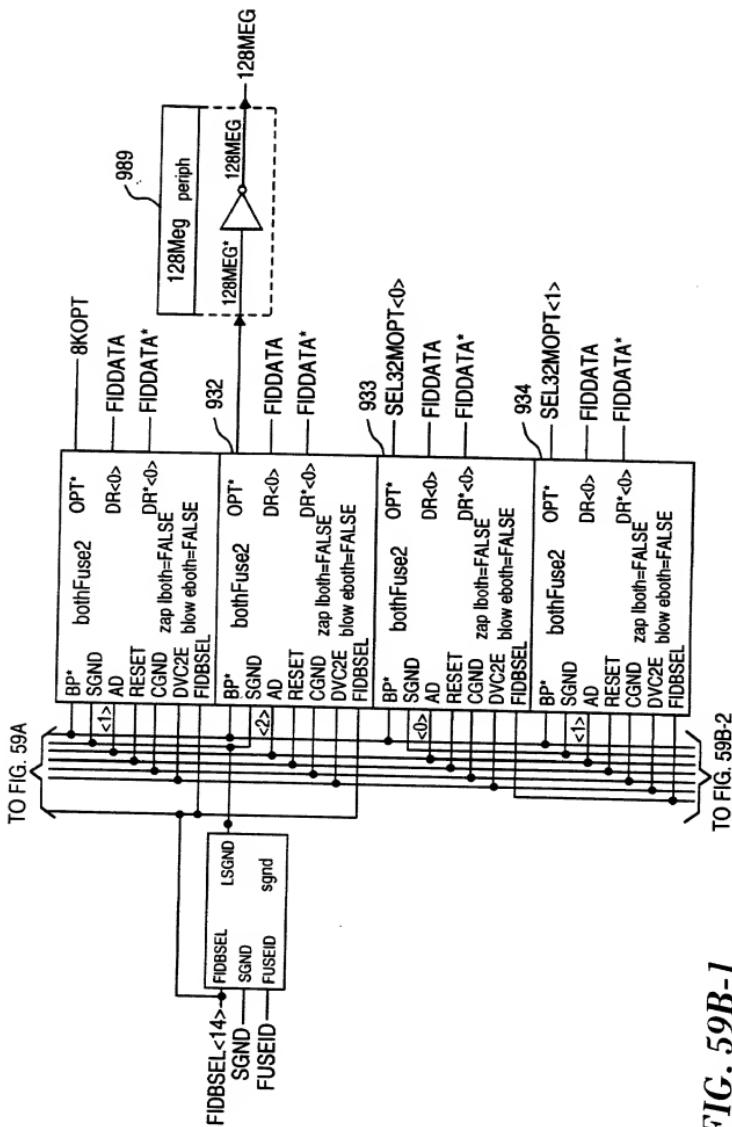


FIG. 59A



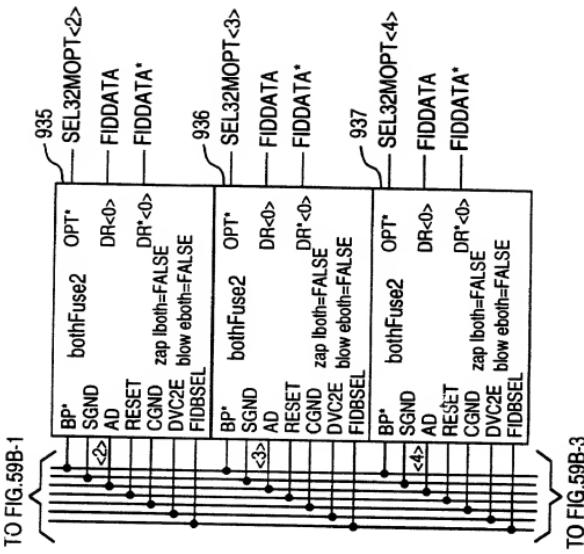


FIG. 59B-2

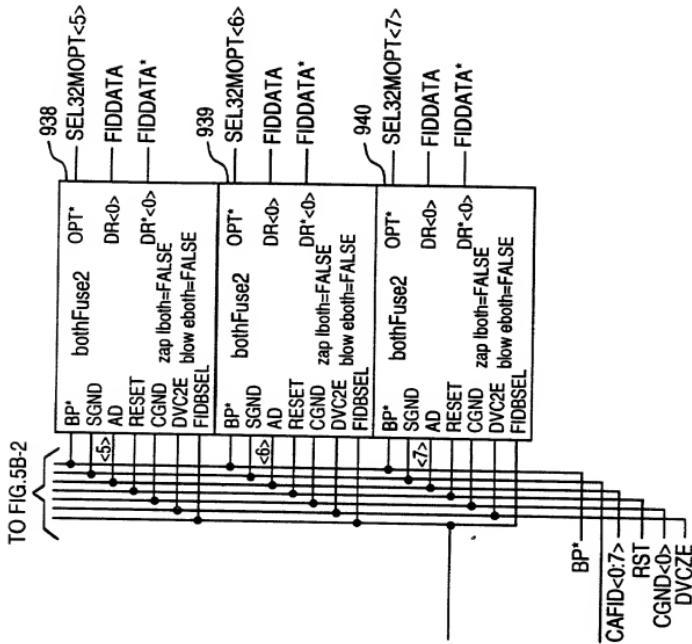


FIG. 59B-3

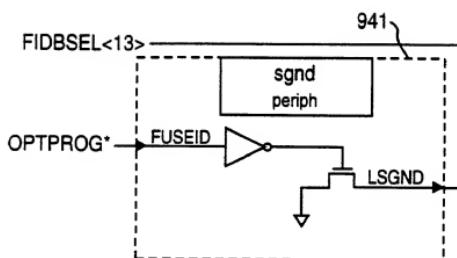


FIG. 59C

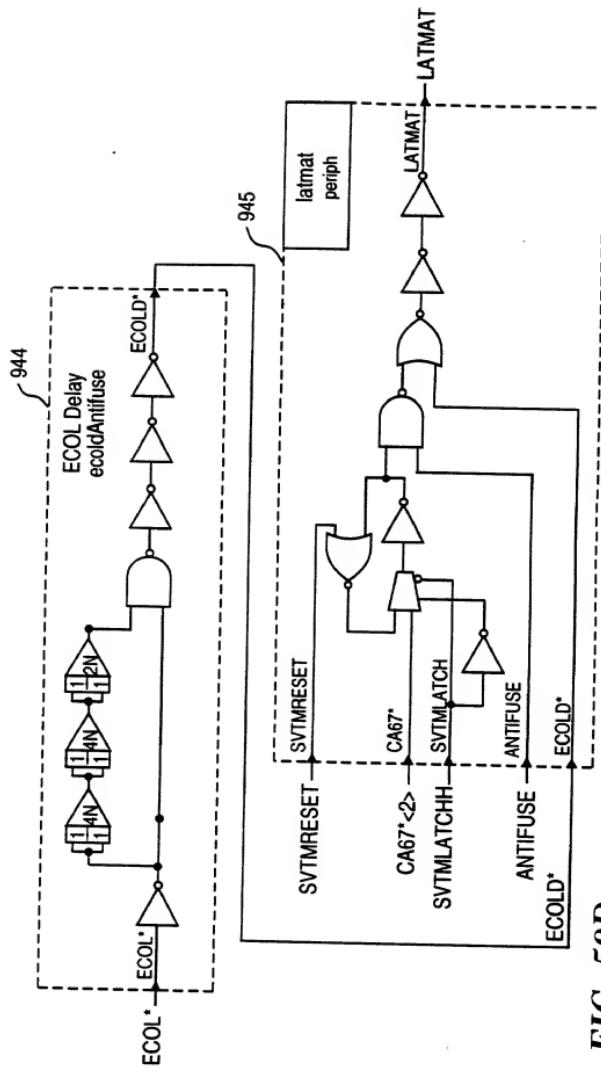


FIG. 59D

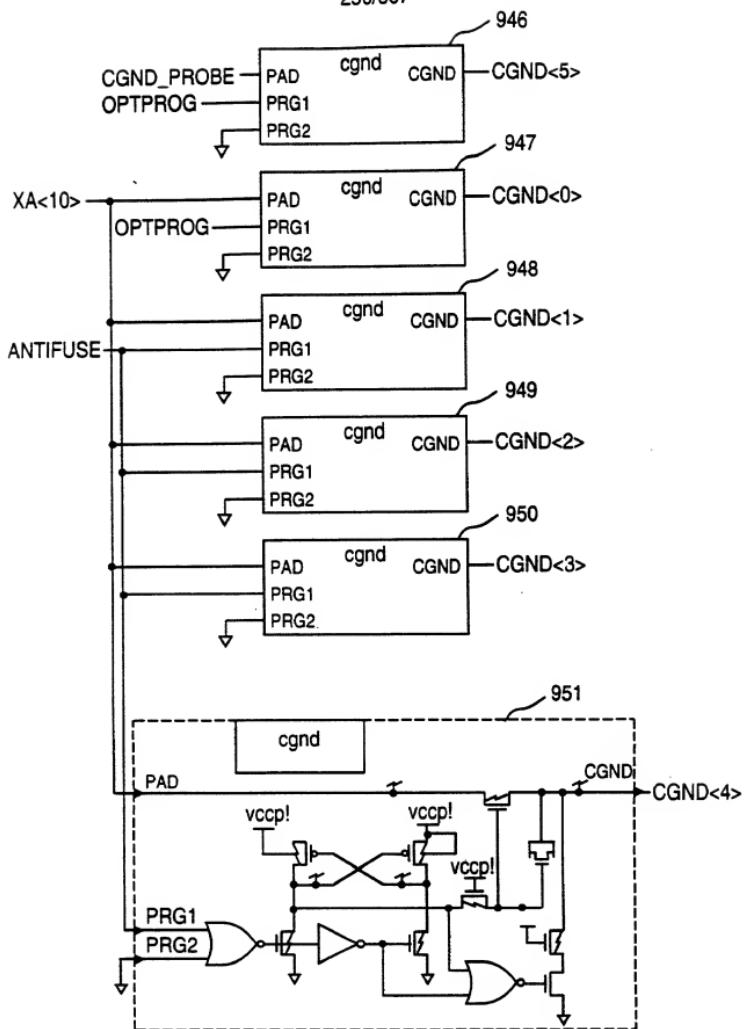


FIG. 59E

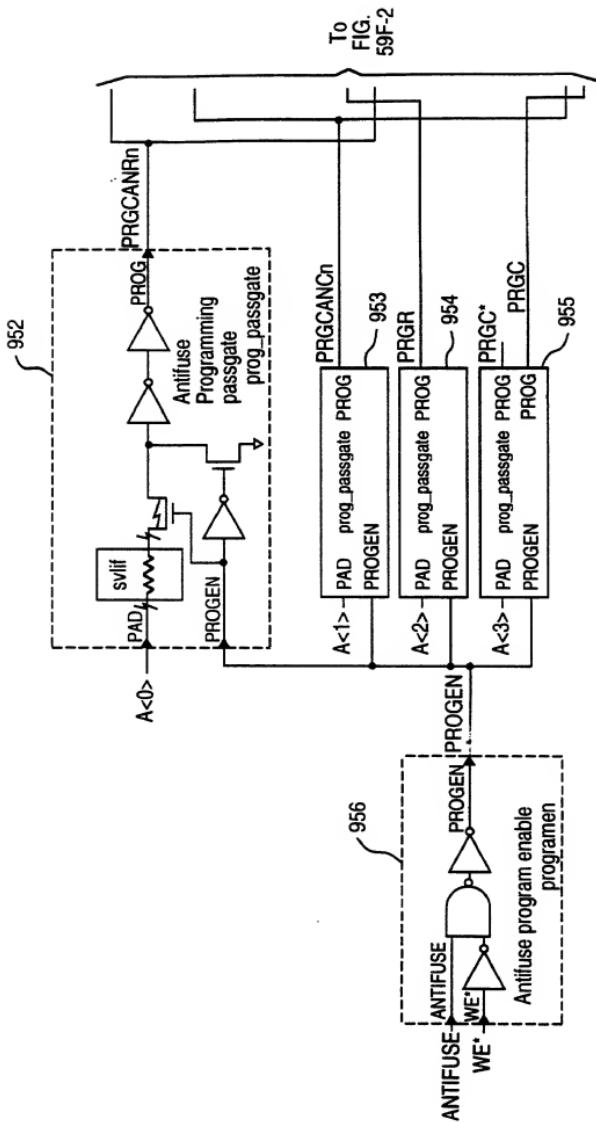
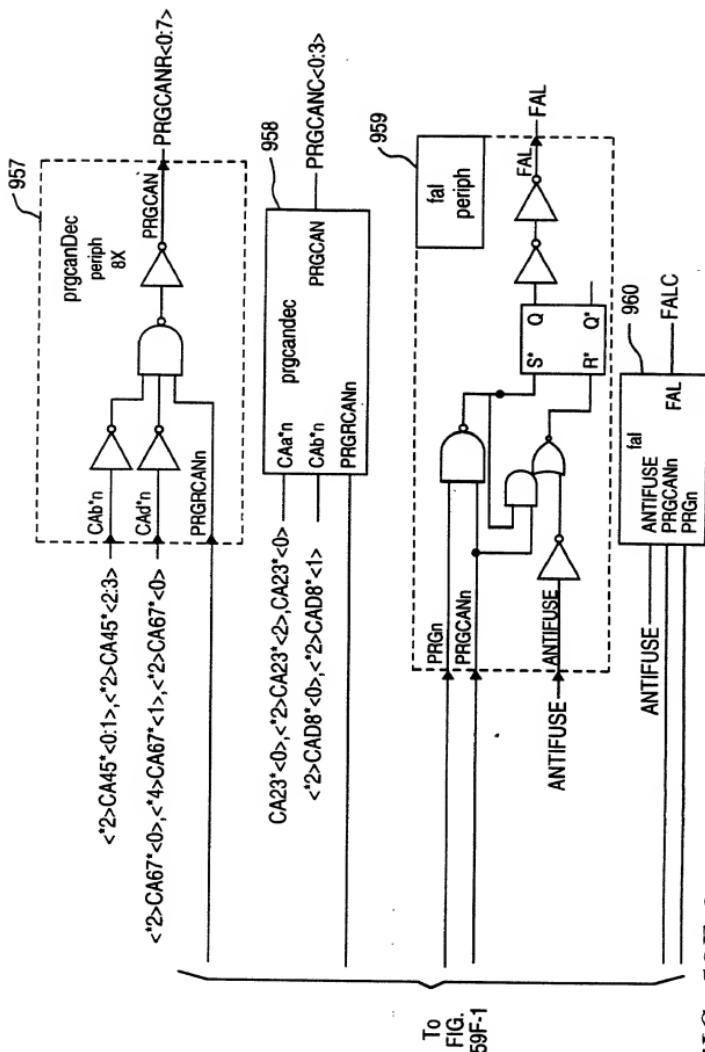


FIG. 59F-1



To
FIG.
59F-1

FIG. 59F-2

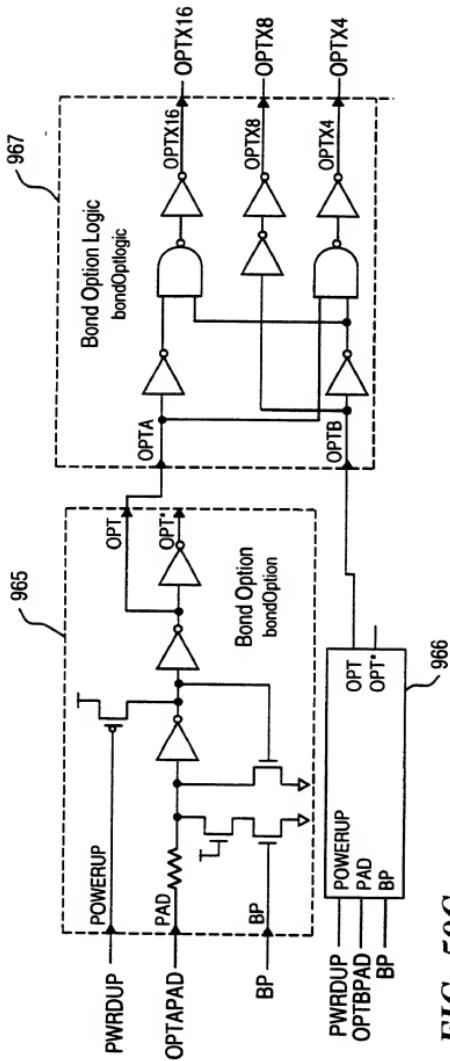


FIG. 59G

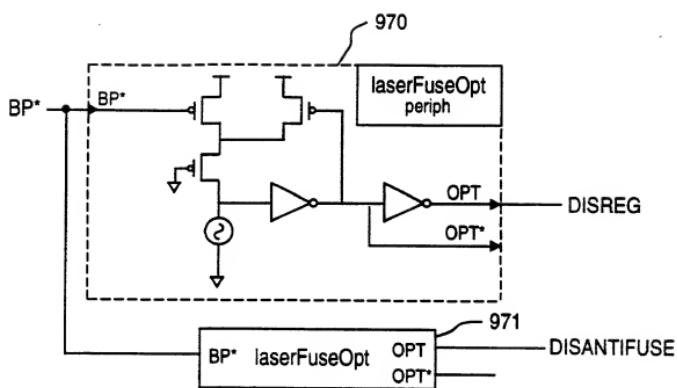


FIG. 59H

T082330 "6866660

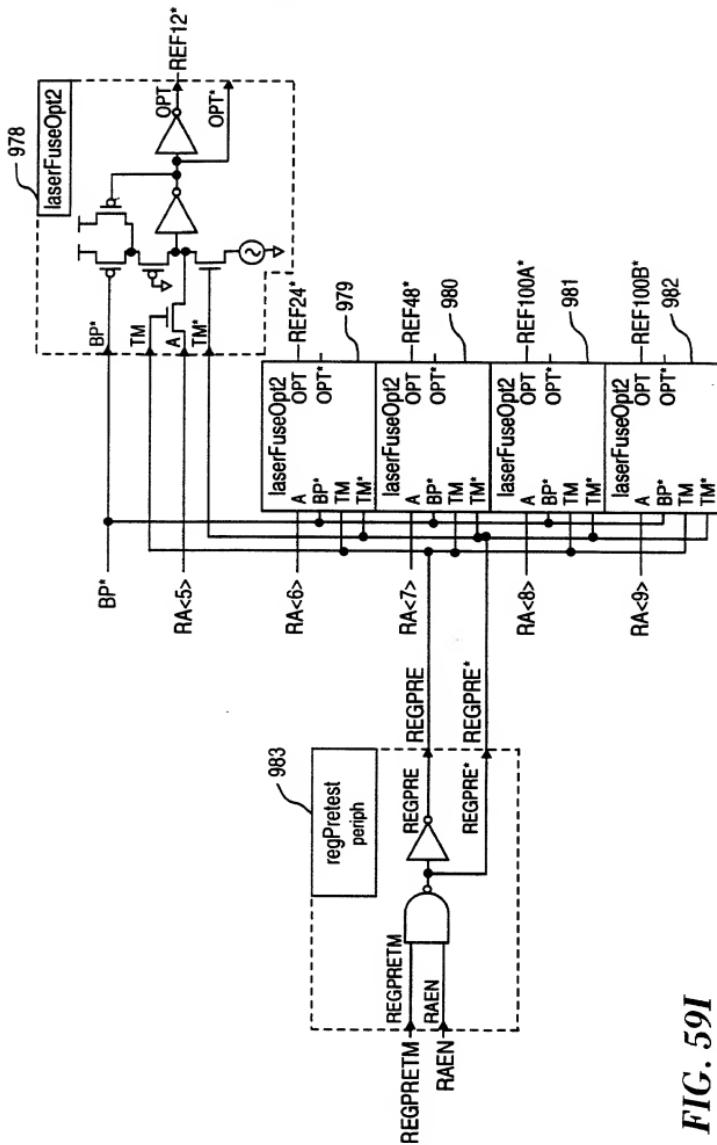


FIG. 59I

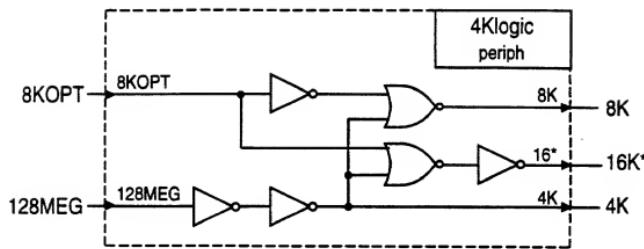
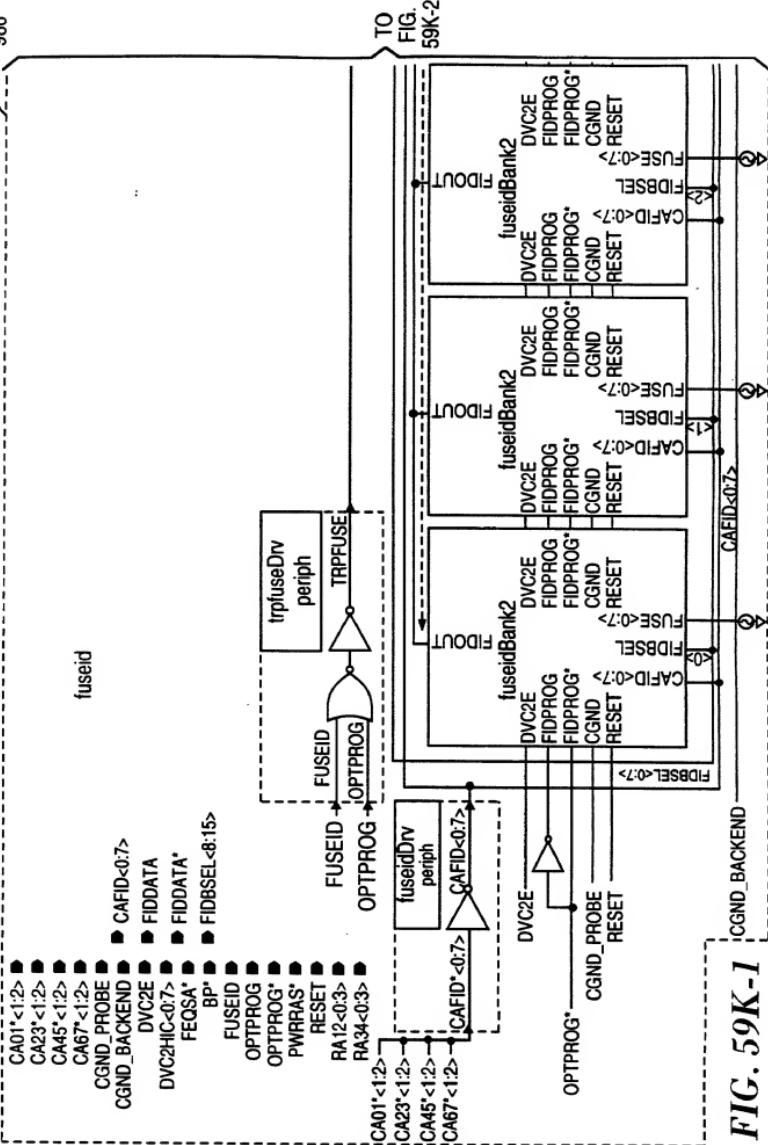
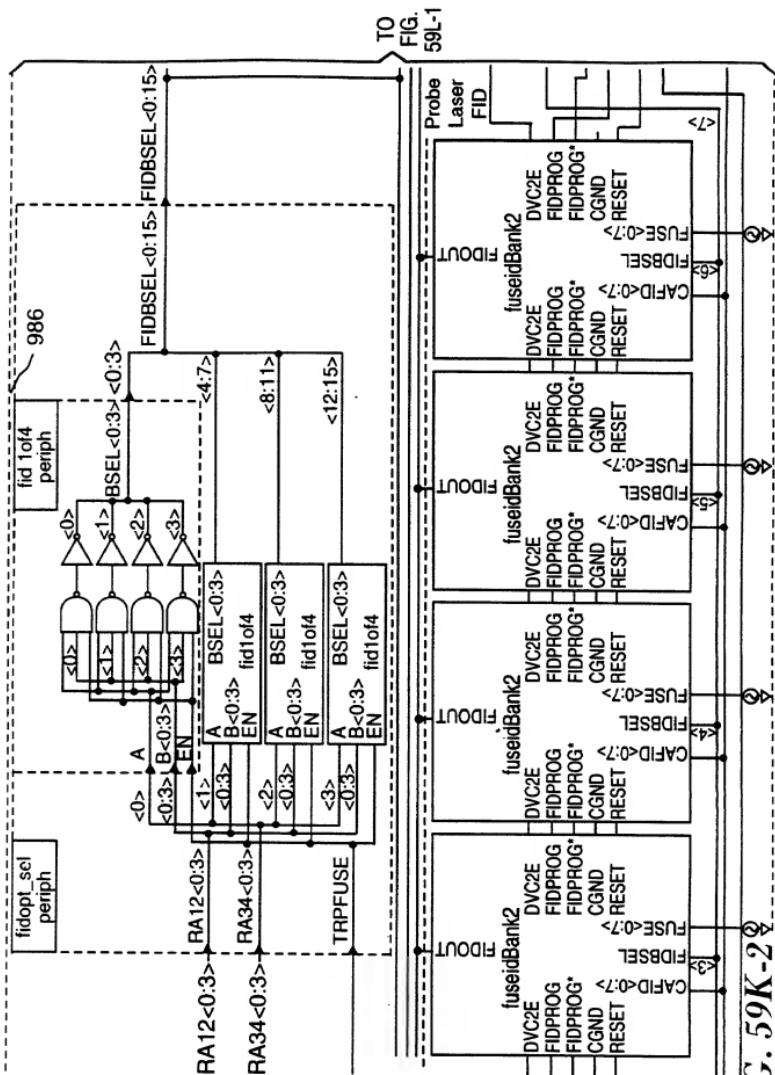


FIG. 59J

0982339-052214



**FIG. 59K-2**

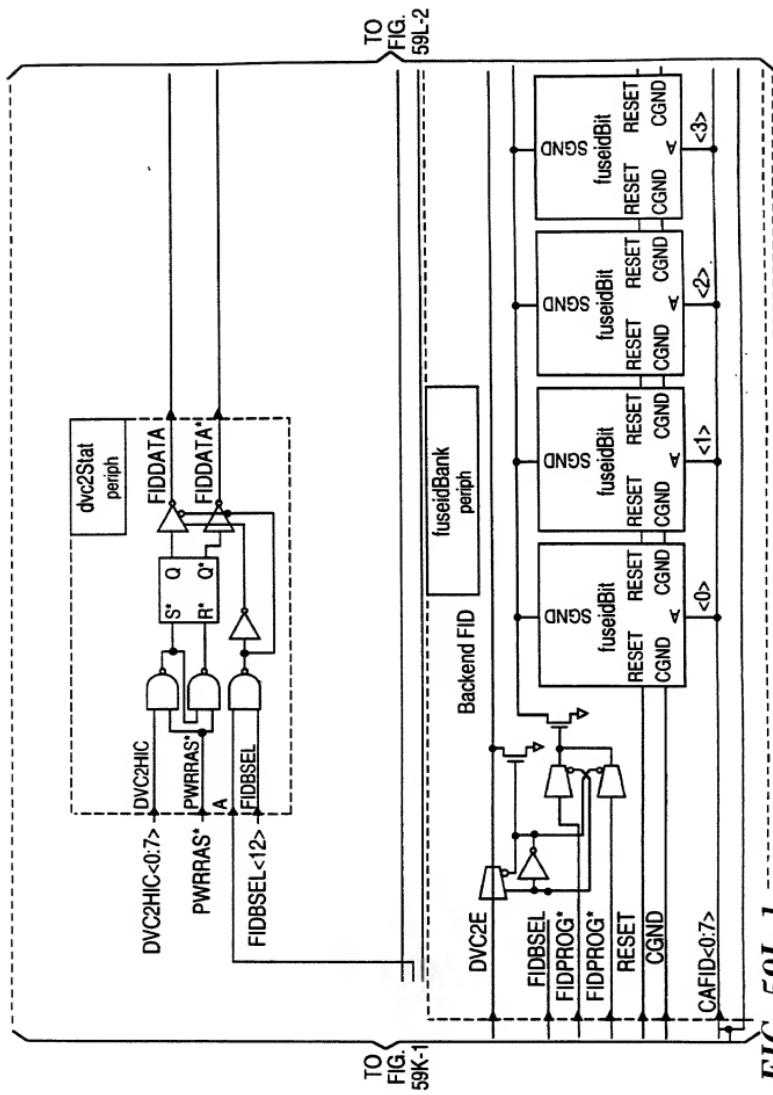


FIG. 59L-1

986

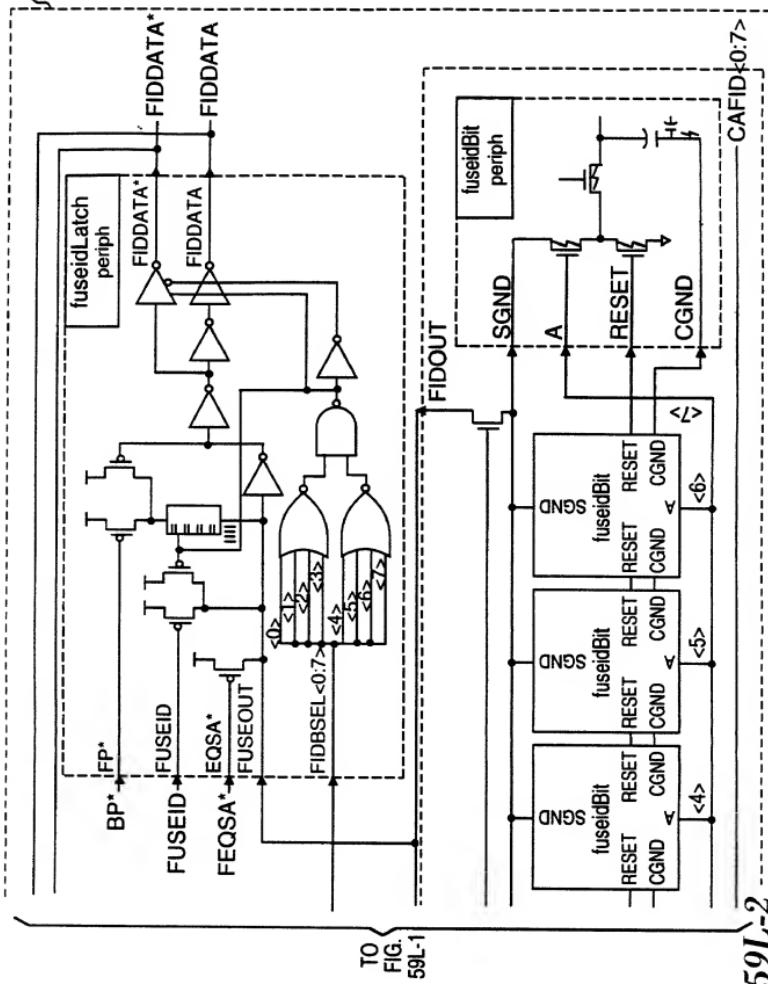
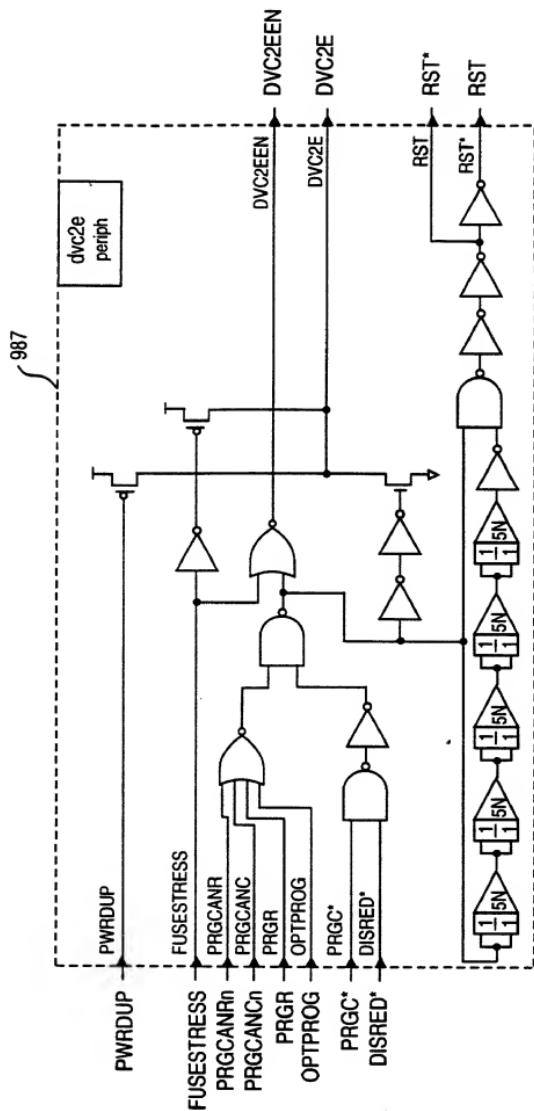
TO
FIG.
59L-1

FIG. 59L-2

X96290 = 58556660



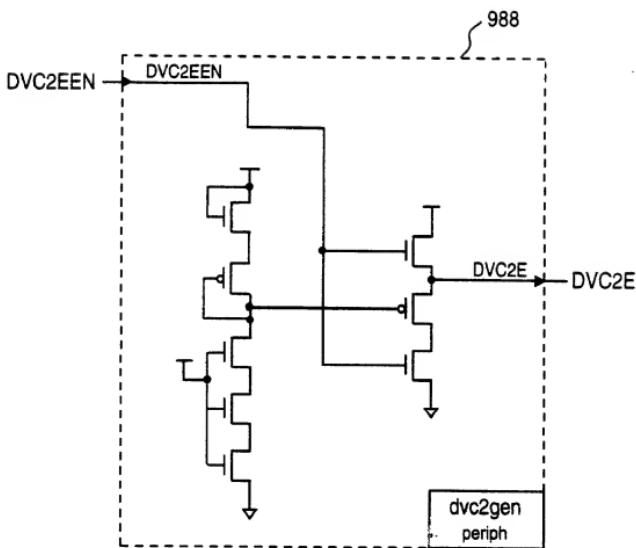


FIG. 59N

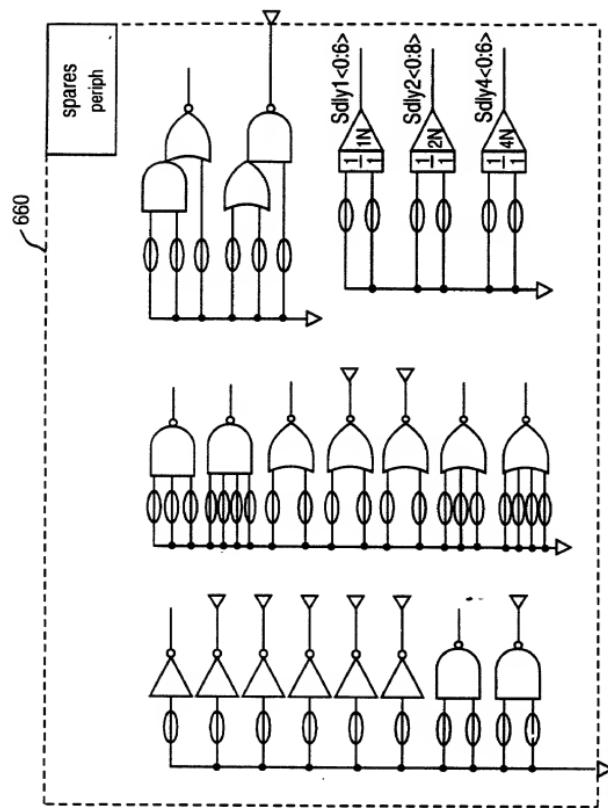


FIG. 590

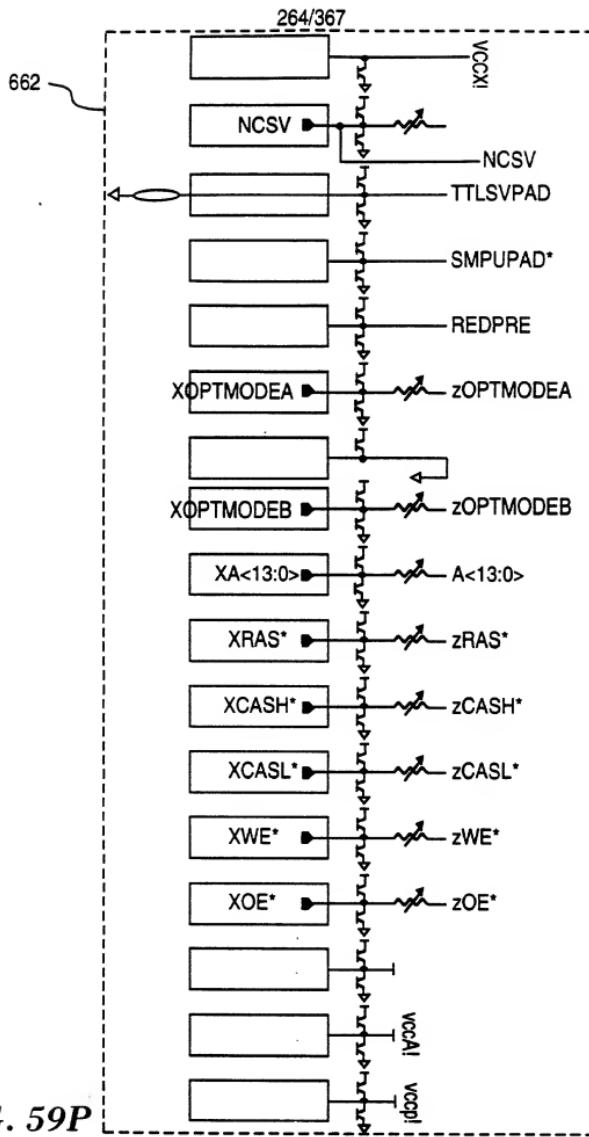


FIG. 59P

09993339.062234

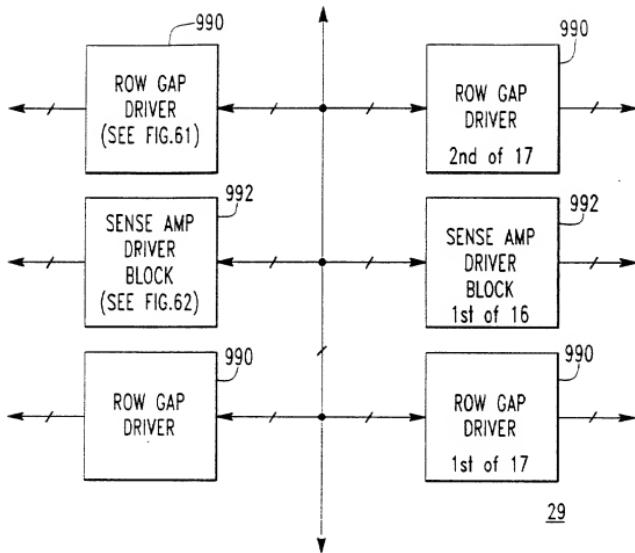


FIG. 60

338933939

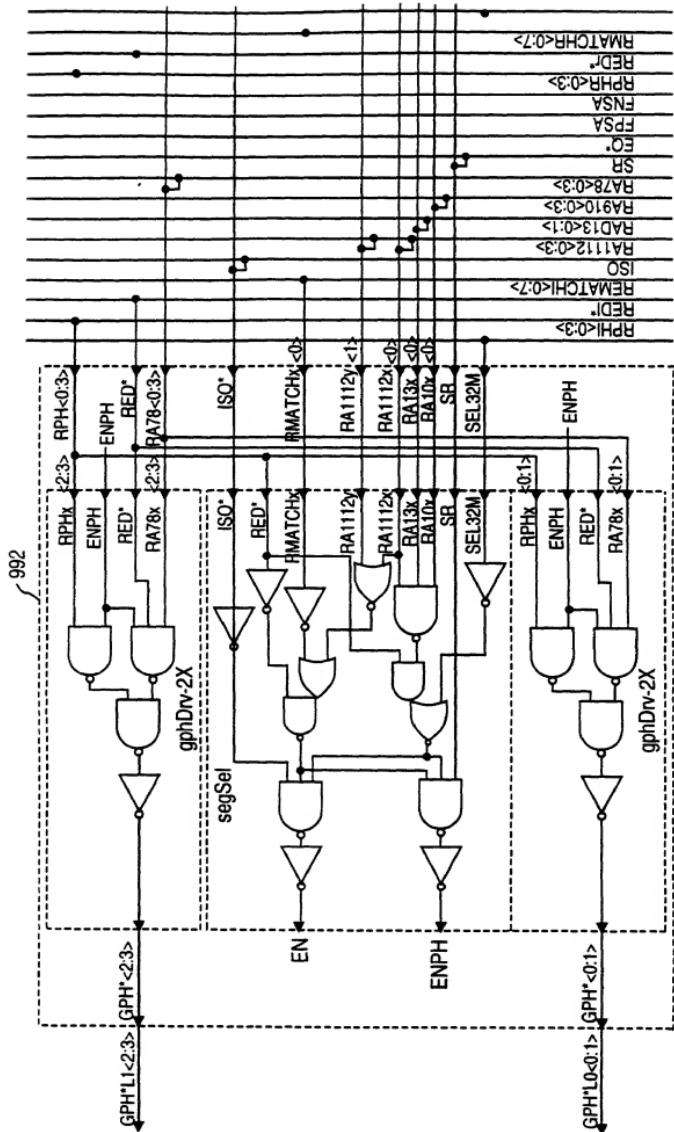


FIG. 61

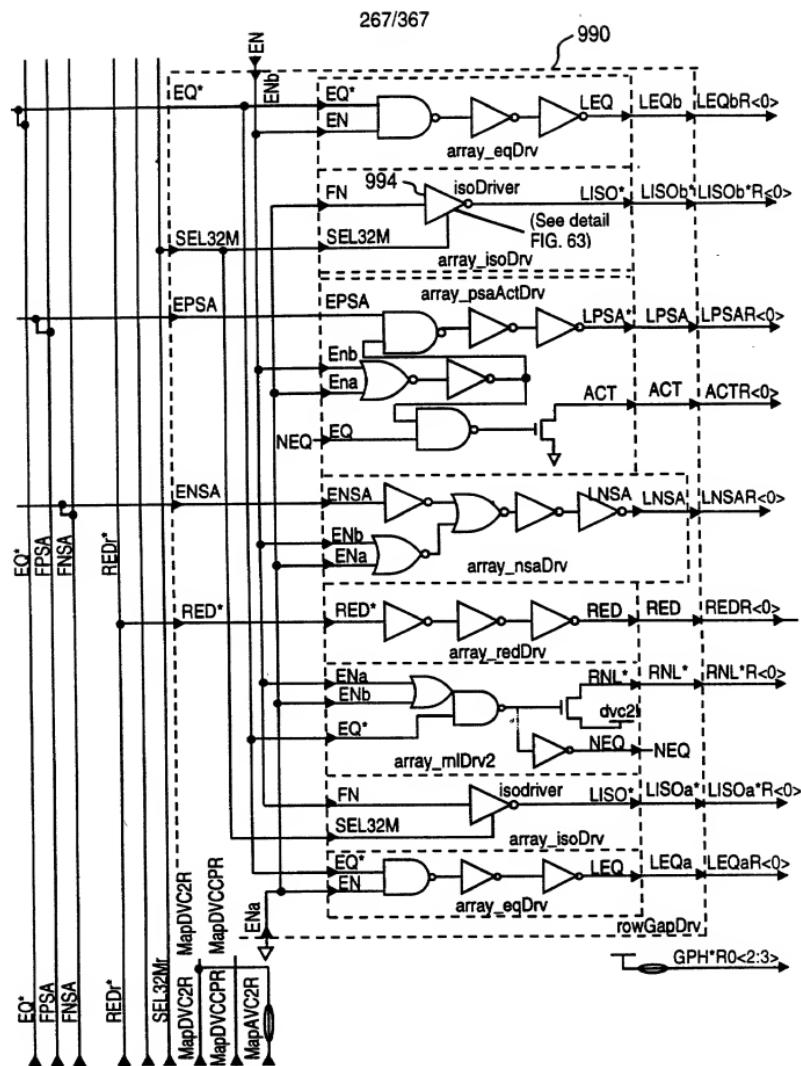


FIG. 62

T38230 "68EE6860

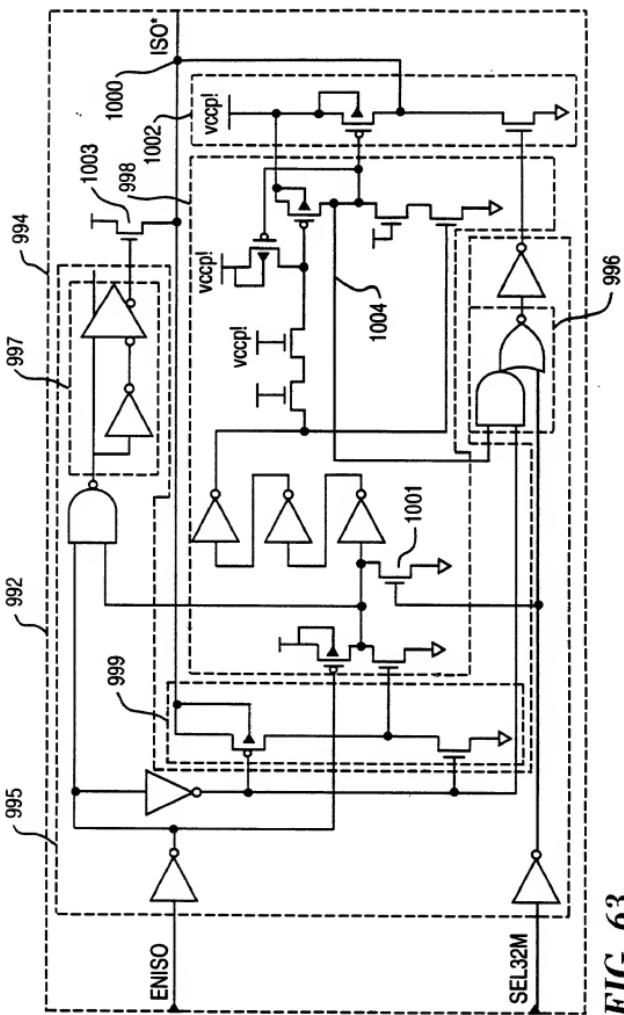


FIG. 63

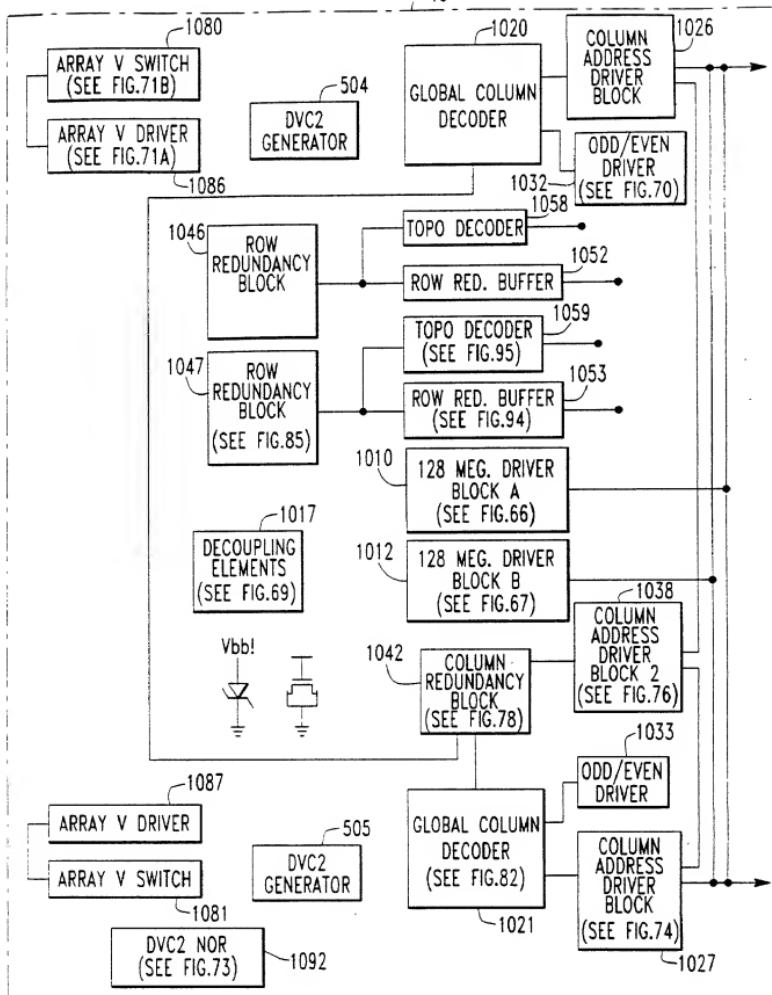


FIG. 64A

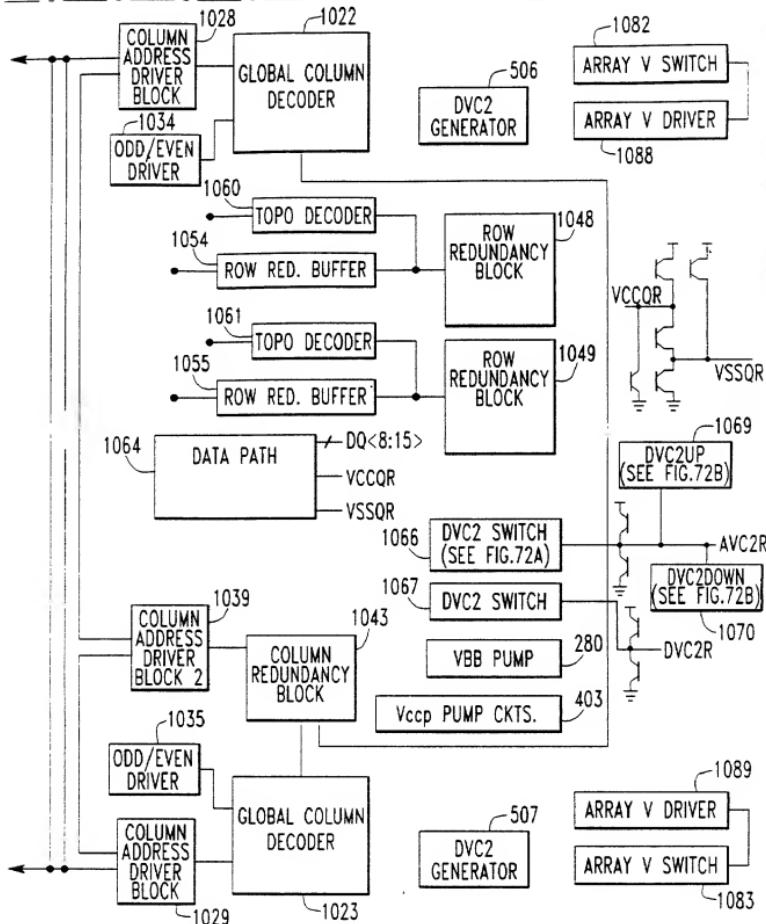


FIG. 64B

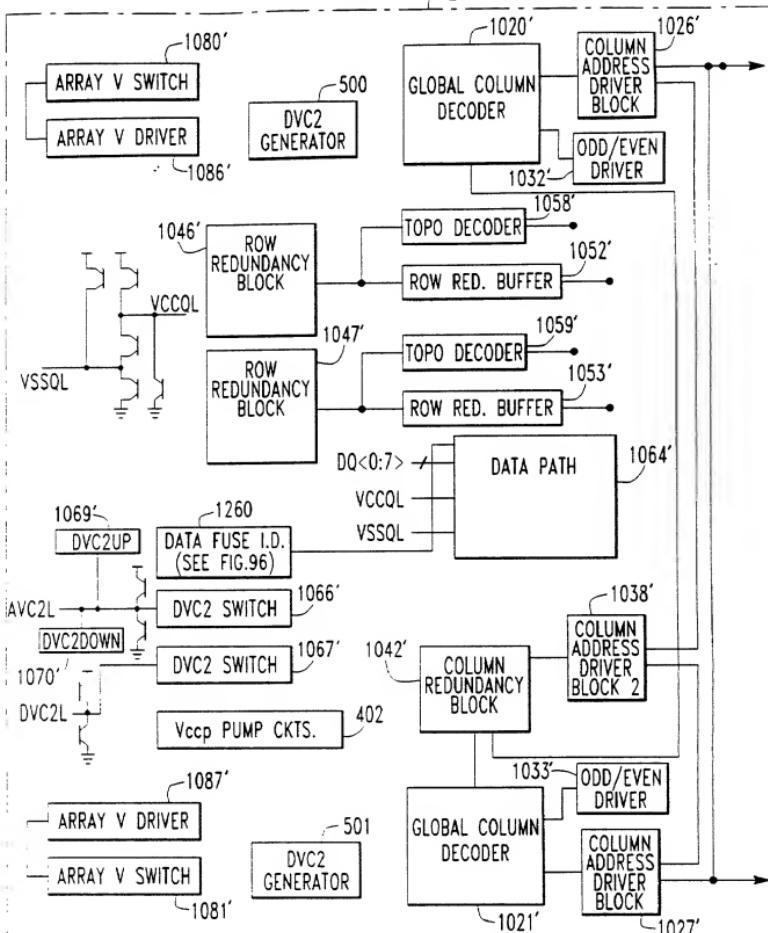


FIG. 65A

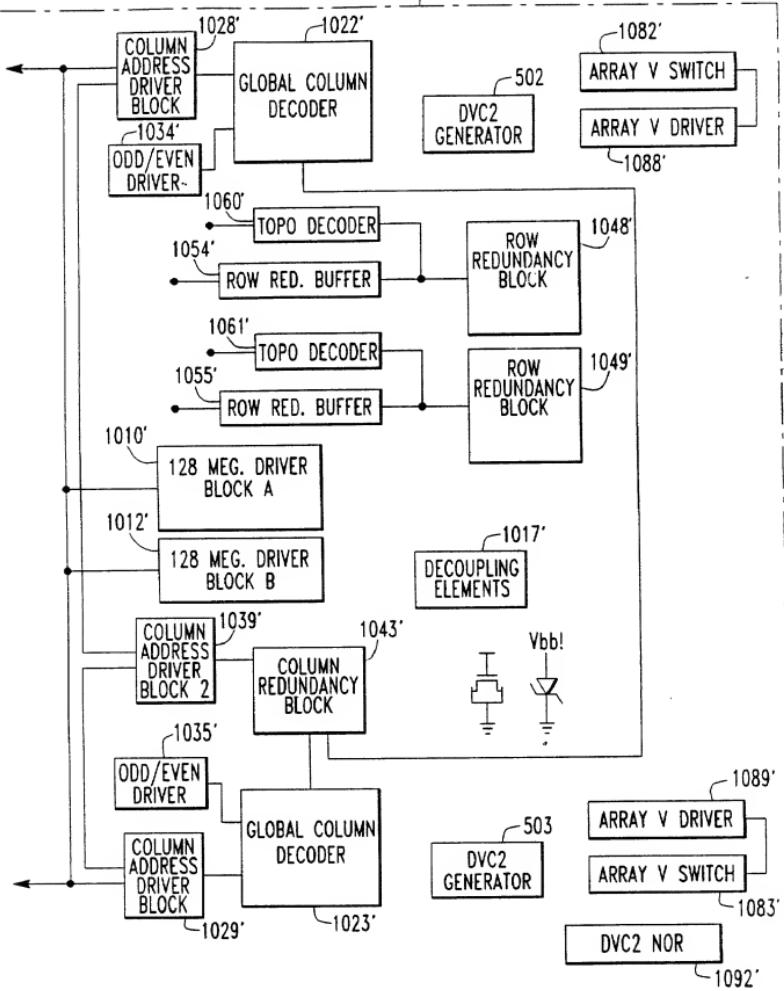
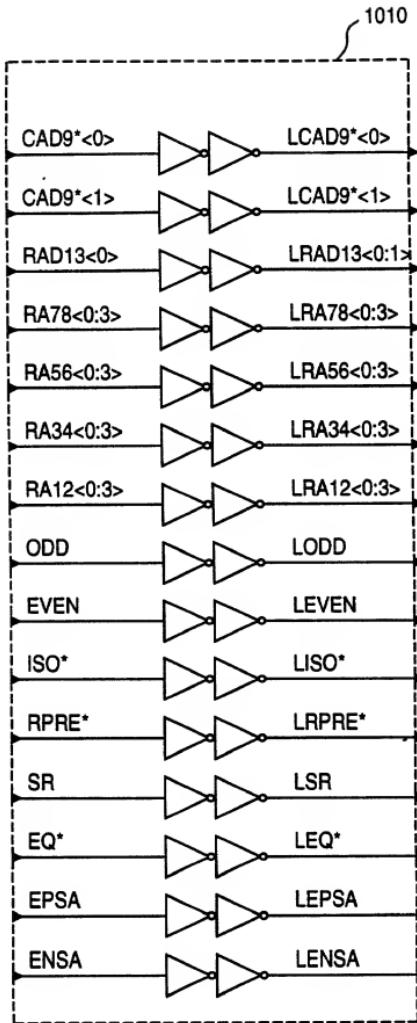


FIG. 65B

**FIG. 66**

702230 " 68EE5860

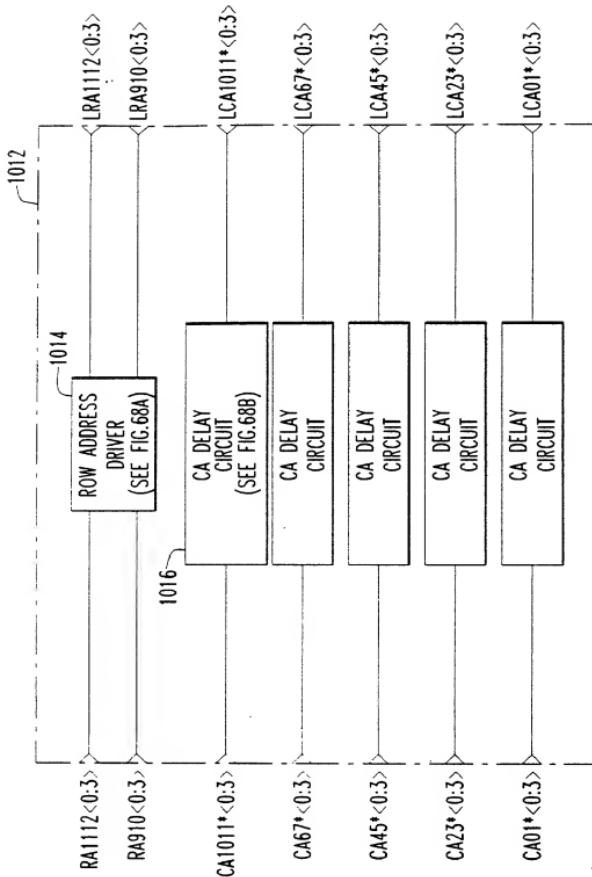


FIG. 67

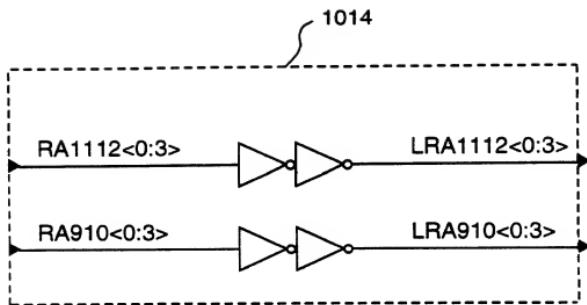
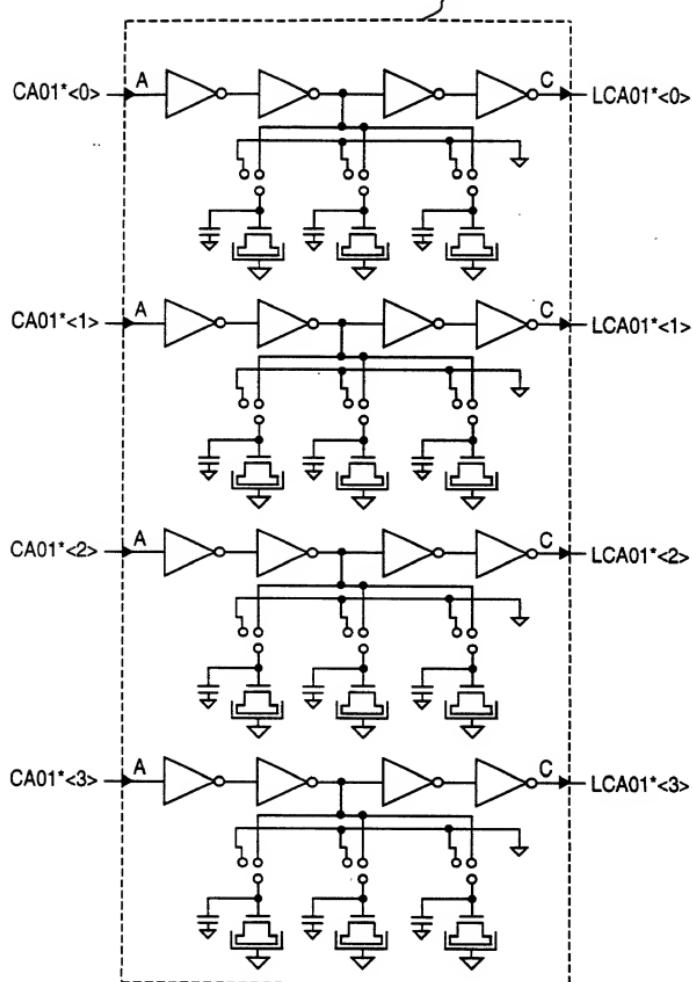


FIG. 68A



708290-03356360

FIG. 68B

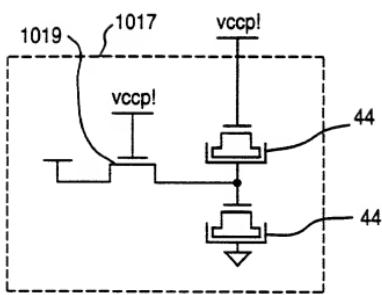


FIG. 69

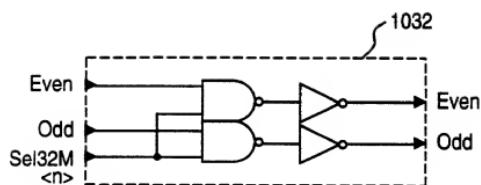


FIG. 70

1032 1032 1032 1032 1032 1032

Transistor Transistor Logic

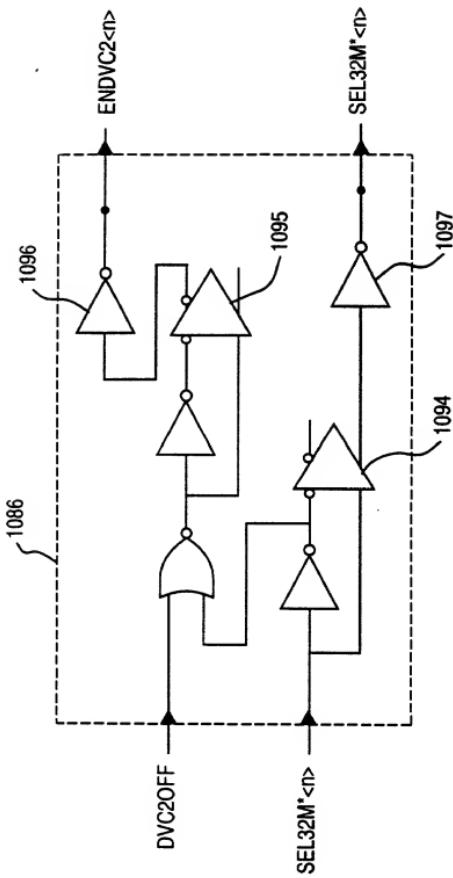


FIG. 71A

09853595 "U.S. Pat.

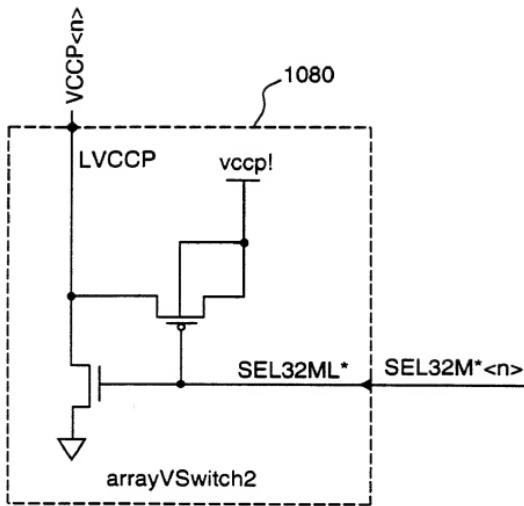
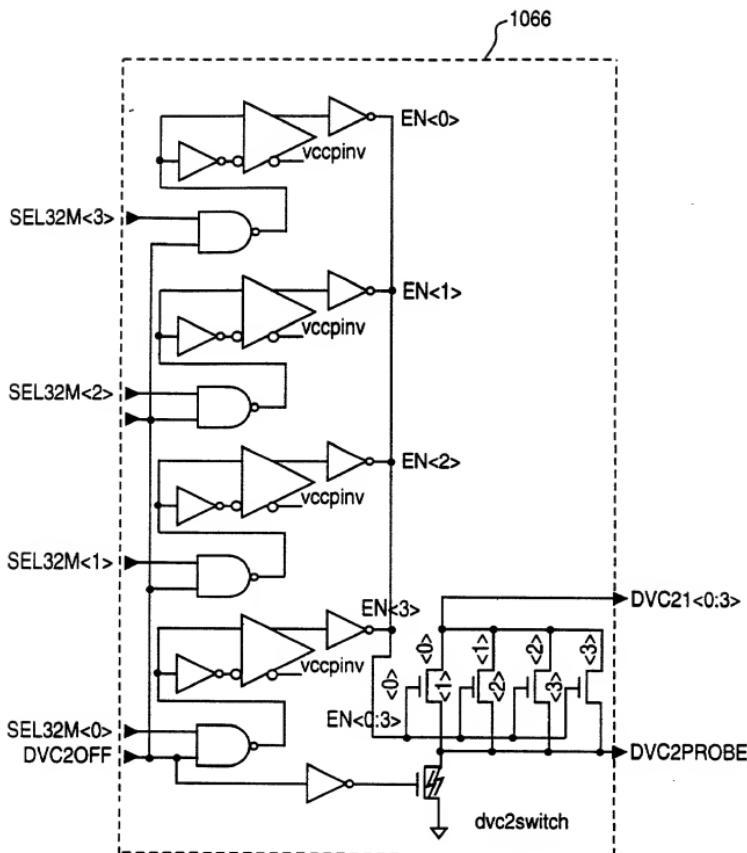


FIG. 71B

T08230-00006860

**FIG. 72A**

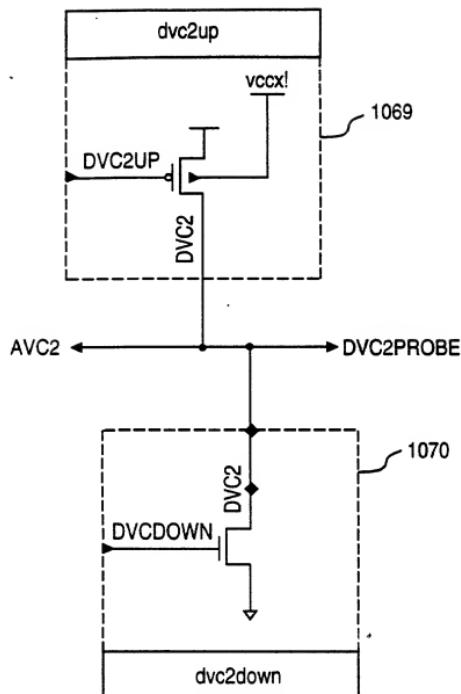


FIG. 72B

T08290 "68556860

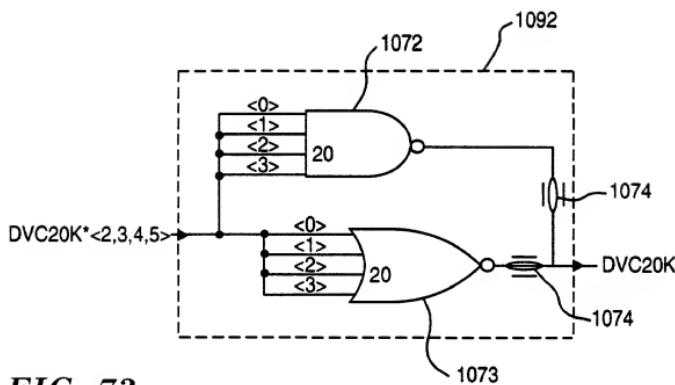


FIG. 73

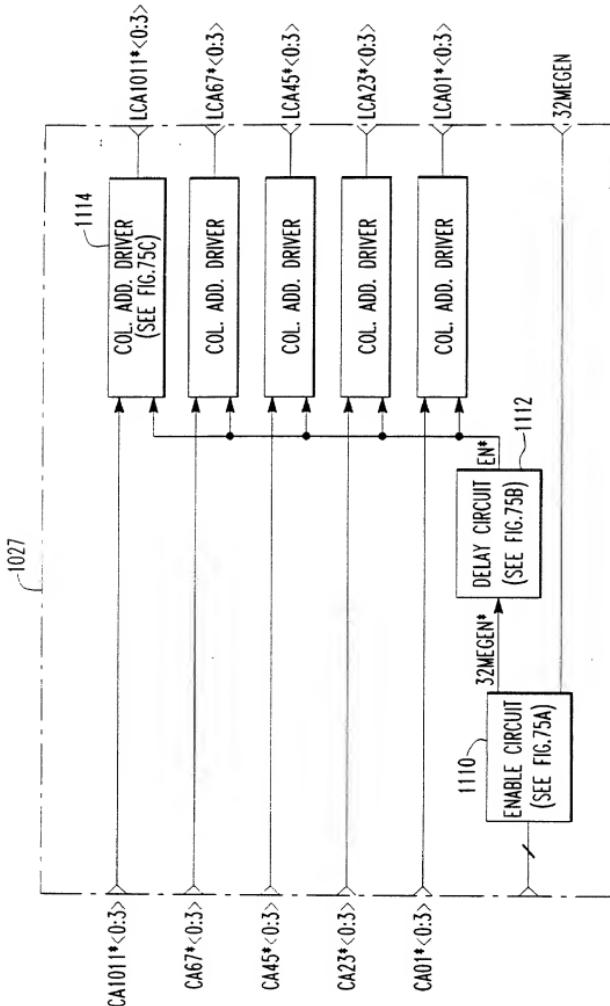


FIG. 74

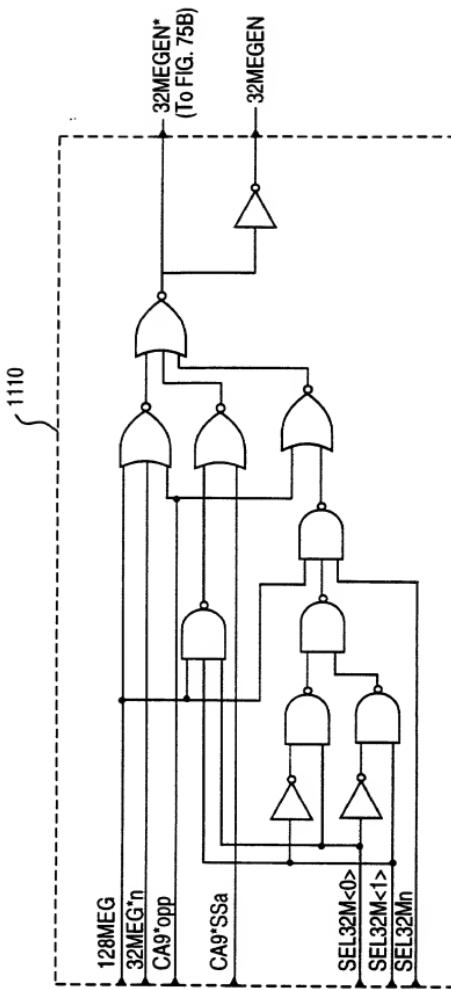


FIG. 75A

T082301-5656660

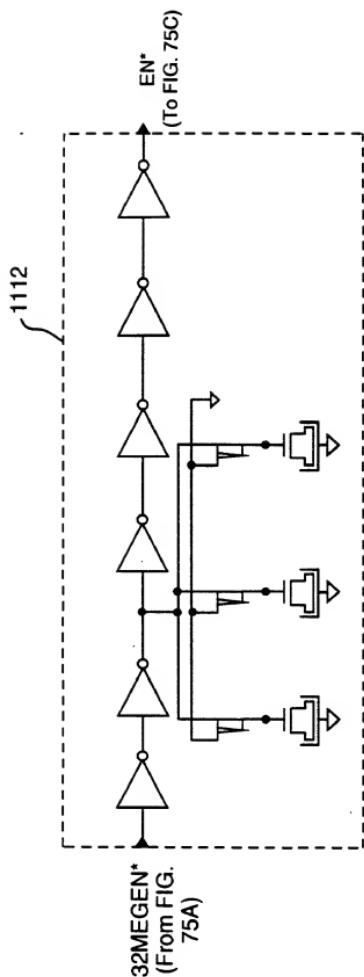


FIG. 75B

YD2250 "GSE2660

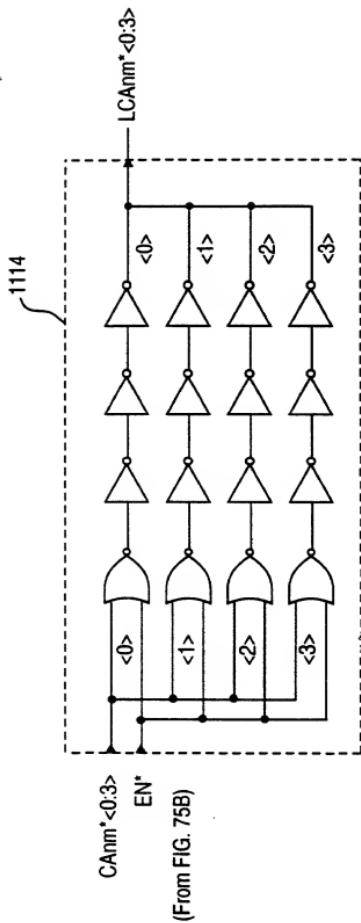


FIG. 75C

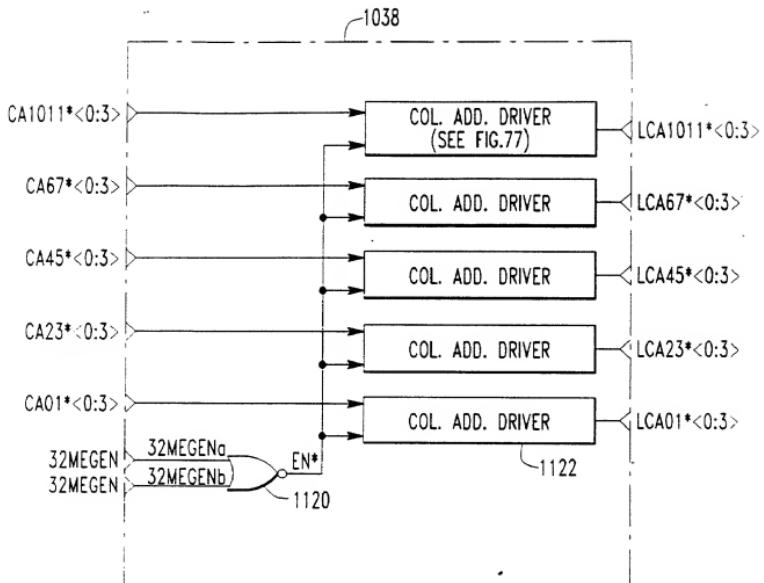


FIG. 76

T032290 "68EE6600

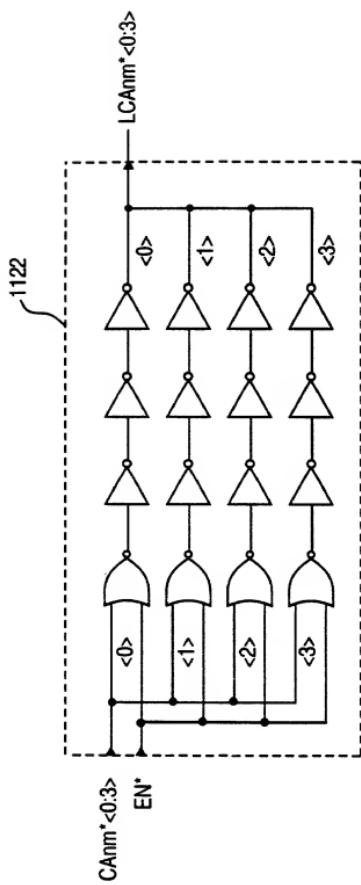


FIG. 77

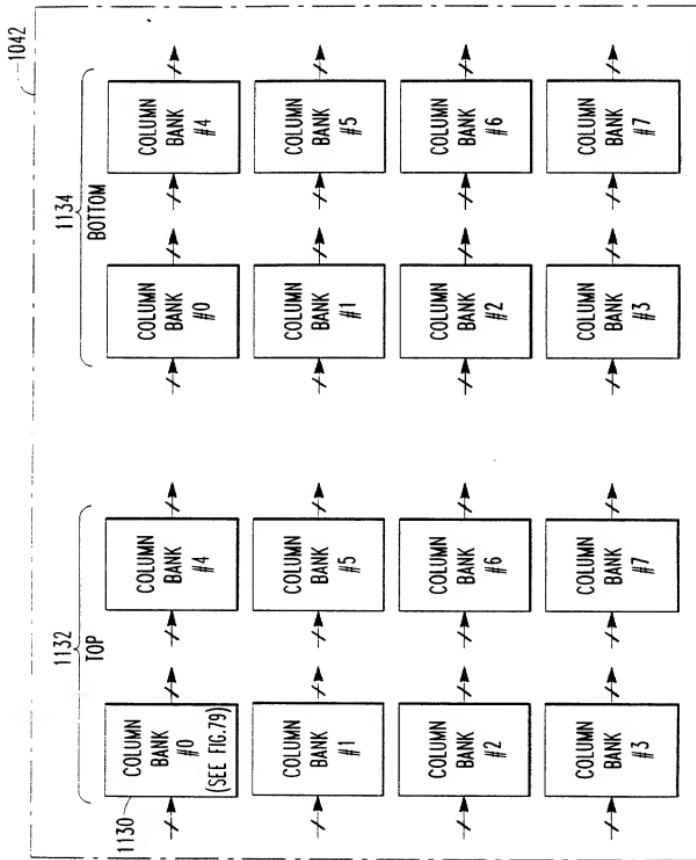
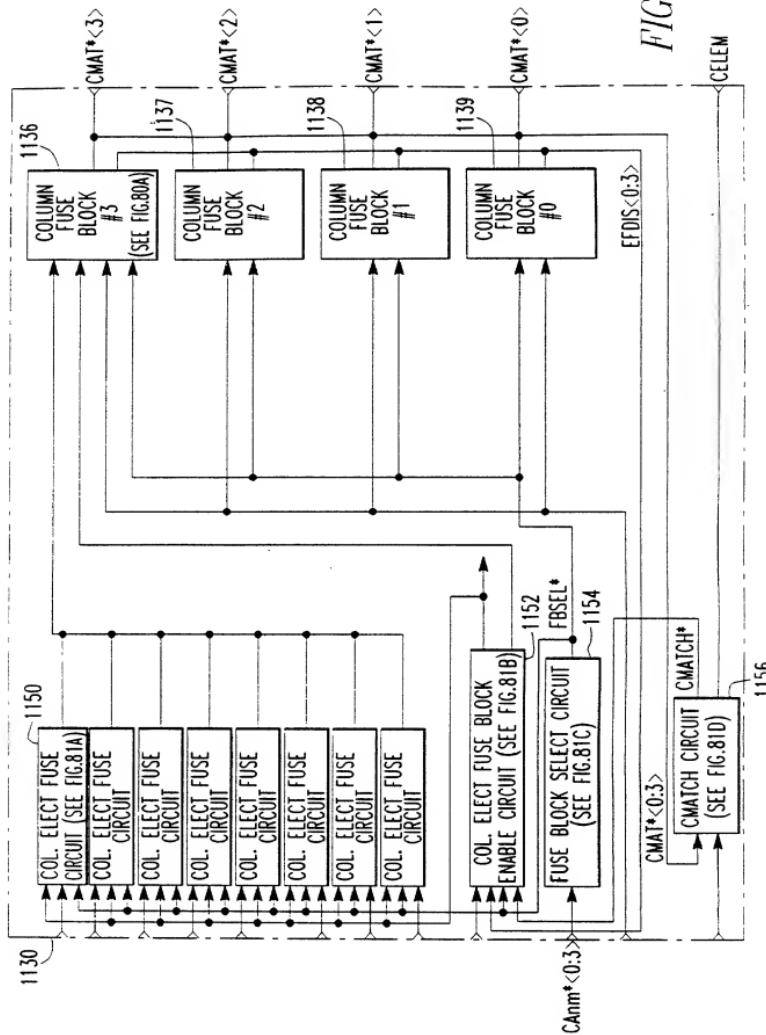


FIG. 78

T08291-68666860



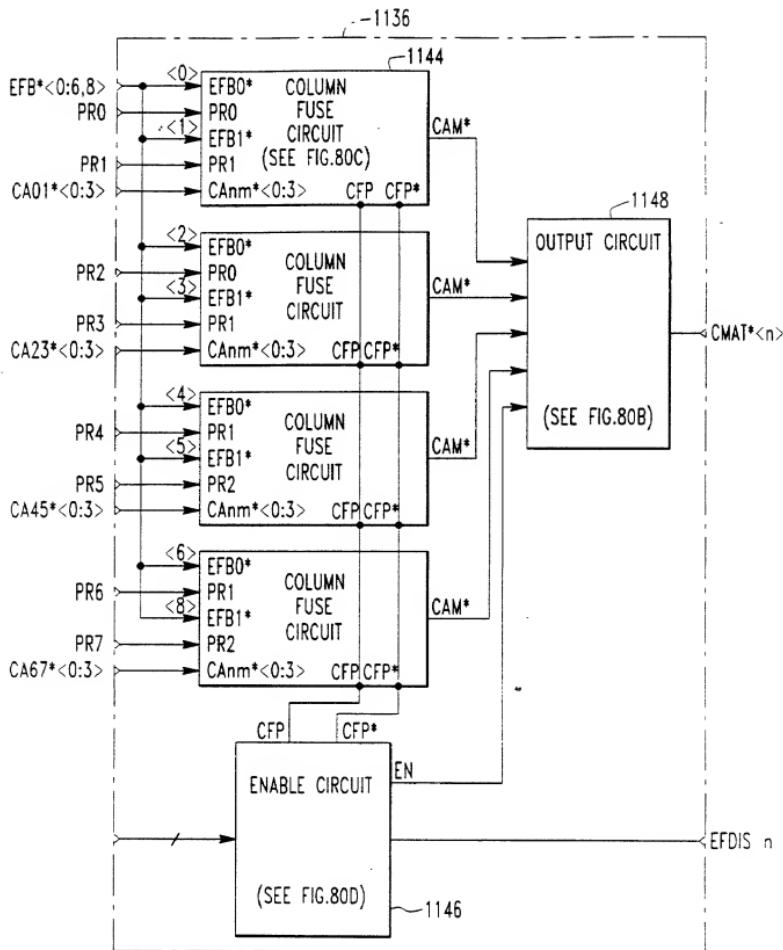


FIG. 80A

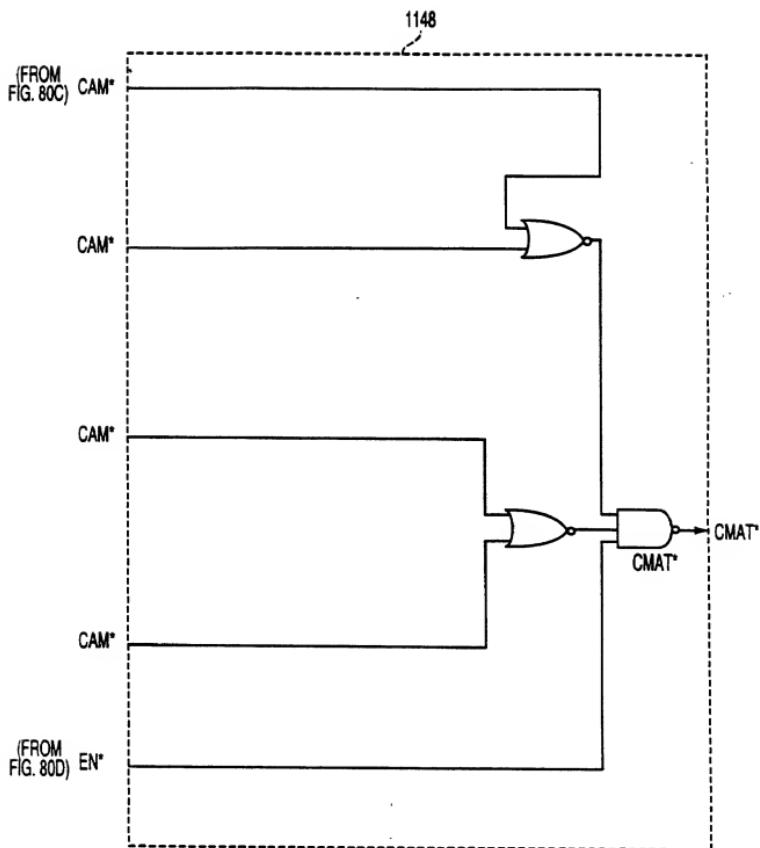


FIG. 80B

294/367

卷之三

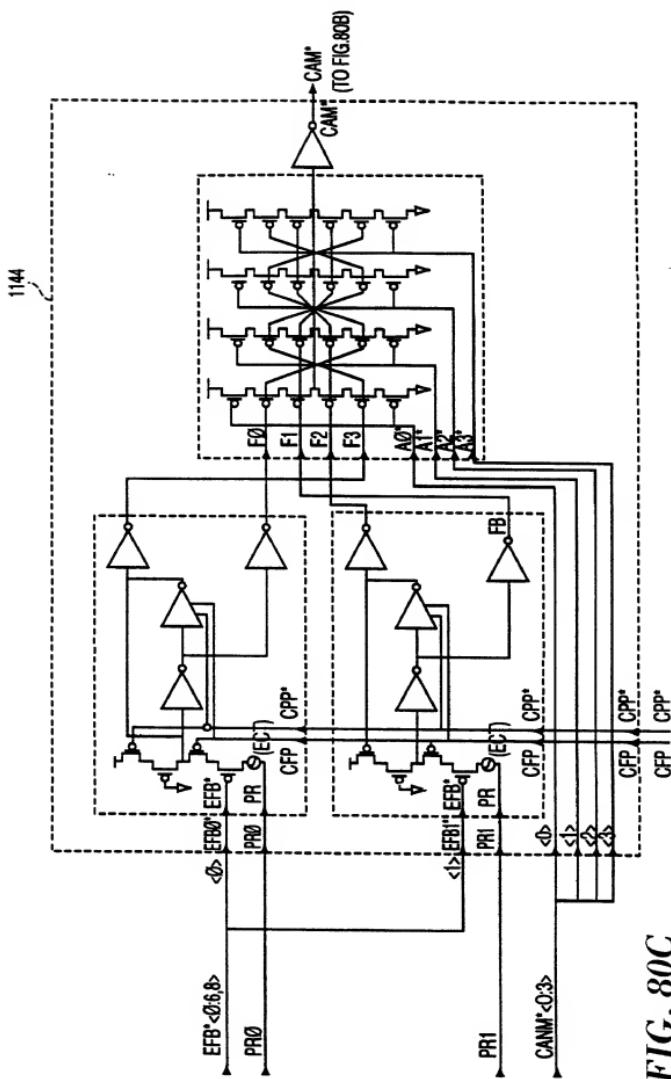


FIG. 80C

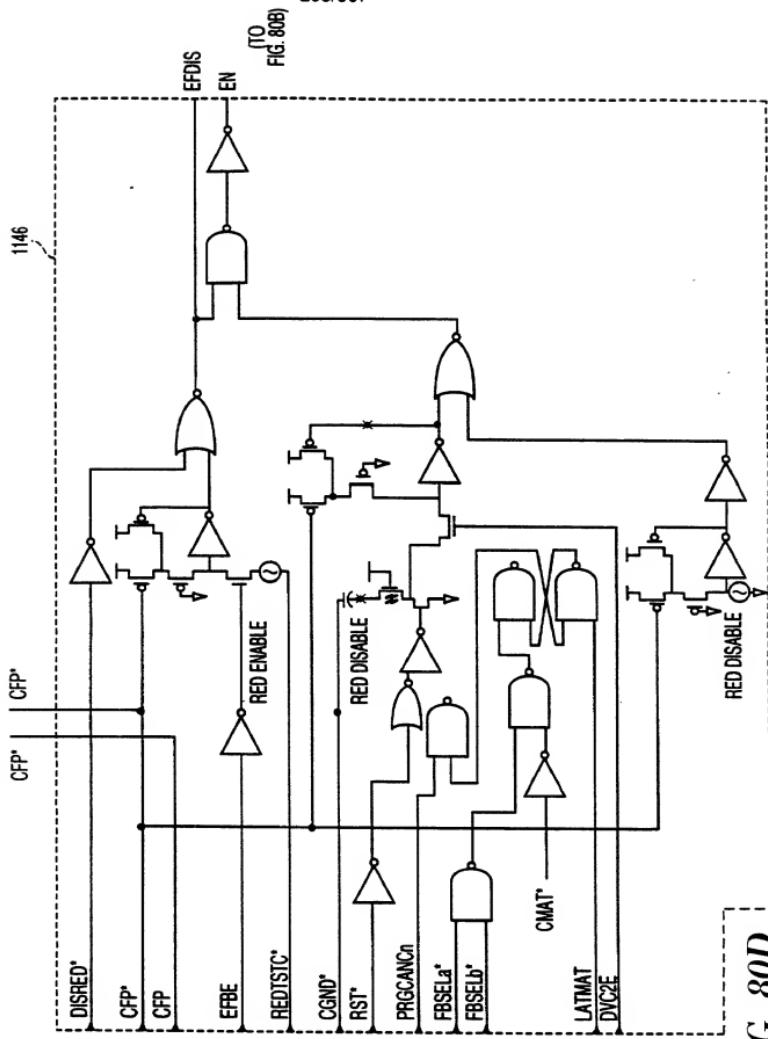


FIG. 80D

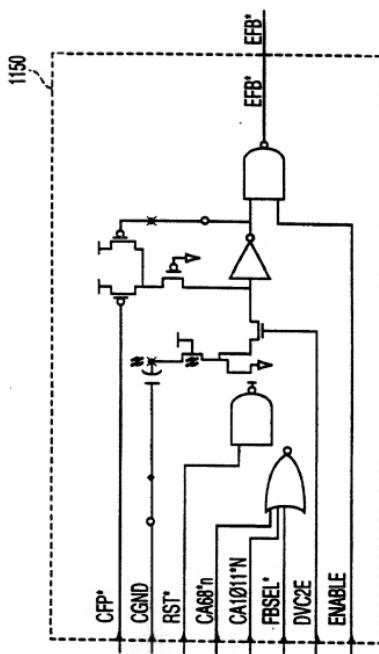


FIG. 81A

T032250 "68EE660

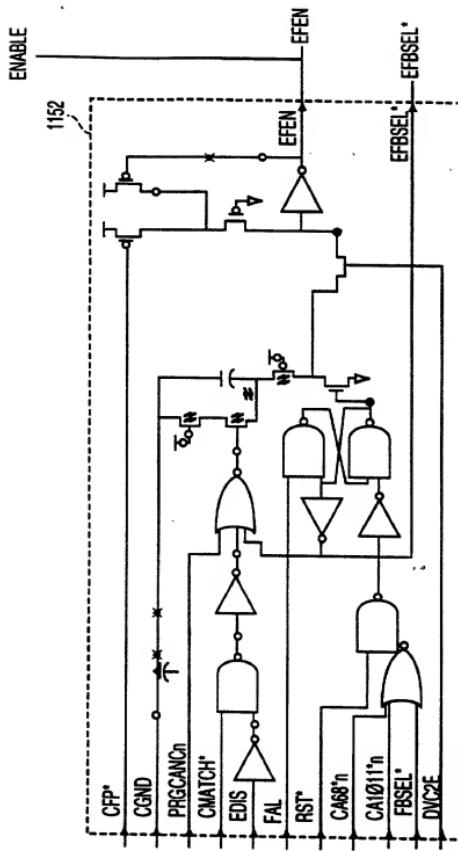


FIG. 81B

卷之三

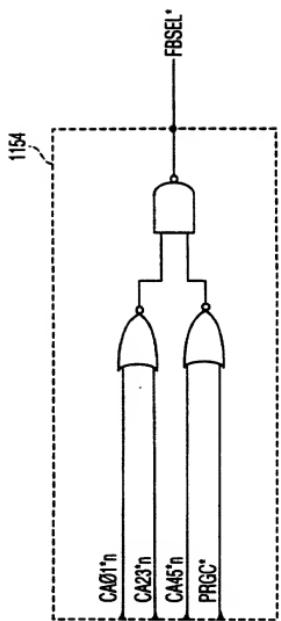


FIG. 81C

TOS230 "GSEGEGG

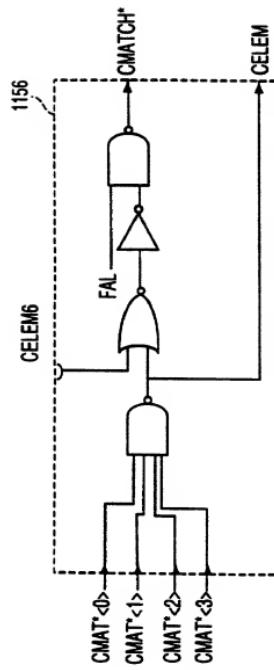


FIG. 81D

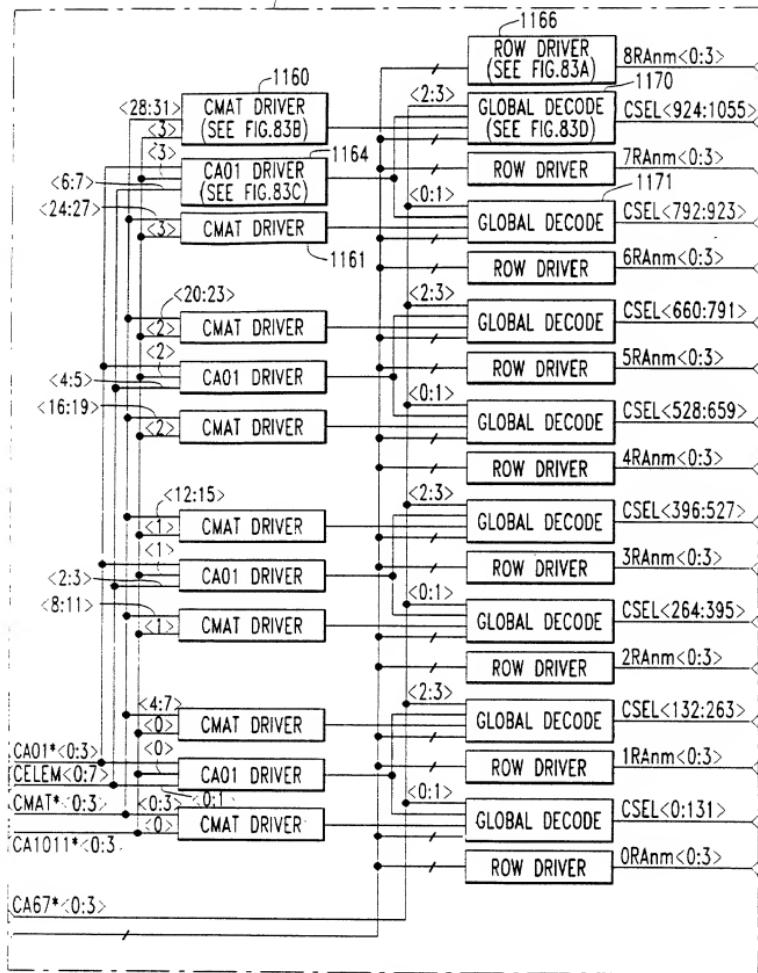


FIG. 82

T08290" SEC 60

301/367

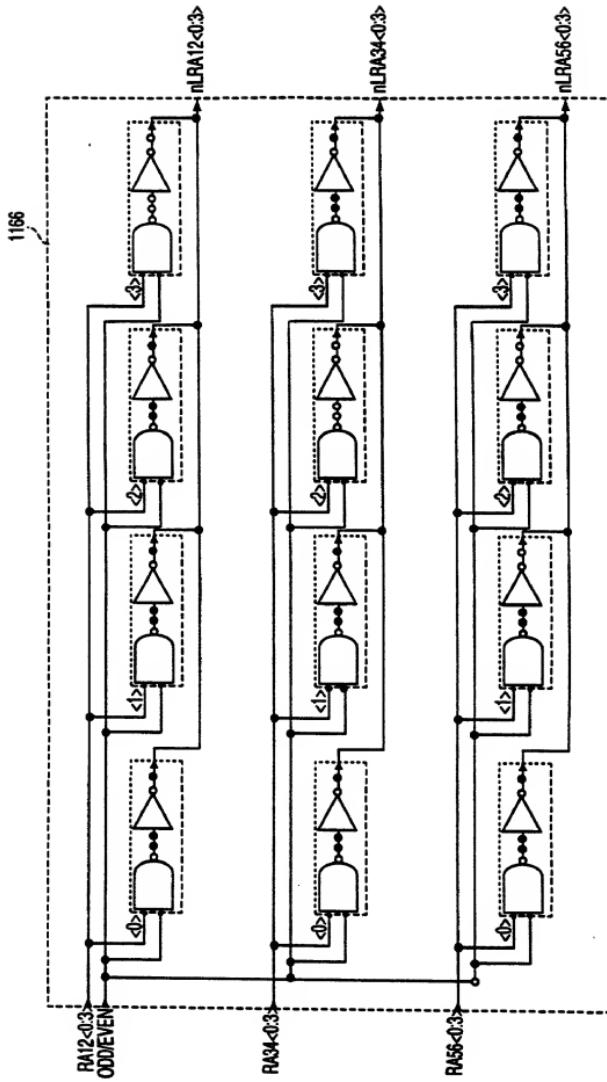


FIG. 83A

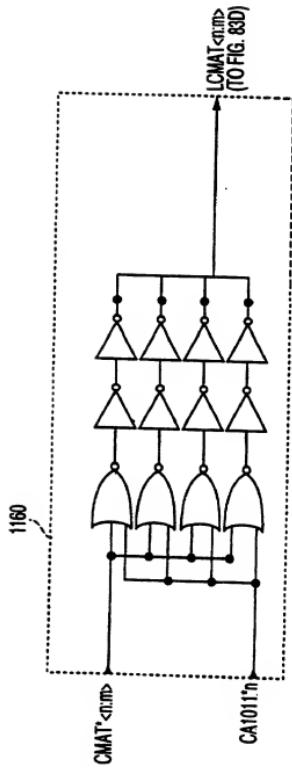


FIG. 83B

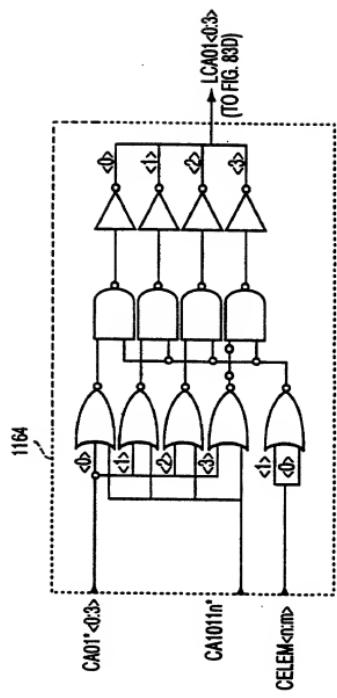


FIG. 83C

304/367

卷之三

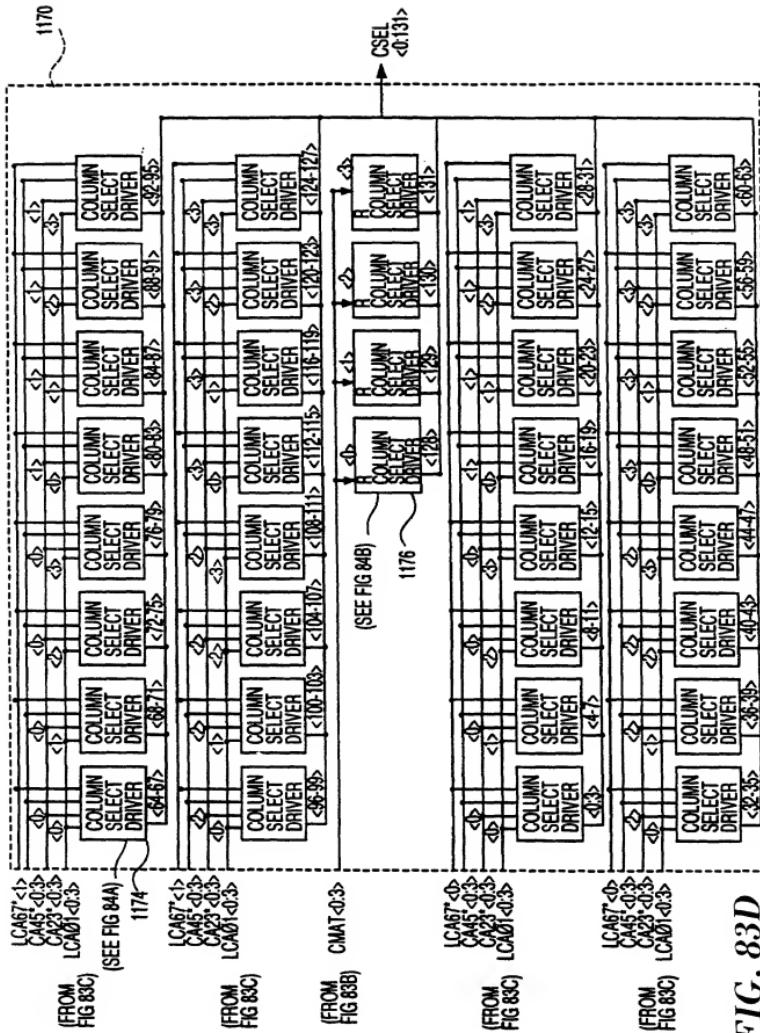


FIG. 8.3D

T08290° 63E6860

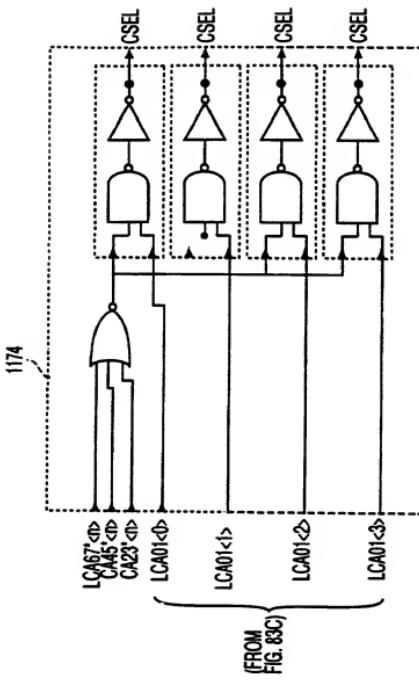


FIG. 84A

106290-6356360

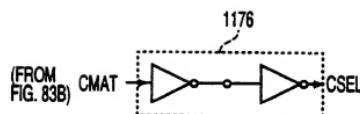


FIG. 84B

-1047

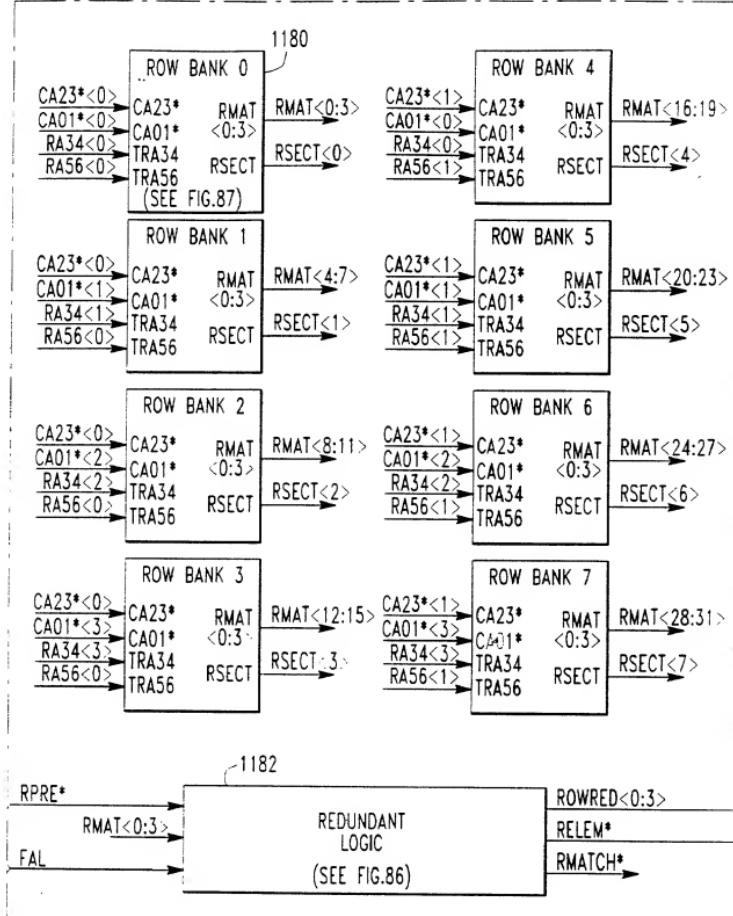


FIG. 85

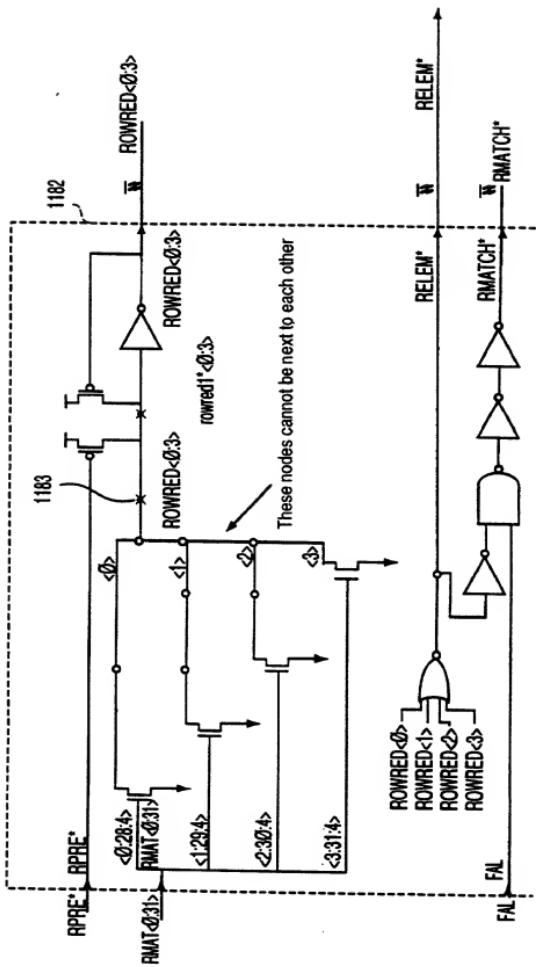


FIG. 86

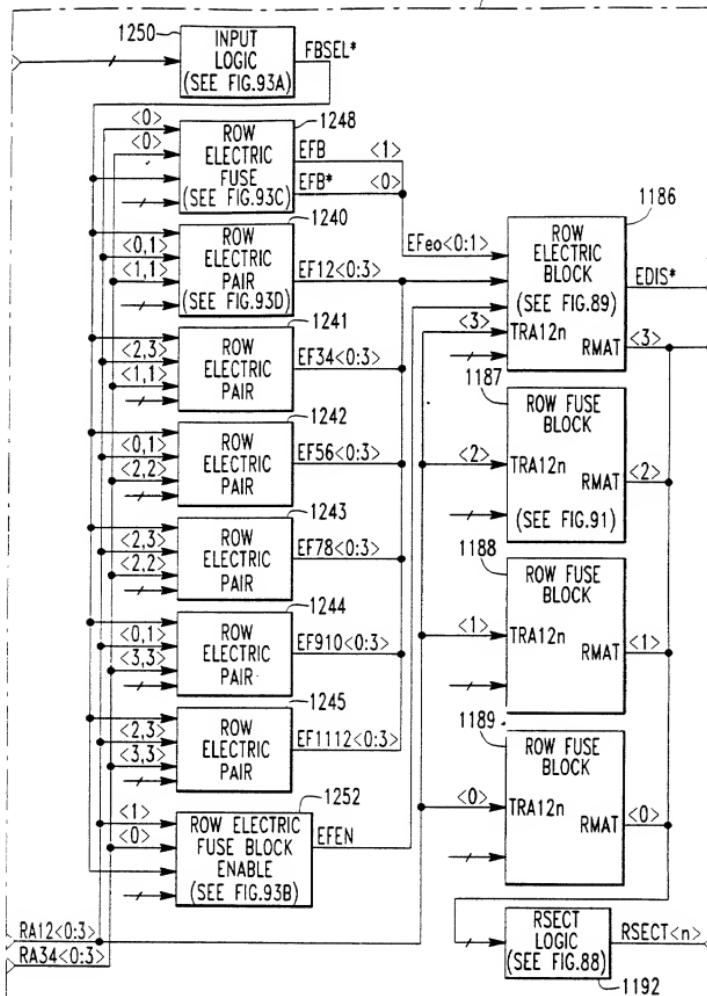


FIG. 87

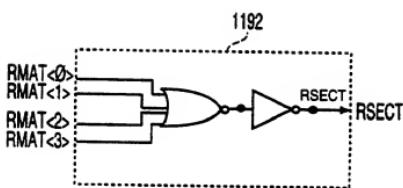


FIG. 88

00000000000000000000000000000000

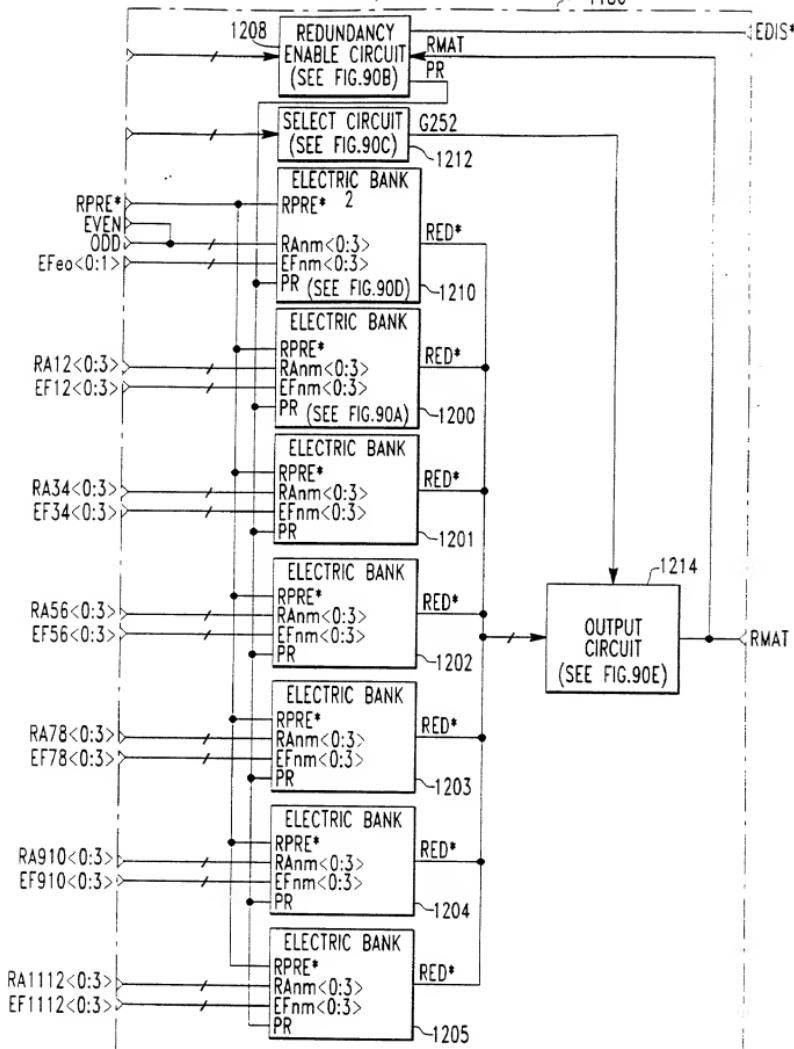


FIG. 89

T08230153866860

438230 "63356660

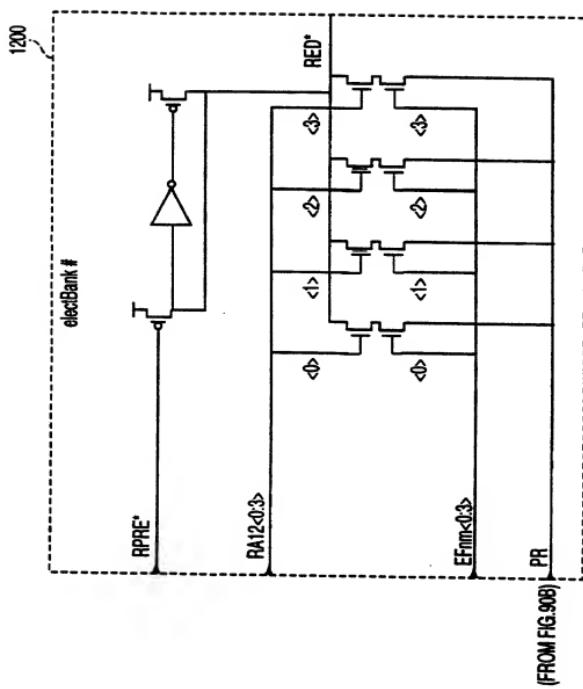


FIG. 90A

(FROM FIG. 90B)

卷之三

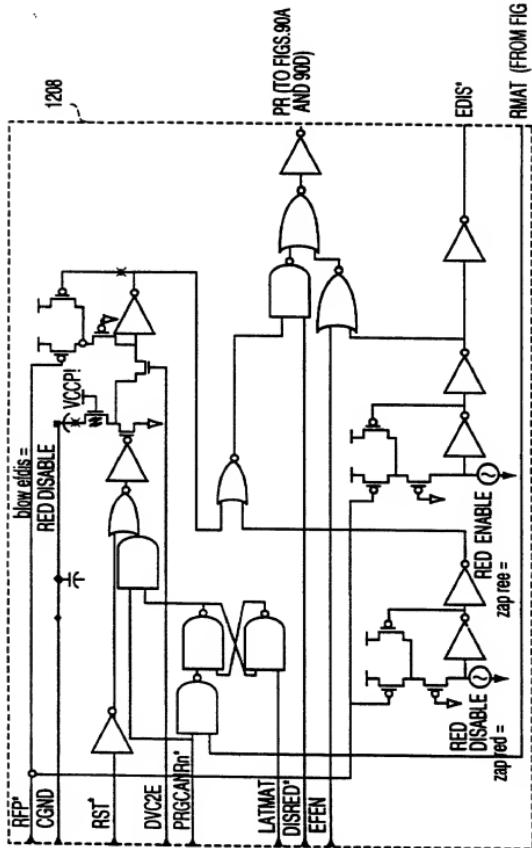


FIG. 90B

1082290 " 68256860

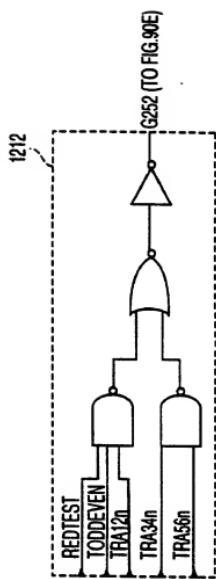


FIG. 90C

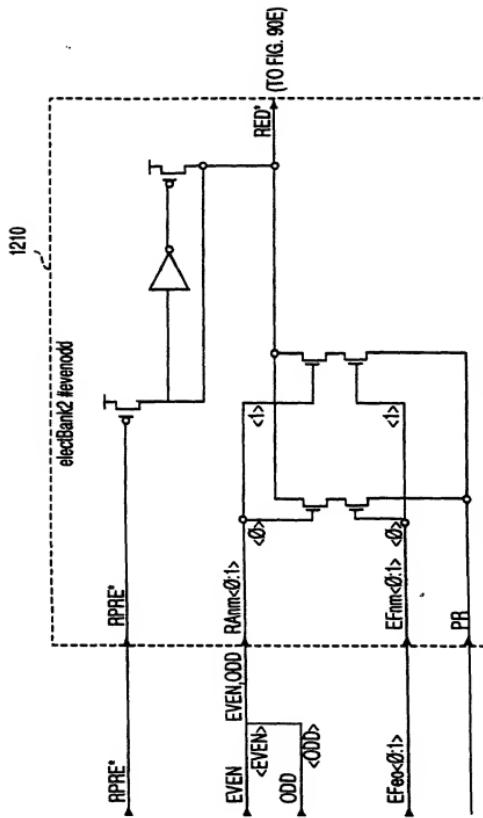


FIG. 90D

T08230 "6666860

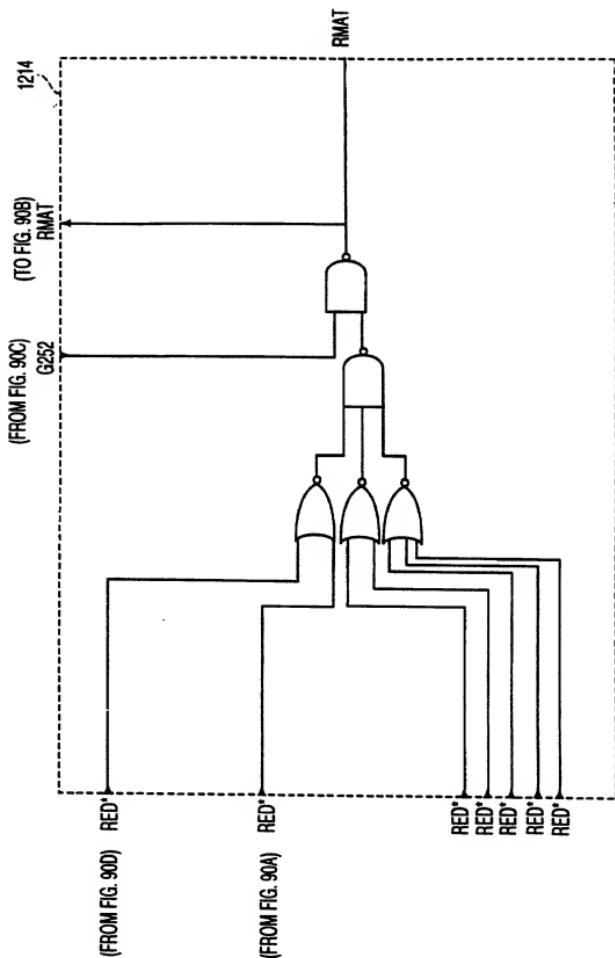


FIG. 90E

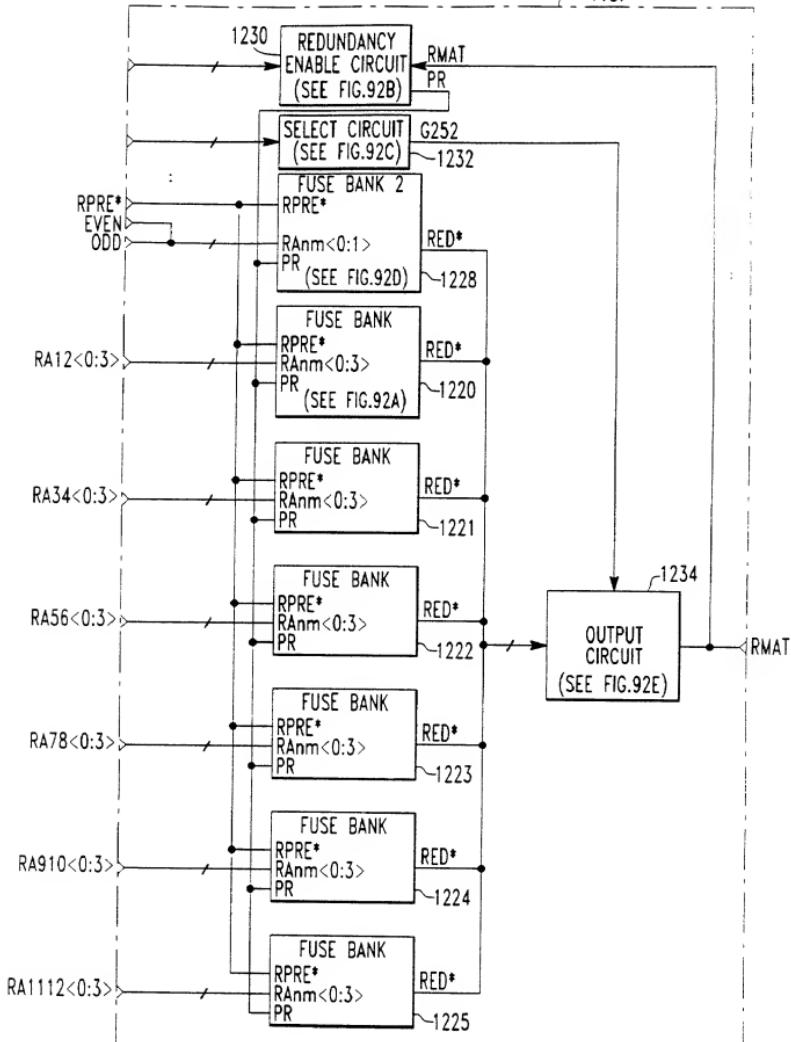
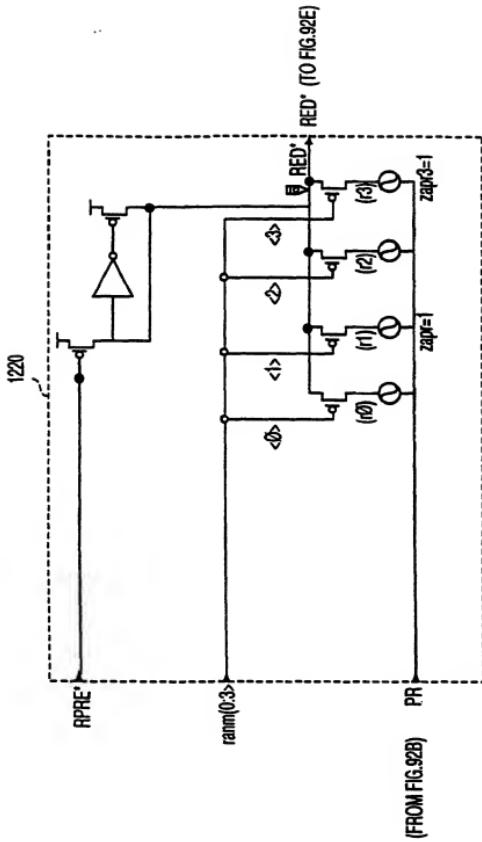


FIG. 91

T08230'68E6860

TOSHIBA "6866666666

**FIG. 92A**

(FROM FIG.92B)

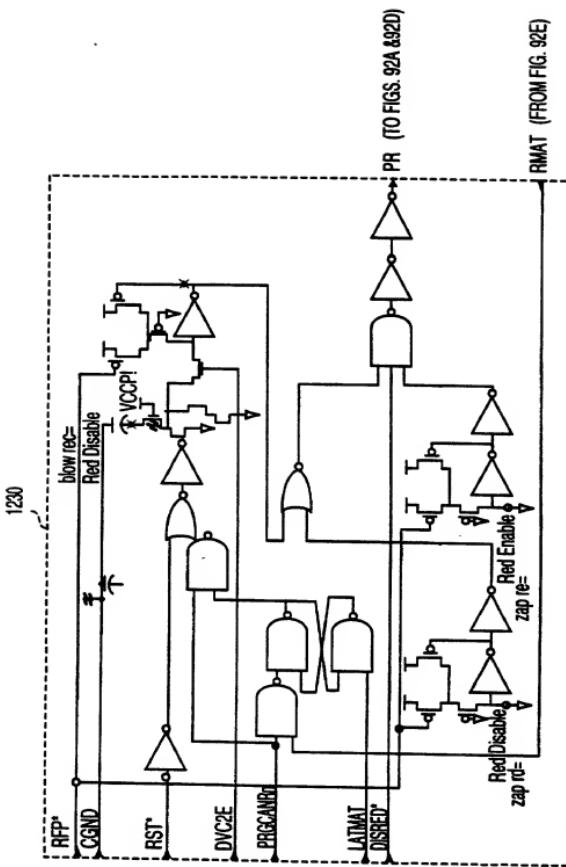


FIG. 92B

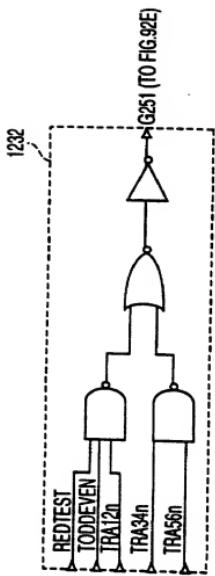
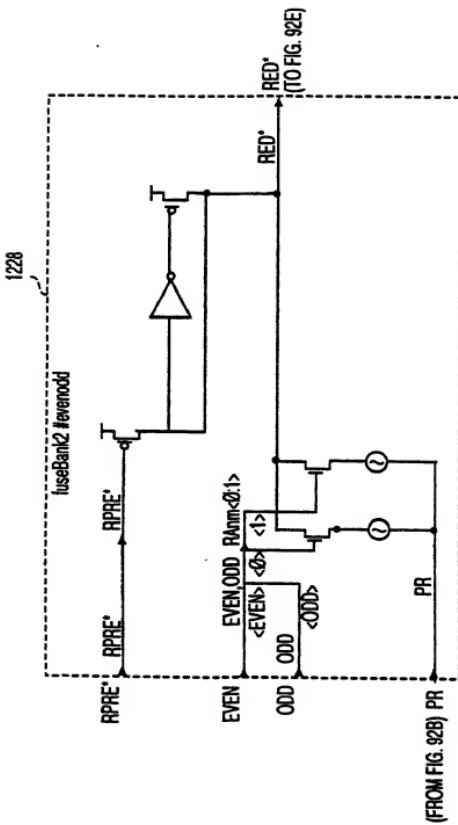


FIG. 92C

TOP SECRET//GCCB/GD

*FIG. 92D*

T 06230 " 666666

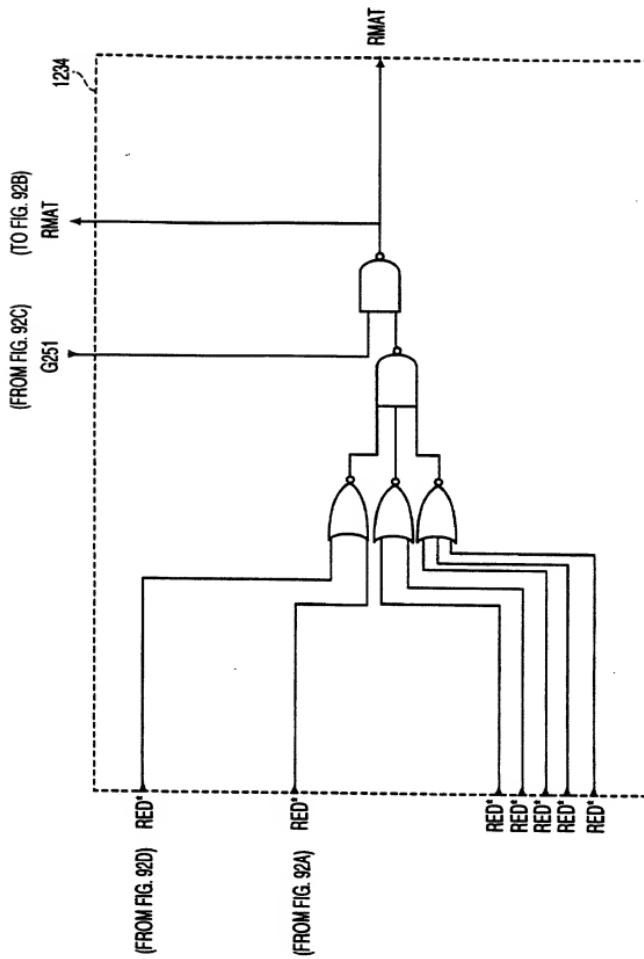


FIG. 92E

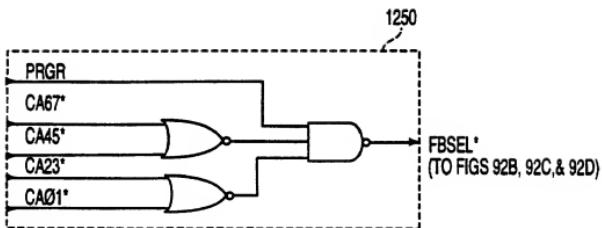


FIG. 93A

T08230-6866860

108250 "686660

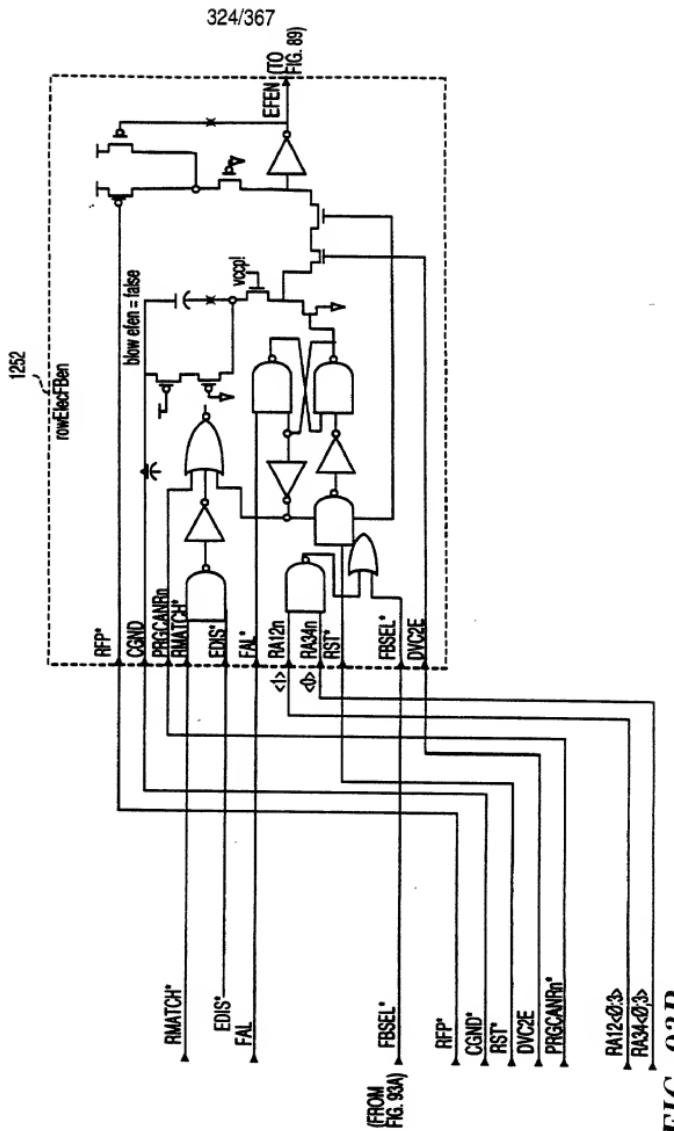


FIG. 93B

TURBO 2000 "S" 3836860

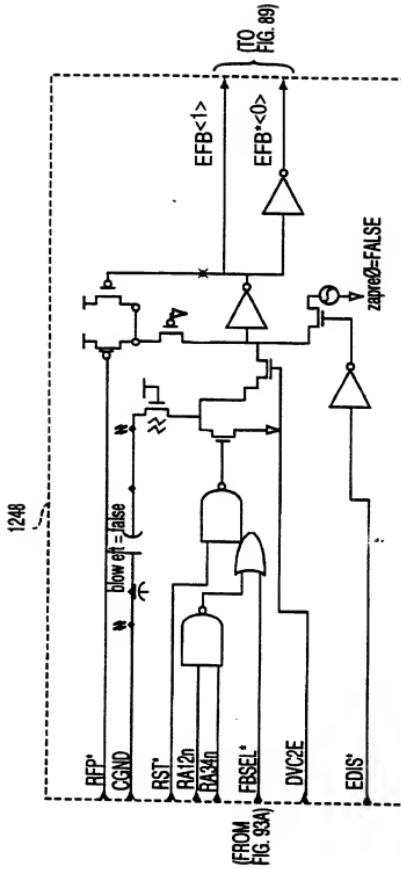
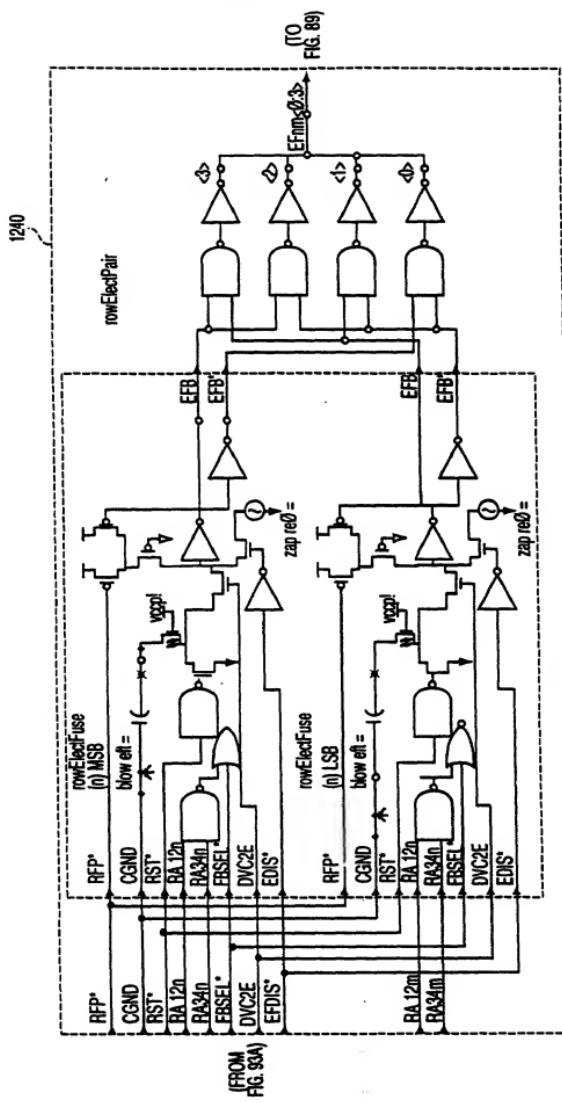


FIG. 93C

**FIG. 93D**

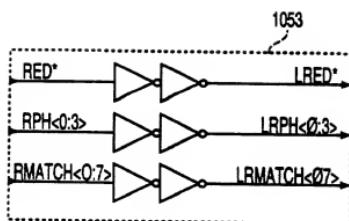


FIG. 94

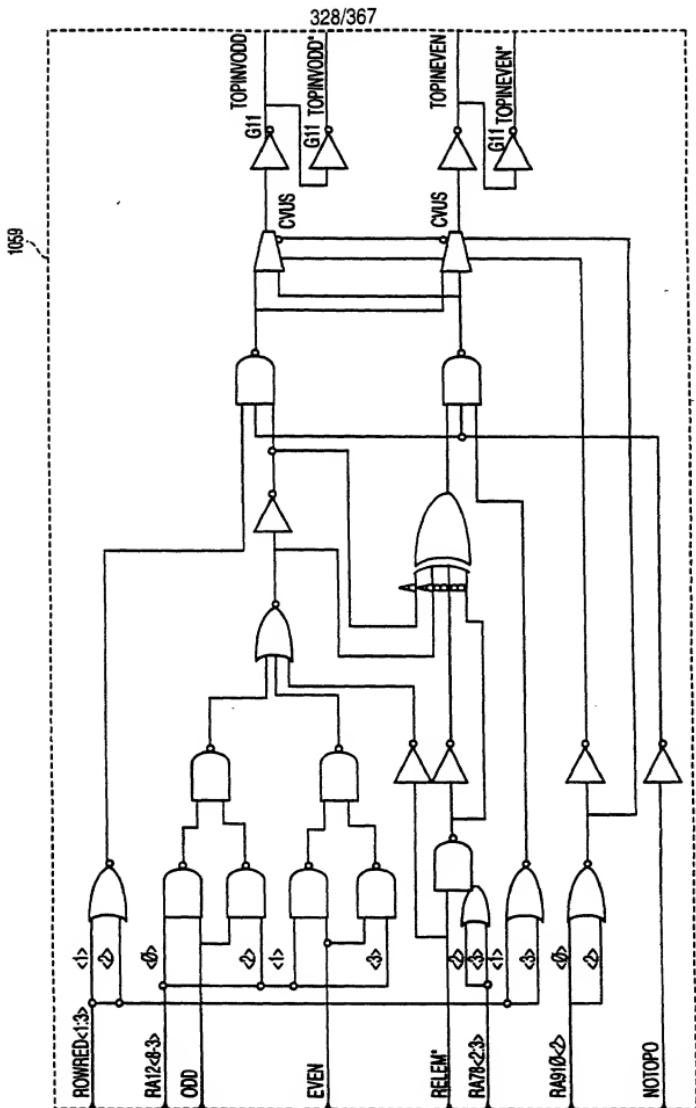


FIG. 95

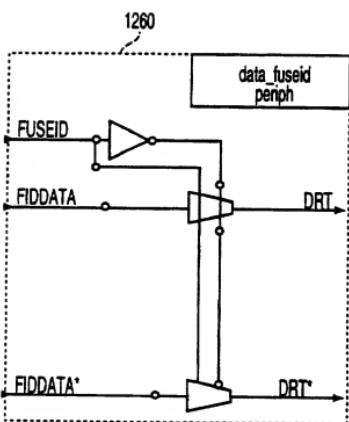


FIG. 96

卷之三

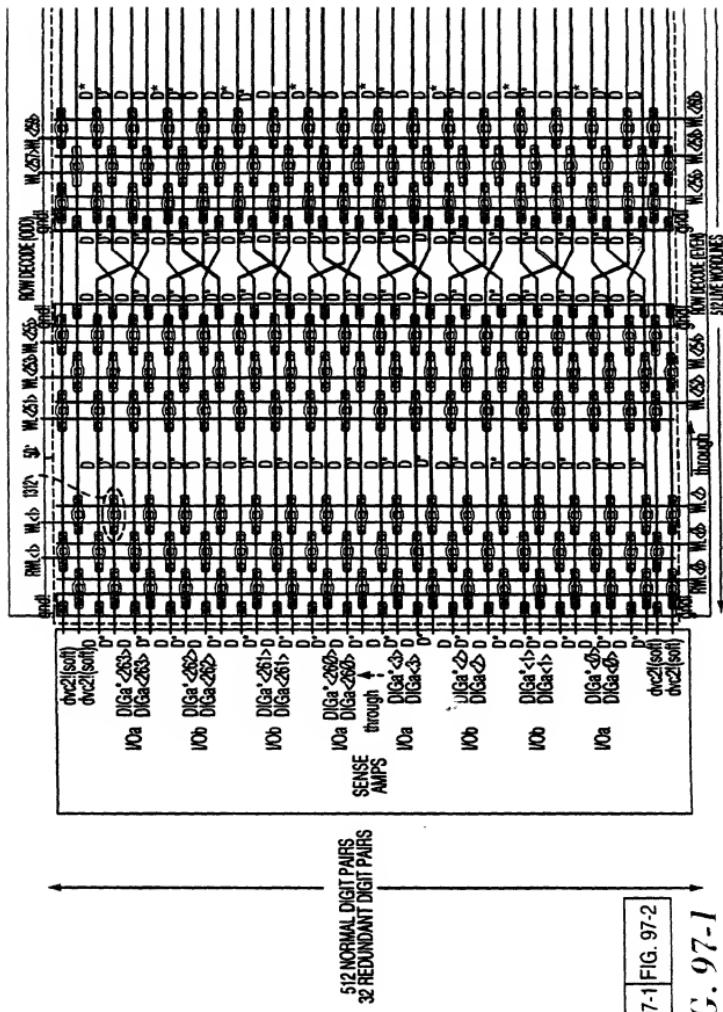


FIG. 97-1

卷之三

331/367

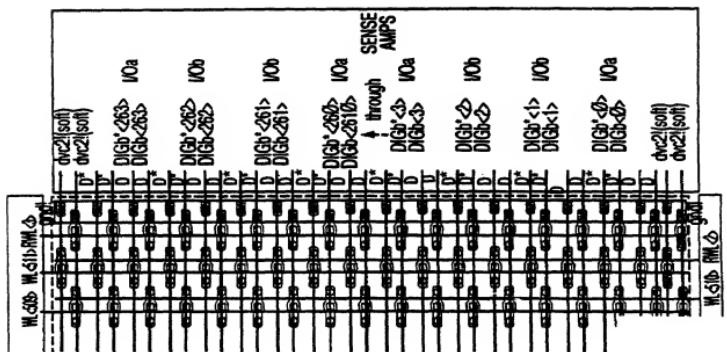


FIG. 97-2

FIG. 97-1 FIG. 97-2

103290-6236860

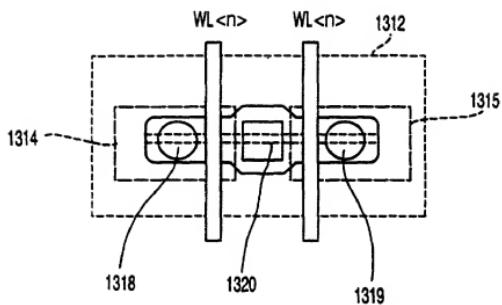


FIG. 98

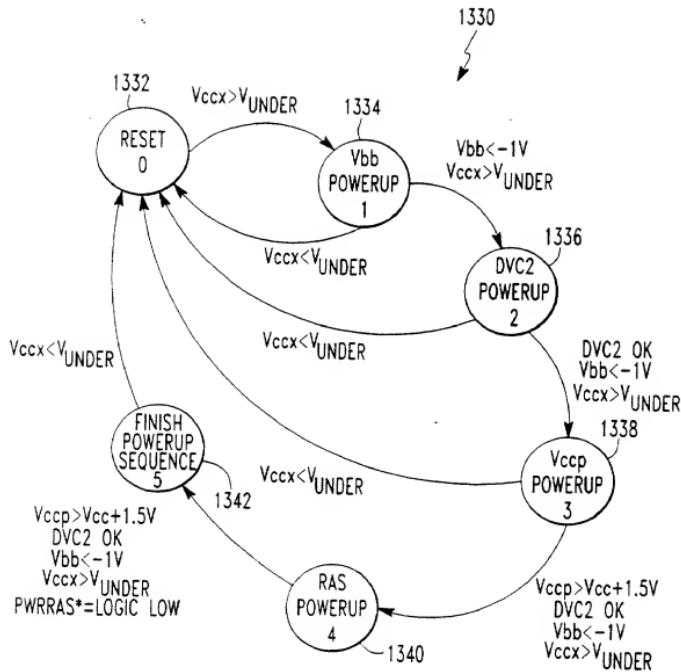
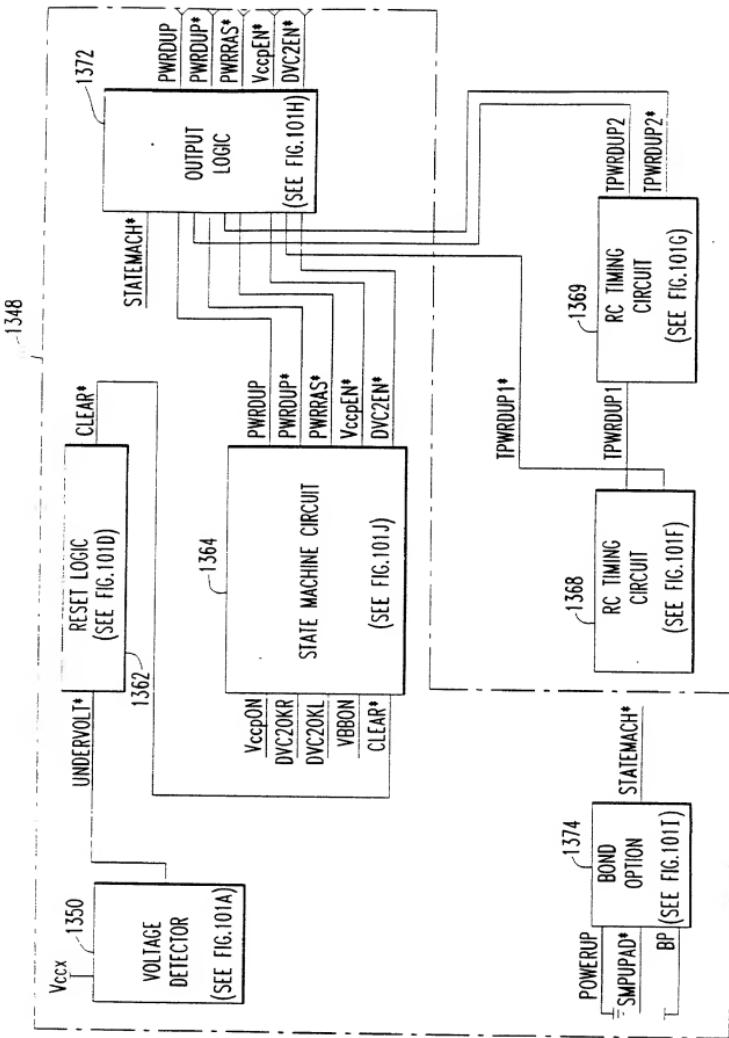


FIG. 99

TDS220 "EEE66666



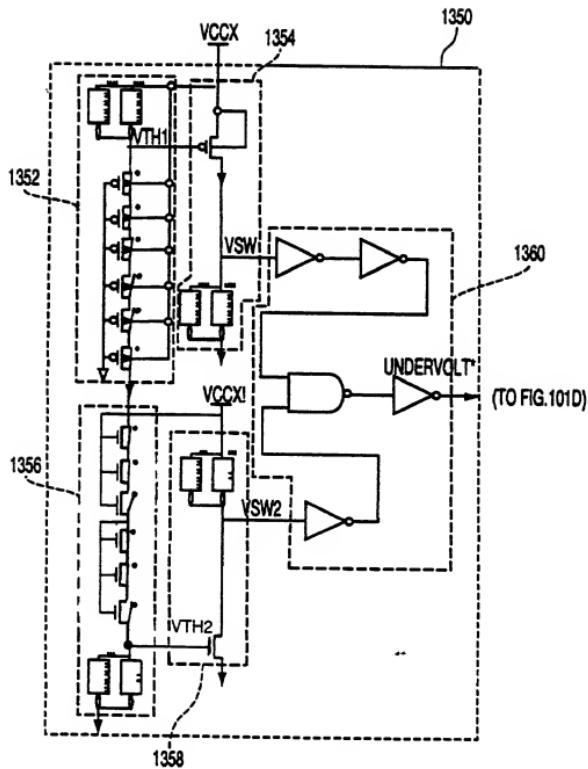


FIG. 101A

T08280 = 6336B6D

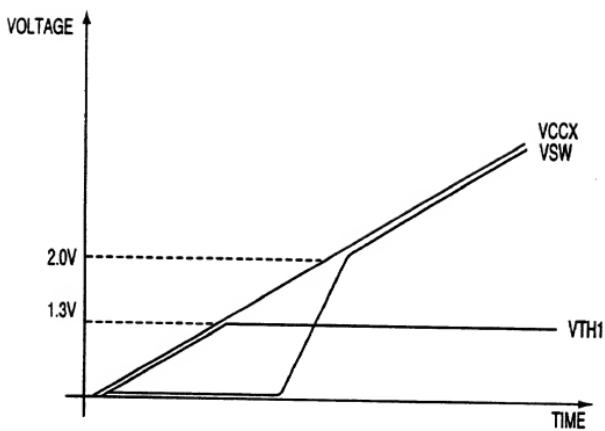


FIG. 101B

0993563852281

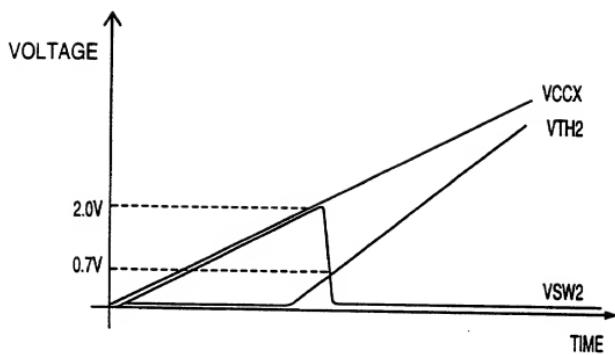
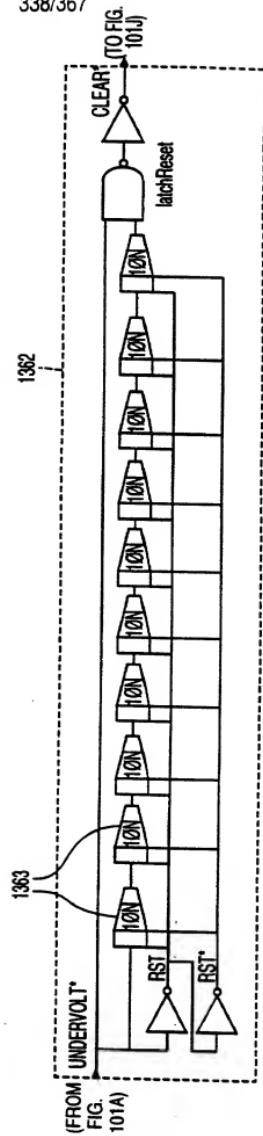


FIG. 101C

*FIG. 101D*

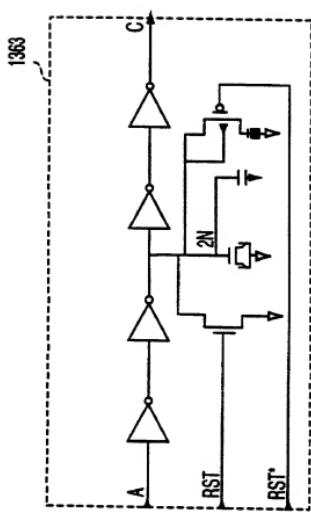


FIG. 101E

T082301 6886860

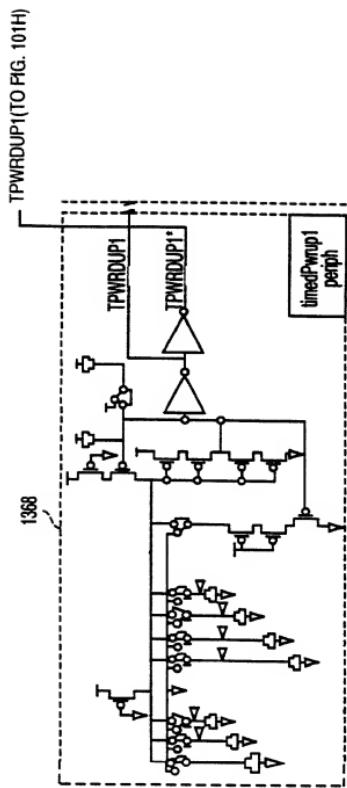


FIG. 101F

卷之三

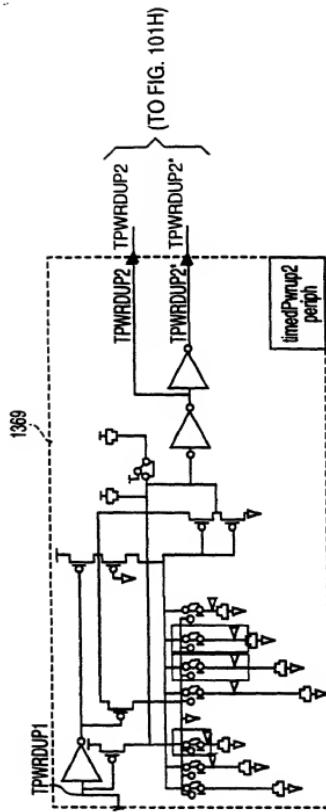


FIG. 101G

T08290 "68E686D

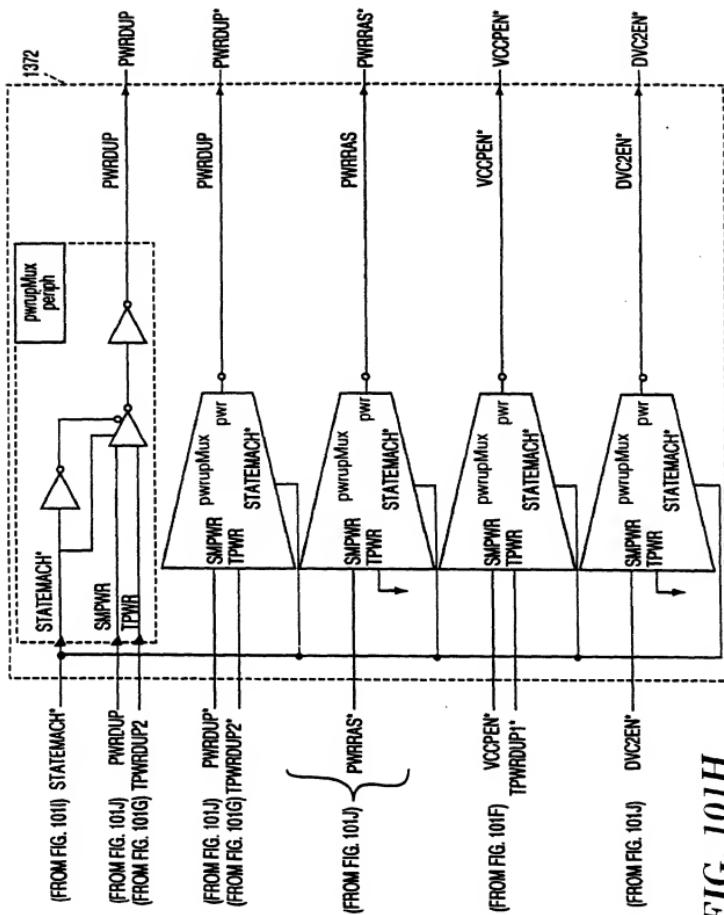


FIG. 10H

YD8291 "SSEE6660

343/367

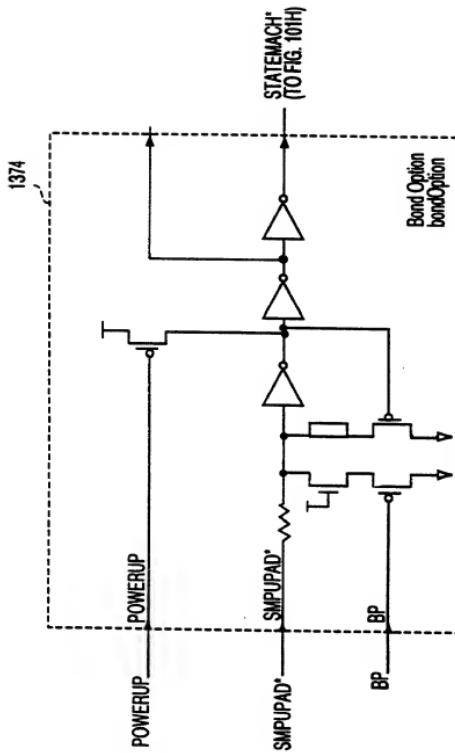


FIG. 1011

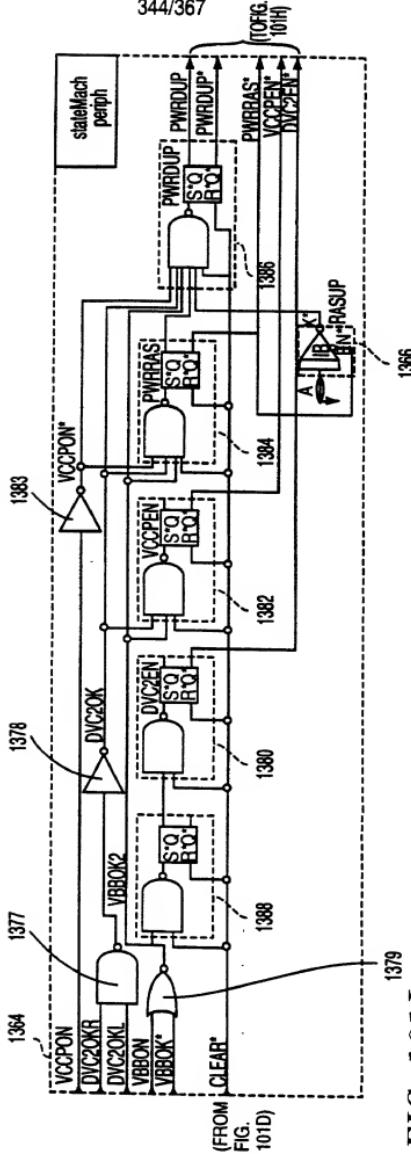


FIG. 101J

T03290 "62556660

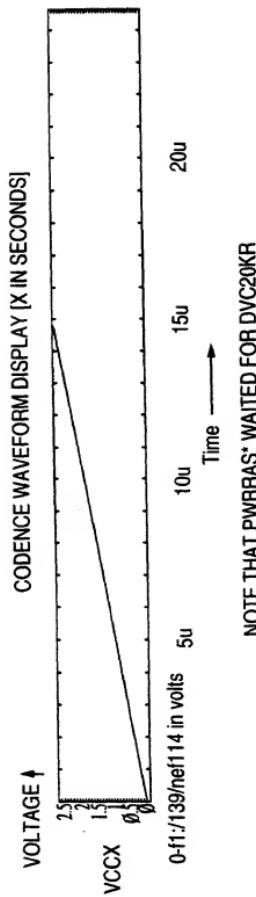


FIG. 102A

TOP SECRET // SECURITY INFORMATION

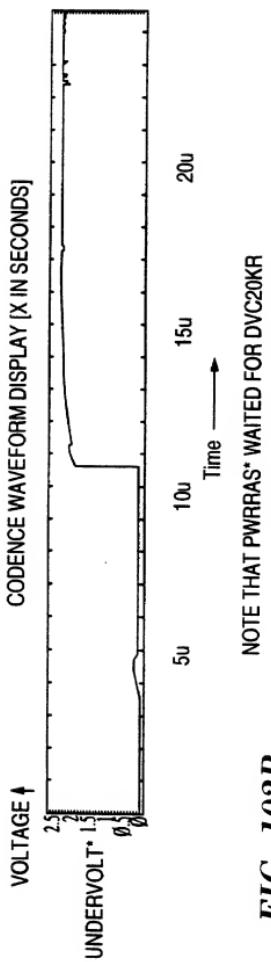


FIG. 102B

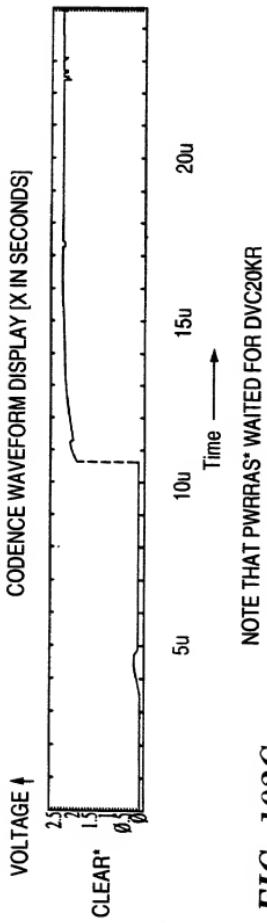


FIG. 102C

T01B2201 62EE6660

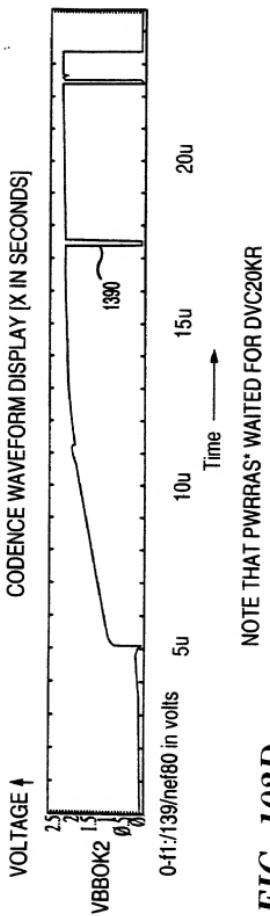


FIG. 102D

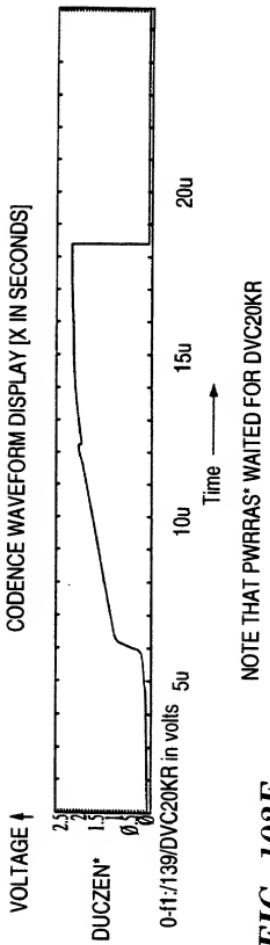
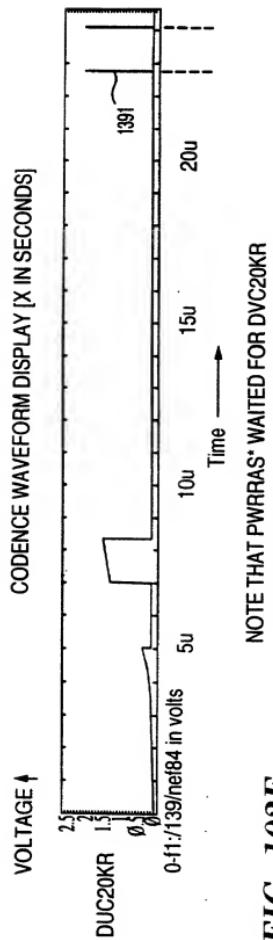
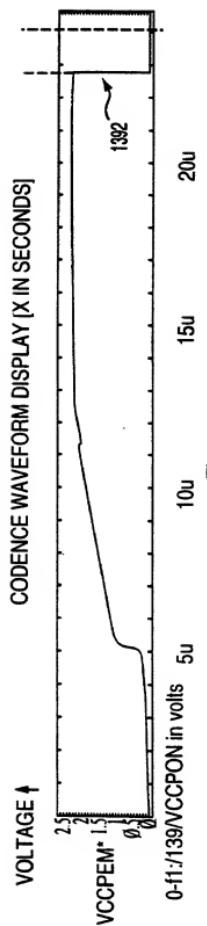


FIG. 102E

708250-686666

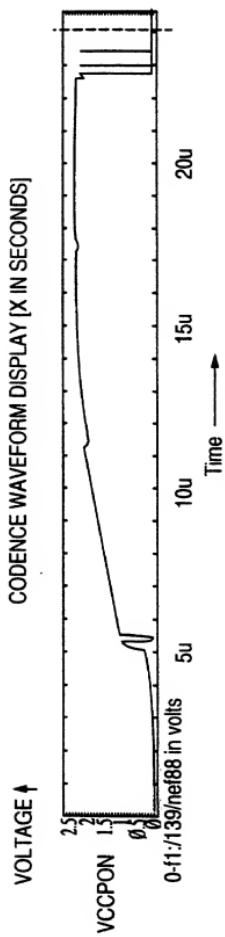
**FIG. 102F**

T022301-6666860



NOTE THAT PWRAS WAITED FOR DVCC20KA

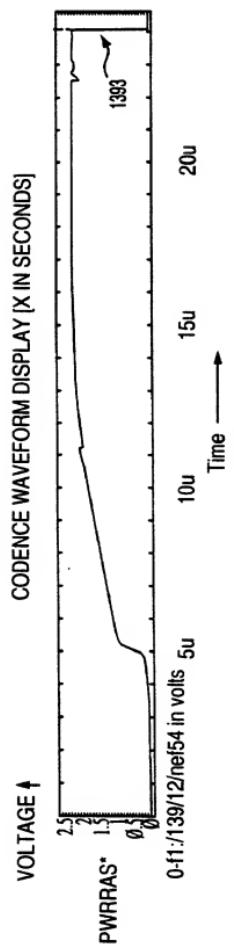
FIG. 102G



NOTE THAT PWRRA'S WAITED FOR DVC20KR

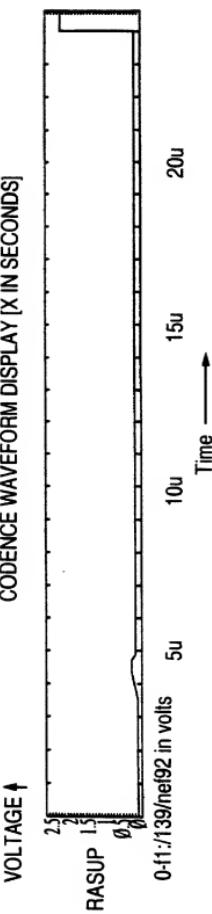
FIG. 102H

T08290 "66666660



NOTE THAT PWR/RAS* WAITED FOR DVC20KR

FIG. 102I



NOTE THAT PWRRAS WAITED FOR DVC20KR

FIG. 102J

106230" 686660

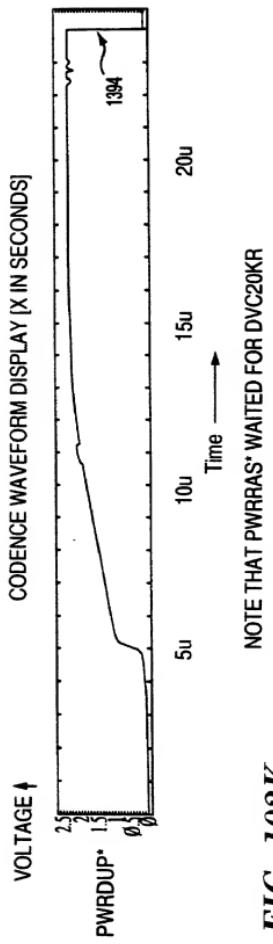
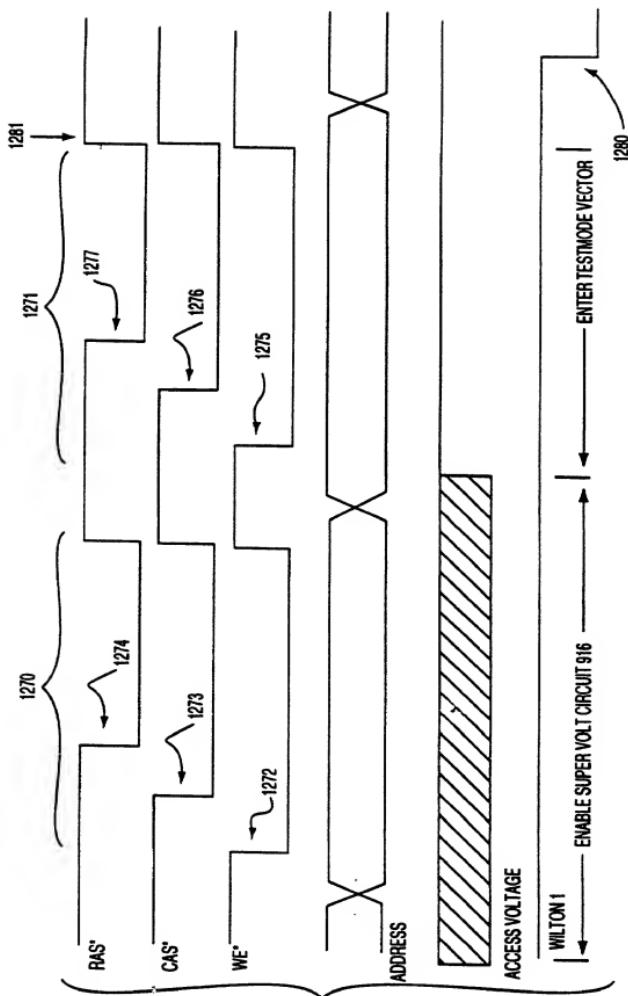
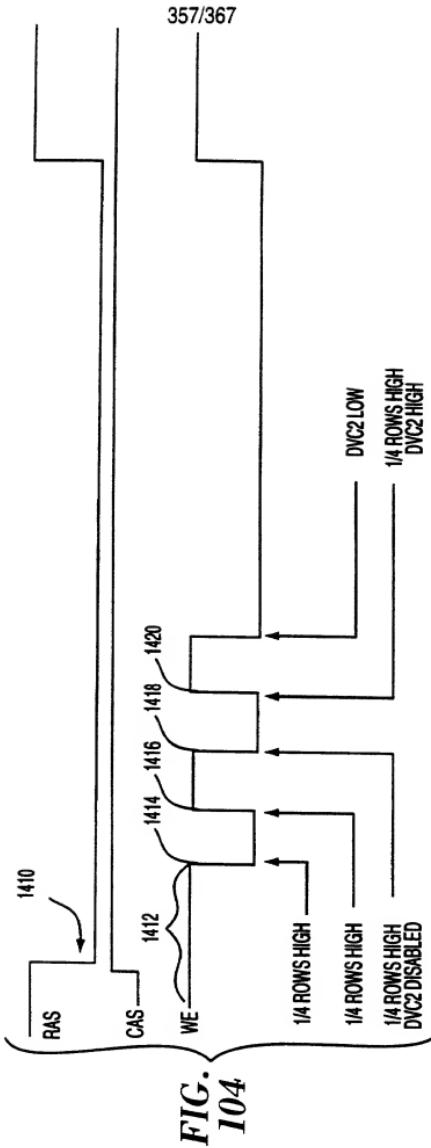


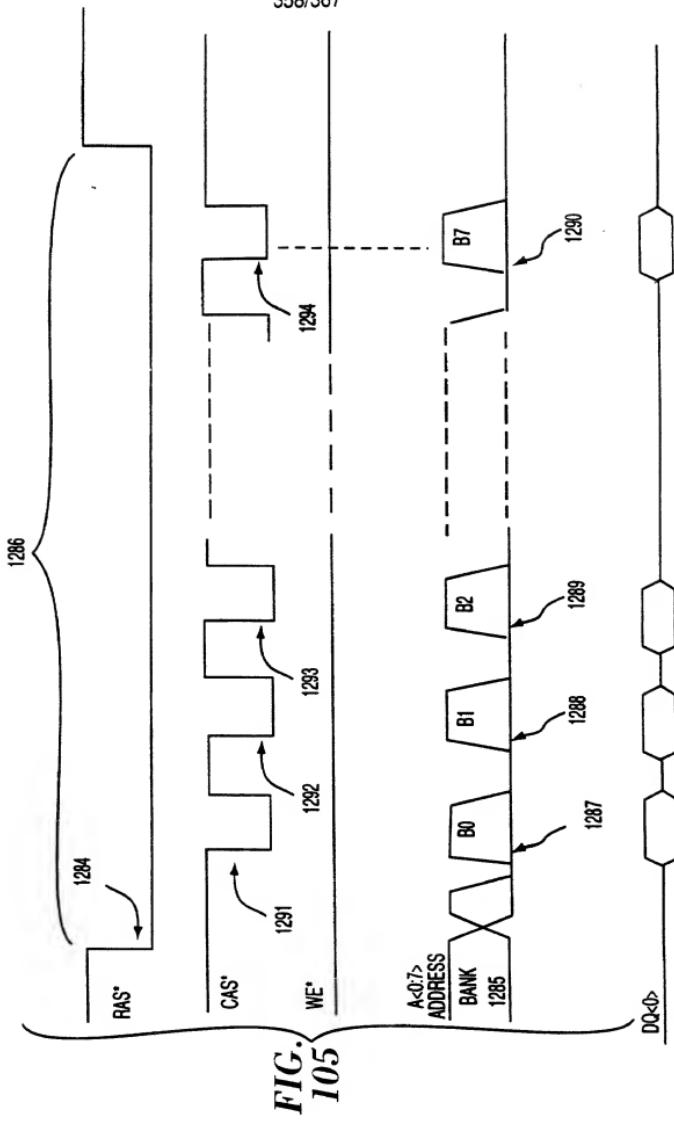
FIG. 102K

TOP SECRET//COMINT

FIG.
103



1088290 "358/367"



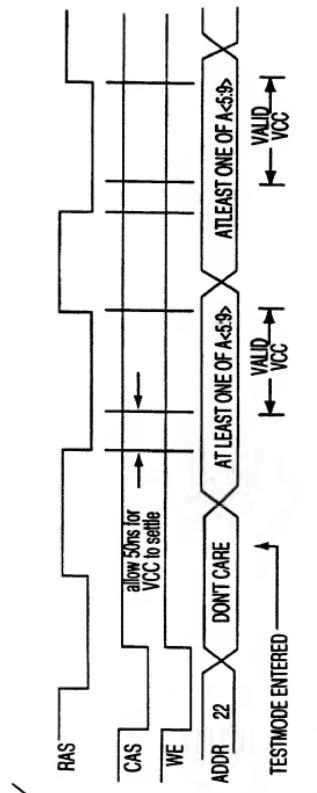


FIG. 106

T08230 * 6 SEC GRS

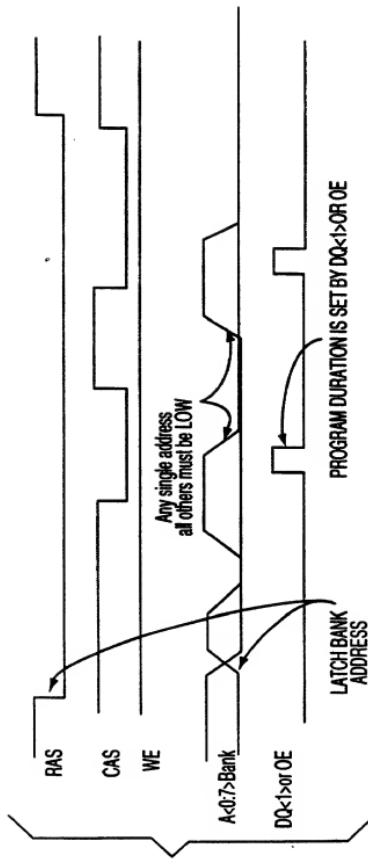


FIG. 107

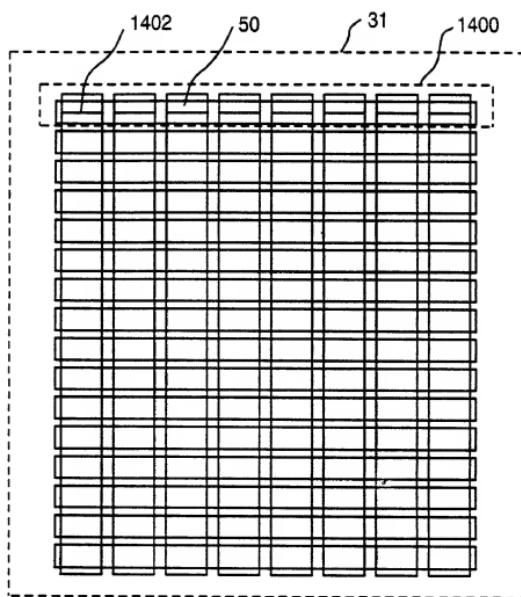
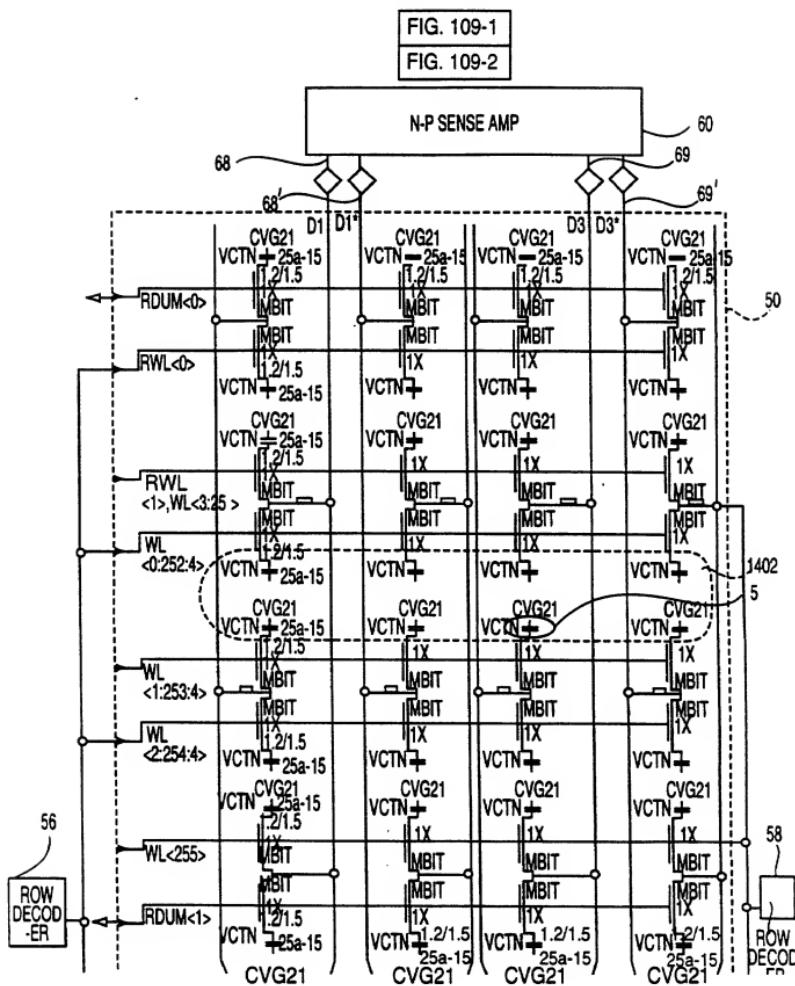


FIG. 108

FIG. 109-1



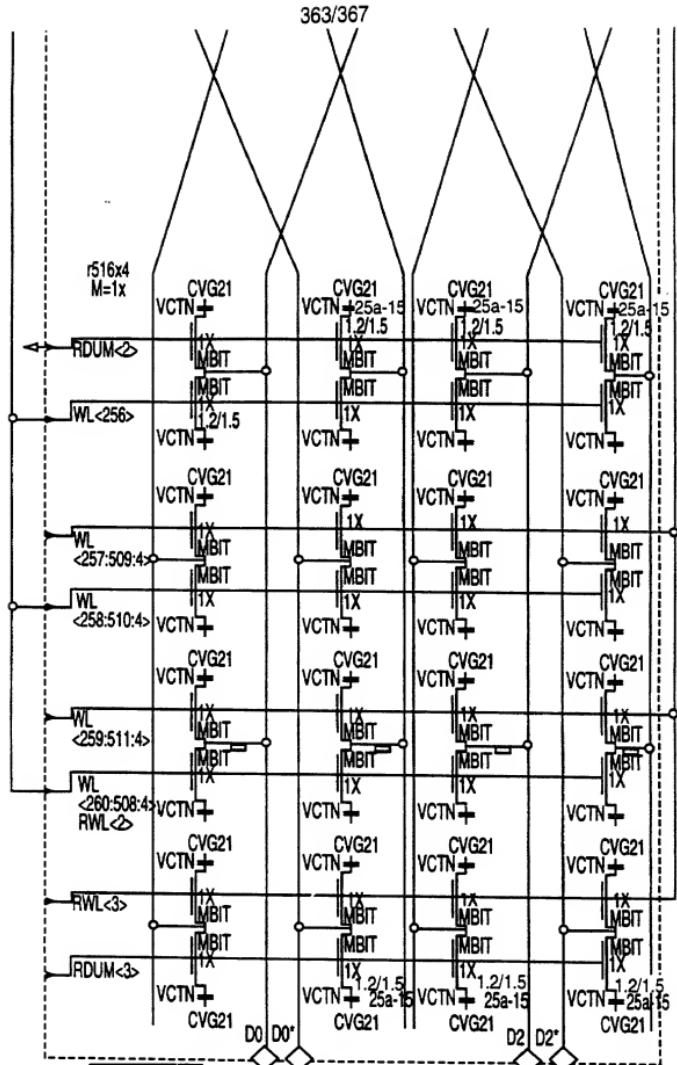


FIG 109-1

FIG. 109-2

N-P SENSE AMP

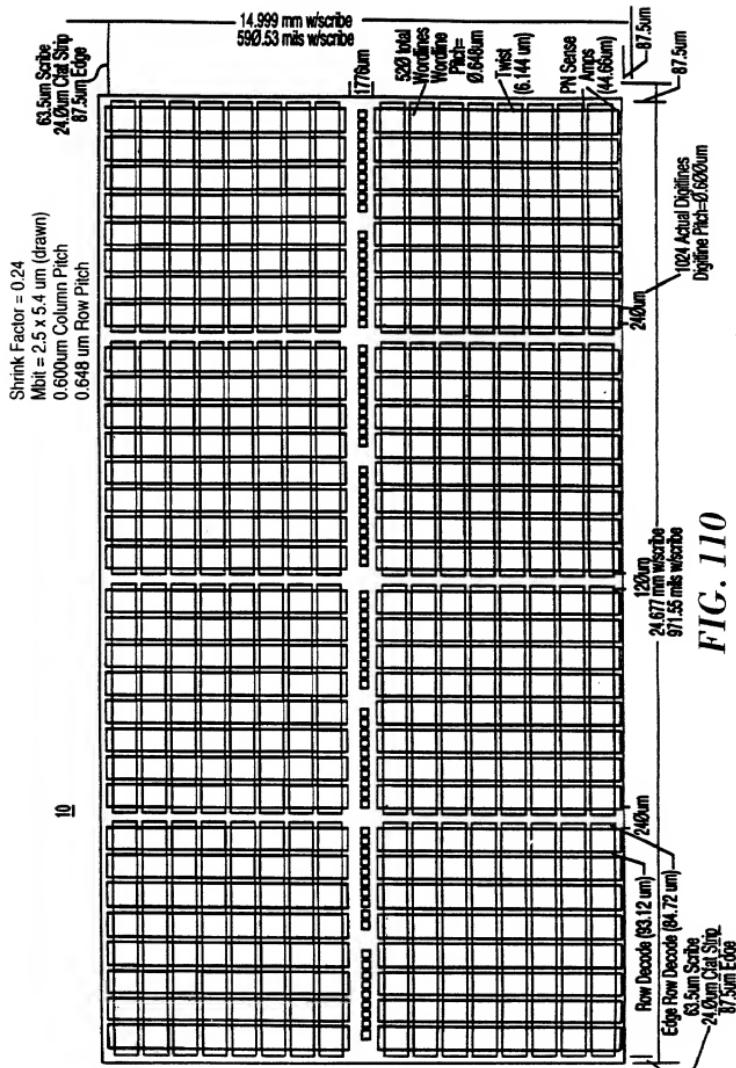


FIG. 110

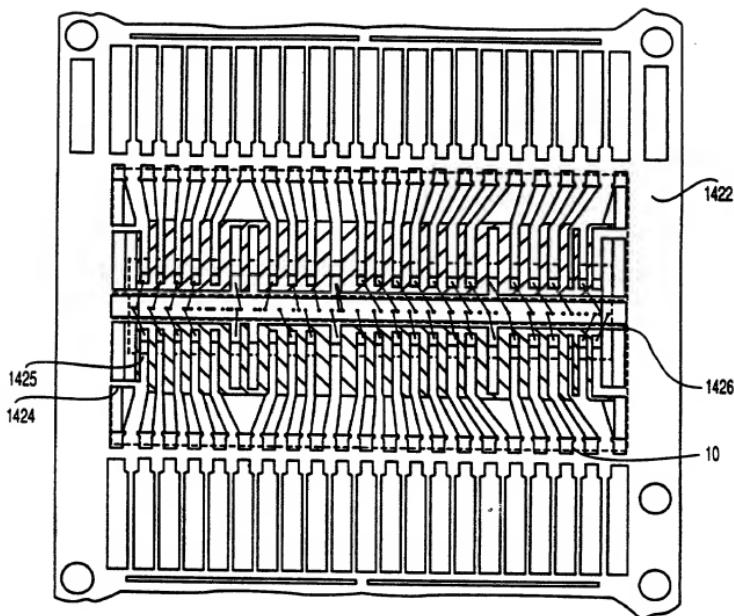


FIG. 111

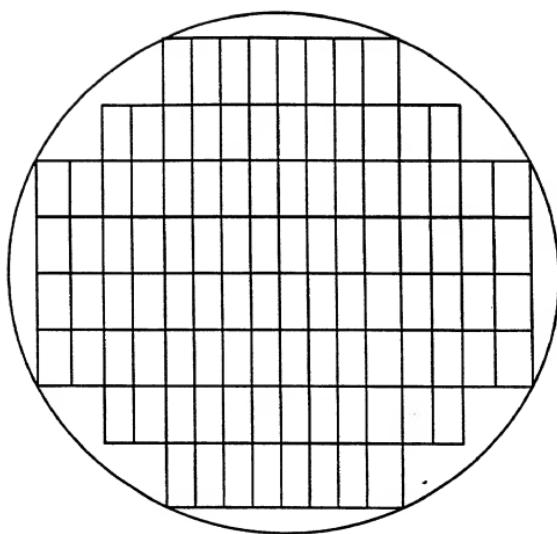


FIG. 112

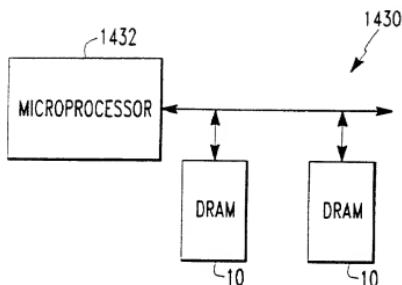


FIG. 113

01082300 "38660" 04